

# Thermal Simulation: LM98725 in 56L TSSOP, on 2 & 4L JEDEC Board

11 Nov 2011

# Objective and Package Specifications

## Objective:

To evaluate thermal performance of 2 and 4 layers JEDEC boards applied for 56L,TSSOP Package.

<b>Package Specifications</b>	<b>56L, TSSOP</b>	
Package Size (mm)	14.0 x 6.1 x 0.9	
Die Size (mm)		
DAP Size (mm)		
Input Power (Watt)	0.755W on die	
<b>Board Specifications</b>		
Board Type	2L, JEDEC	4L, JEDEC
Board Size (In)	3x 4.5	3x 4.5
Stack-up	2-Layer	4-Layer
Cu Thickness (Power/Ground Layers)	N/A	1 oz
Signal Traces	2 oz	2 oz
via under exposed pad	NO	NO
<b>Environmental Conditions</b>	<b>Natural Convection</b>	
Ambient Temperature	22°C	
Air Flow	0 m/s	

# Material Properties & Assumptions

Components	Material	Thermal Conductivity (W/m-K)
Die	Silicon	111
Mold Compound	EME-7351LS	0.837
Epoxy	84-1LMISR4	1.1
PCB Planes Traces	Copper	377
Board Insulator Layer	FR4	0.35
Lead and Pad Attach	Lead Free Solder	50.0
Lead frame	C194	167

## Assumptions:

- *Theta JA reference to Die (Worst case scenario).*
- *Theta JA calculation base on total power input towards die.*

# Summary of FEM Results

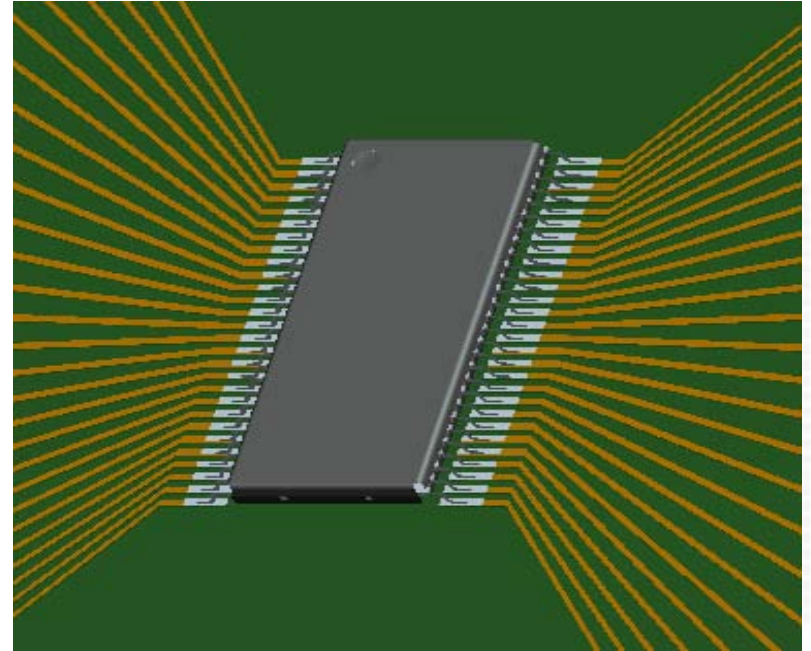
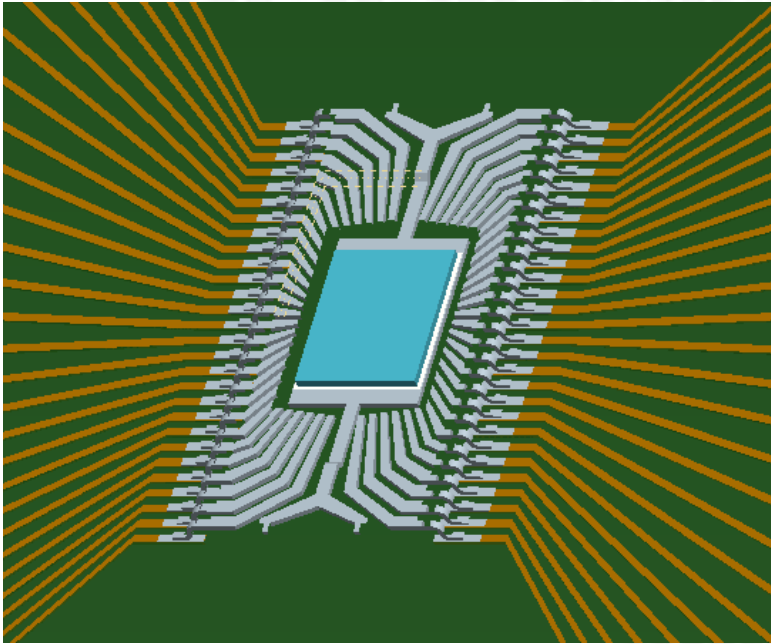
Board Layer	2 layers	4 layers
Total Power Input (W)	0.755	
Ambient Temperature, TA (°C)	22.0	
Max Case Temp, Tj at 22 °C ambient (°C)	84.64	70.10
Max Die Top, Tj at 22 °C ambient (°C)	84.90	70.29
Theta JA at 22 °C ambient (°C/W)	83.31	63.96

## Conclusion

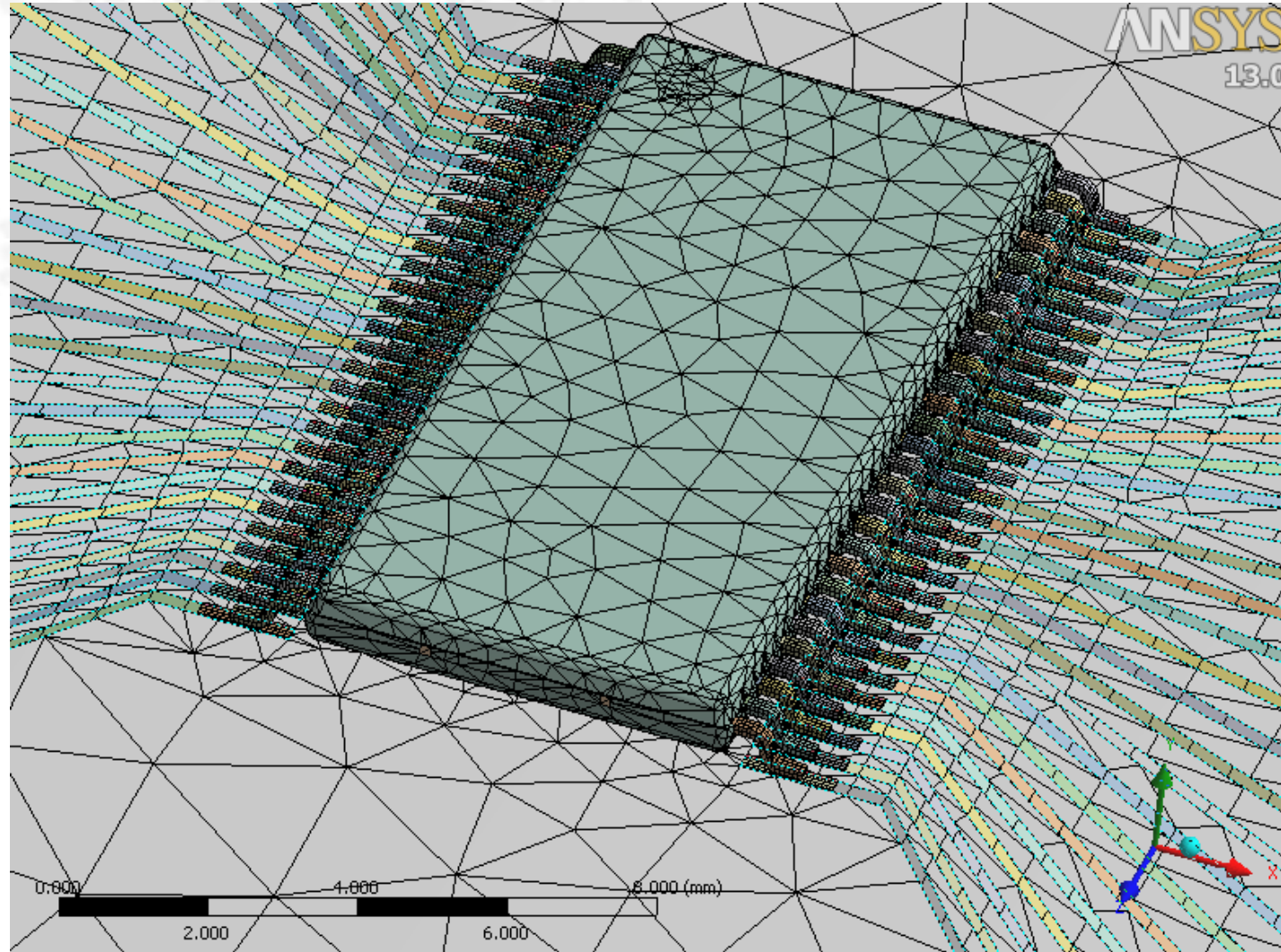
- Theta JA Improvement of 23.2% with 4 layers JEDEC Boards
  - Theta JA with 2 lyrs JEDEC Board: 83.31 (°C/W)
  - Theta JA with 4 lyrs JEDEC Board: 63.96 (°C/W)
  
- Junction temp, Tj Improvement of 17.2% with 4 layers JEDEC Boards
  - Tj with 2 lyrs JEDEC Board: 84.90 °C
  - Tj with 4 lyrs JEDEC Board: 70.29 °C

# Appendix

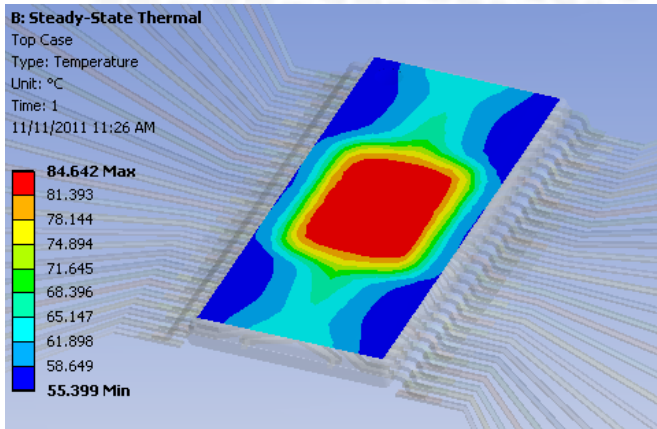
# Package Compilation



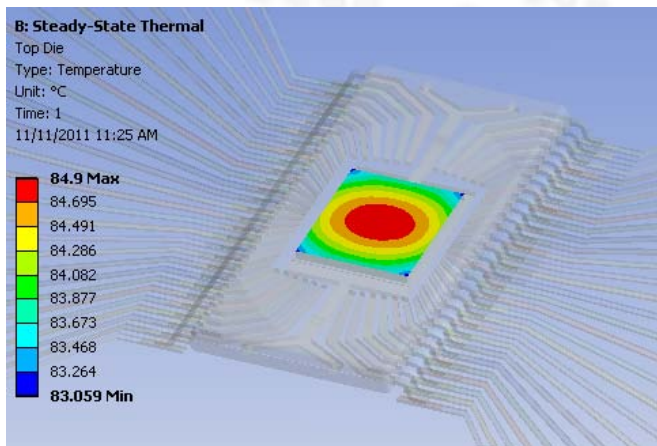
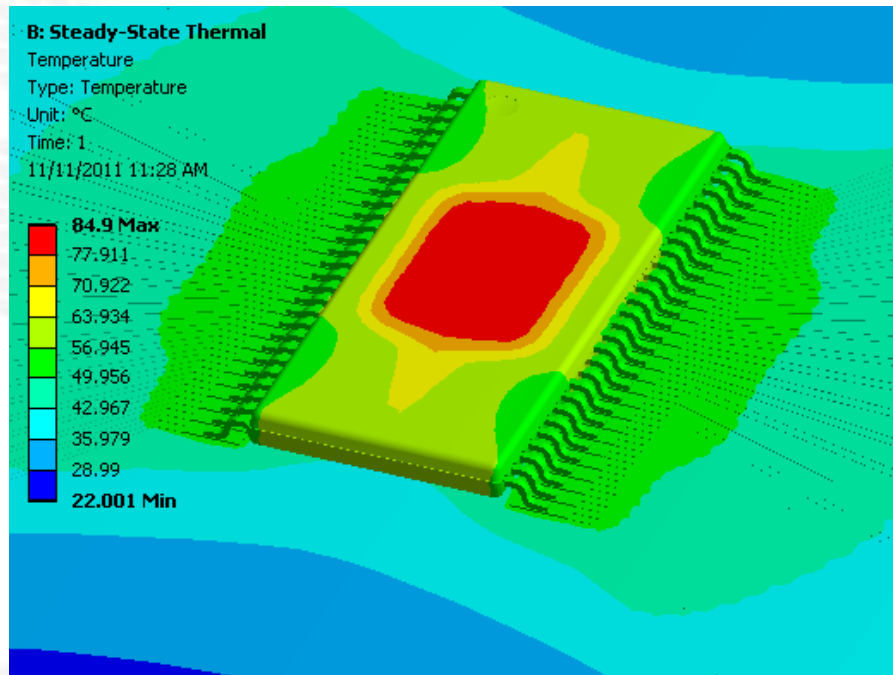
# Finite Element Model



# 2 layers JEDEC Board



**Max. Case Temp. = 84.64 °C**



**Max. Device Temp. = 84.90 °C**

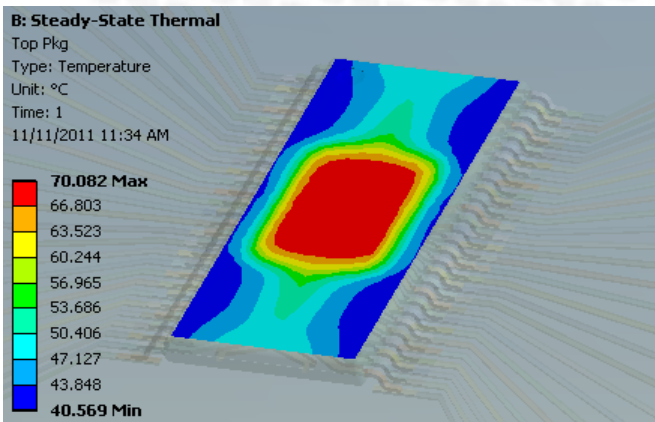
$$T_{Junction} = 84.90 \text{ } ^\circ\text{C}$$

$$\theta_{JA} = (84.90 - 22) / 0.755 \text{ W}$$

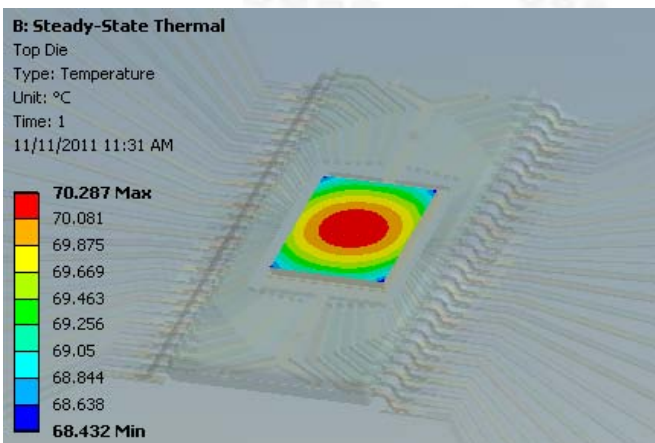
$$= \underline{\underline{83.31 \text{ } ^\circ\text{C/W}}}$$



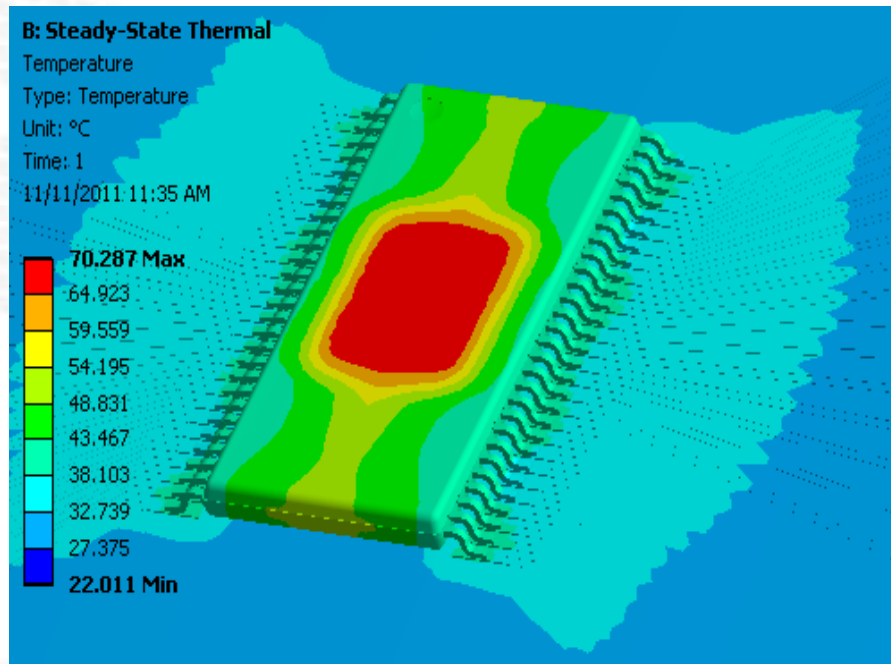
# 4 layers JEDEC Board



**Max. Case Temp. = 70.10 °C**



**Max. Device Temp. = 70.29 °C**



$$T_{\text{Junction}} = 70.29 \text{ }^{\circ}\text{C}$$

$$\theta_{\text{JA}} = (70.29 - 22) / 0.755 \text{ W}$$

$$= \underline{\underline{63.96 \text{ }^{\circ}\text{C/W}}}$$

Thank You