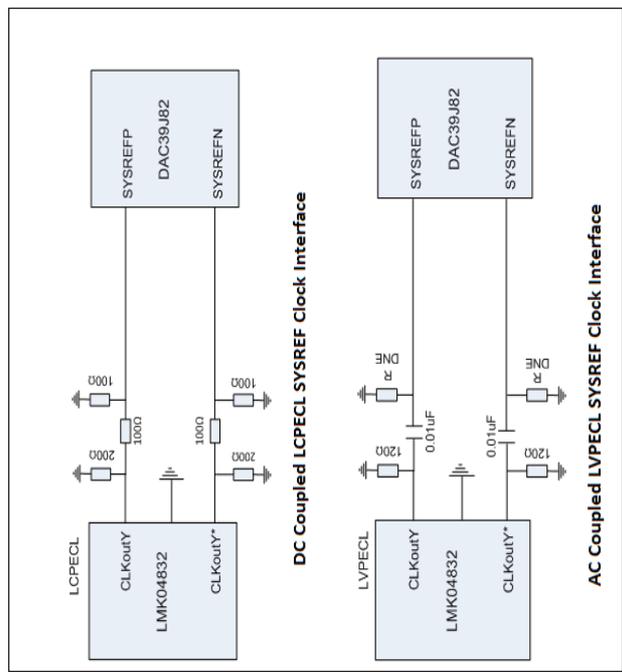
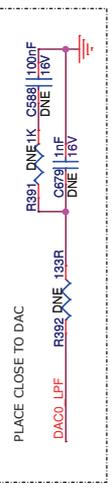
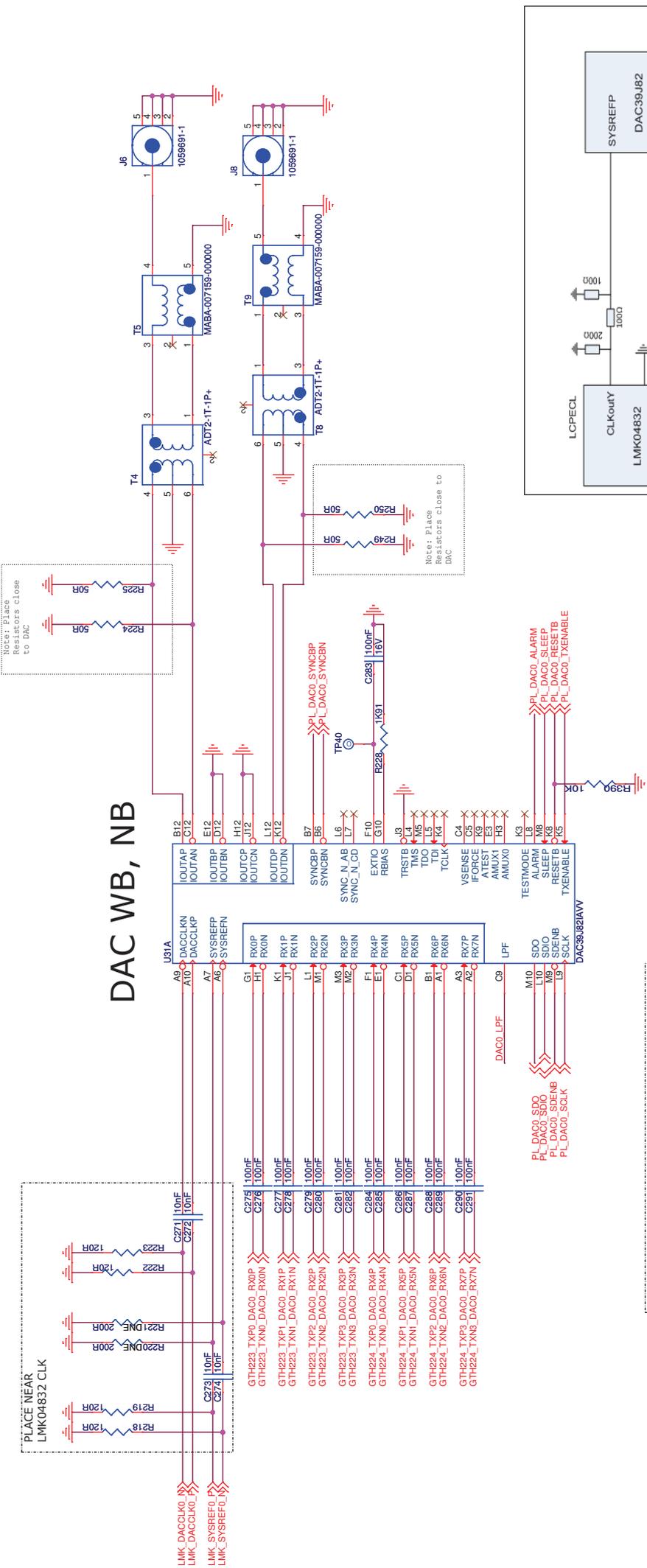


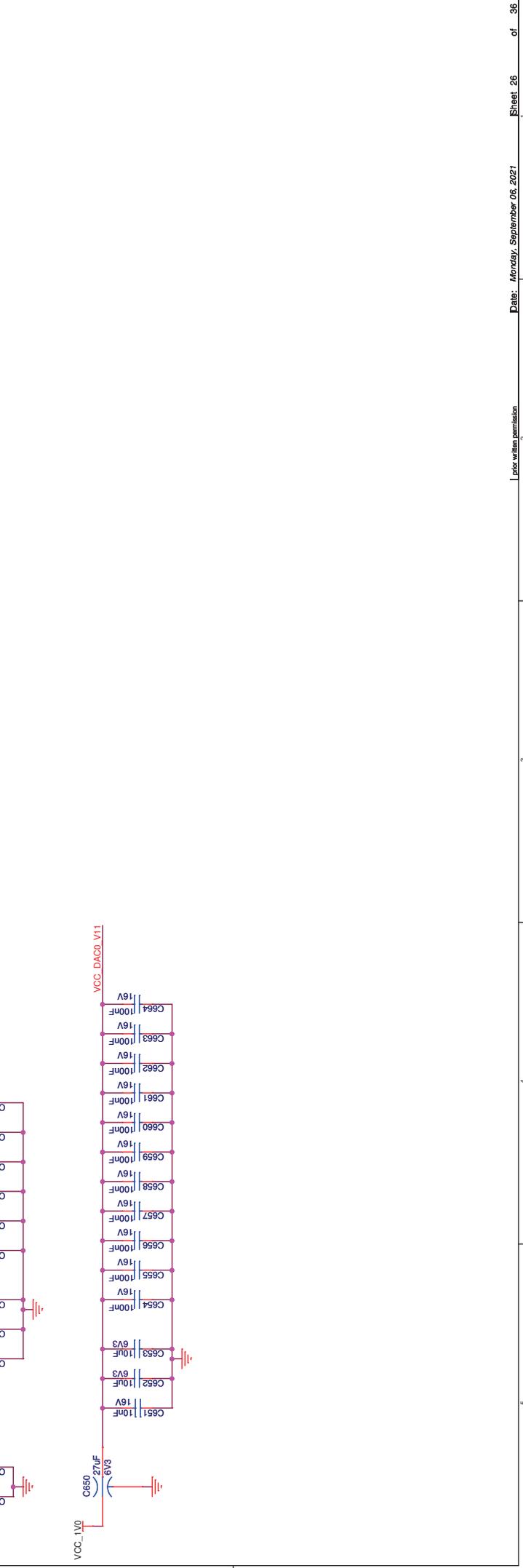
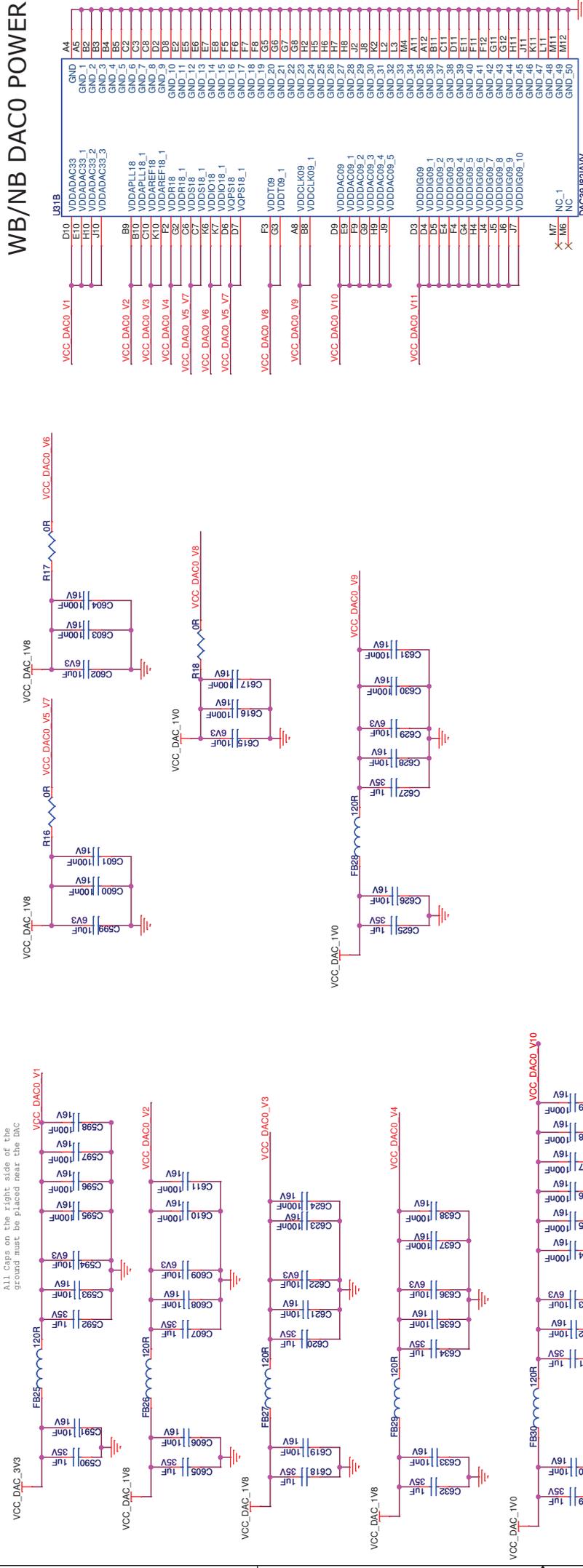


# DAC WB, NB

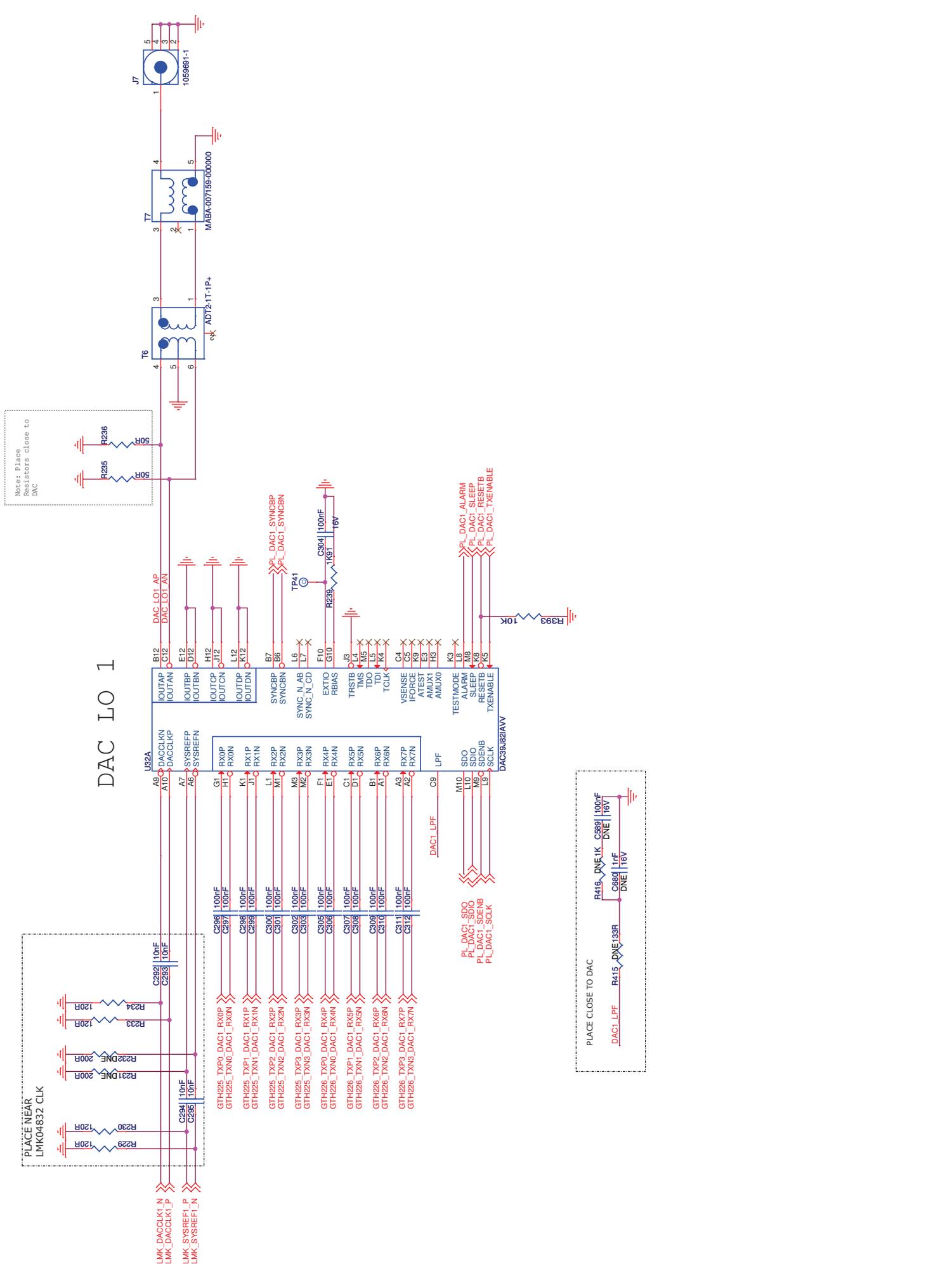


# WB/NB DAC0 POWER

All Caps on the right side of the ground must be placed near the DAC



# DAC LO 1



PLACE NEAR LMK04832 CLK

PLACE CLOSE TO DAC

Note: Place Resistors close to DAC

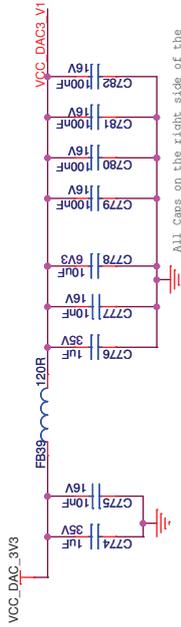
C289	100nF	GTH225_TXP0_DAC1_RX0P
C297	100nF	GTH225_TXN0_DAC1_RX0N
C298	100nF	GTH225_TXP1_RX1P
C299	100nF	GTH225_TXN1_DAC1_RX1N
C300	100nF	GTH225_TXP2_DAC1_RX2P
C301	100nF	GTH225_TXN2_DAC1_RX2N
C302	100nF	GTH225_TXP3_RX3P
C303	100nF	GTH225_TXN3_DAC1_RX3N
C305	100nF	GTH226_TXP0_DAC1_RX4P
C306	100nF	GTH226_TXN0_DAC1_RX4N
C307	100nF	GTH226_TXP1_DAC1_RX5P
C308	100nF	GTH226_TXN1_DAC1_RX5N
C309	100nF	GTH226_TXP2_DAC1_RX6P
C310	100nF	GTH226_TXN2_DAC1_RX6N
C311	100nF	GTH226_TXP3_DAC1_RX7P
C312	100nF	GTH226_TXN3_DAC1_RX7N

DAC1\_LPF

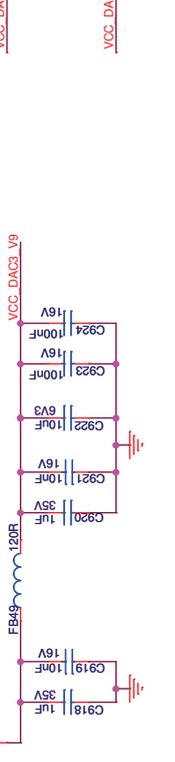
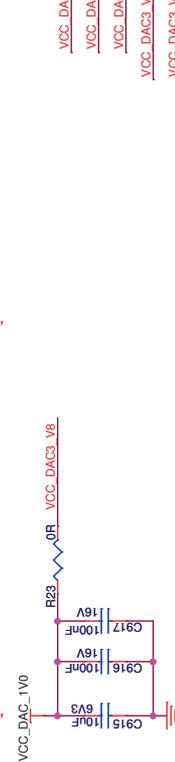
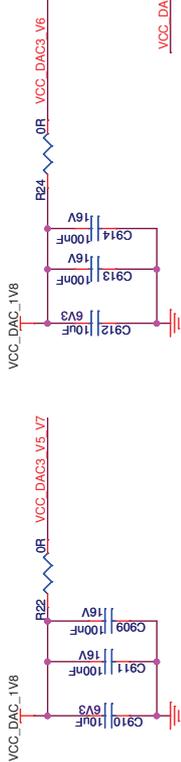
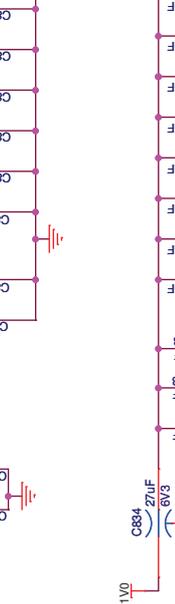
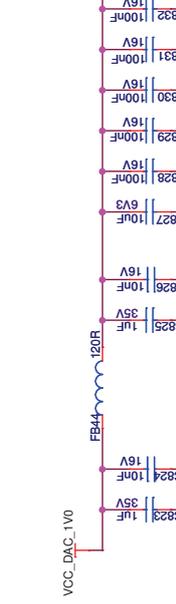
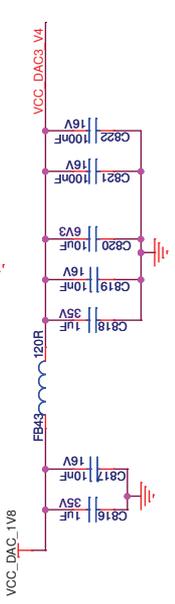
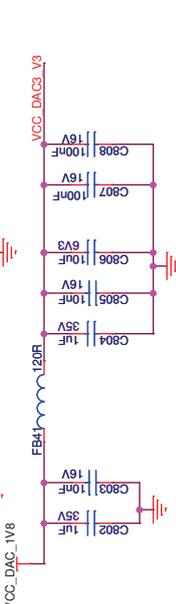
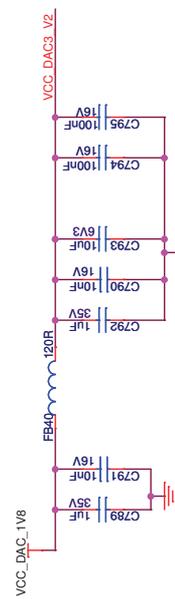
M10	PL_DAC1_SDO
L10	PL_DAC1_SDI0
D10	PL_DAC1_SDENB
L8	PL_DAC1_SCLK
K3	TESTMODE
L8	ALARM
M8	SLEEP
L8	RESETB
K5	TXENABLE
K3	PL_DAC1_ALARM
K3	PL_DAC1_SLEEP
K3	PL_DAC1_RESETB
K3	PL_DAC1_TXENABLE







All Caps on the right side of the ground must be placed near the DAC



# DAC3 POWER

