

Test Procedure of TSW30SH84 with TSW1400

TSW1400EVM

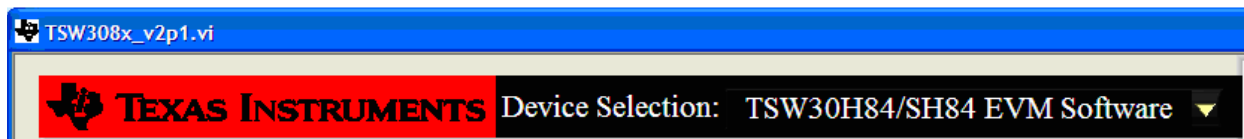
1. Connect 5 V power supply to J12, *5V_IN* jack of the TSW1400 EVM.
2. Connect PC's USB port to J5 USB port of the TSW1400 EVM. The cable should be a standard A to mini-B connector cable.

TSW30SH84EVM

1. Connect J13 connector of TSW30SH84 EVM to J4 connector of TSW1400 EVM.
2. Connect 6V to the J18, *Power In* jack of the TSW30SH84 EVM.
3. Connect PC's USB port to J14 USB port of the TSW30SH84 EVM. The cable should be a standard A to mini-B connector cable.
4. Provide 10 dBm maximum, 300 MHz to 4 GHz LO source to connectors J19 and/or J22. The J19 or the J22 connector routes the LO source to the respective TRF3705 modulator TX path. Optionally, the EVM can be configured to share the LO source between the two TX paths through an on-board 3 Db splitter. The source should be connected to J22 in this case. See the *TX Path Optional Configuration* section for details.
5. Connect the RF output port of J7 and/or J9 to the spectrum analyzer.

TSW30SH84 GUI Setup Procedure

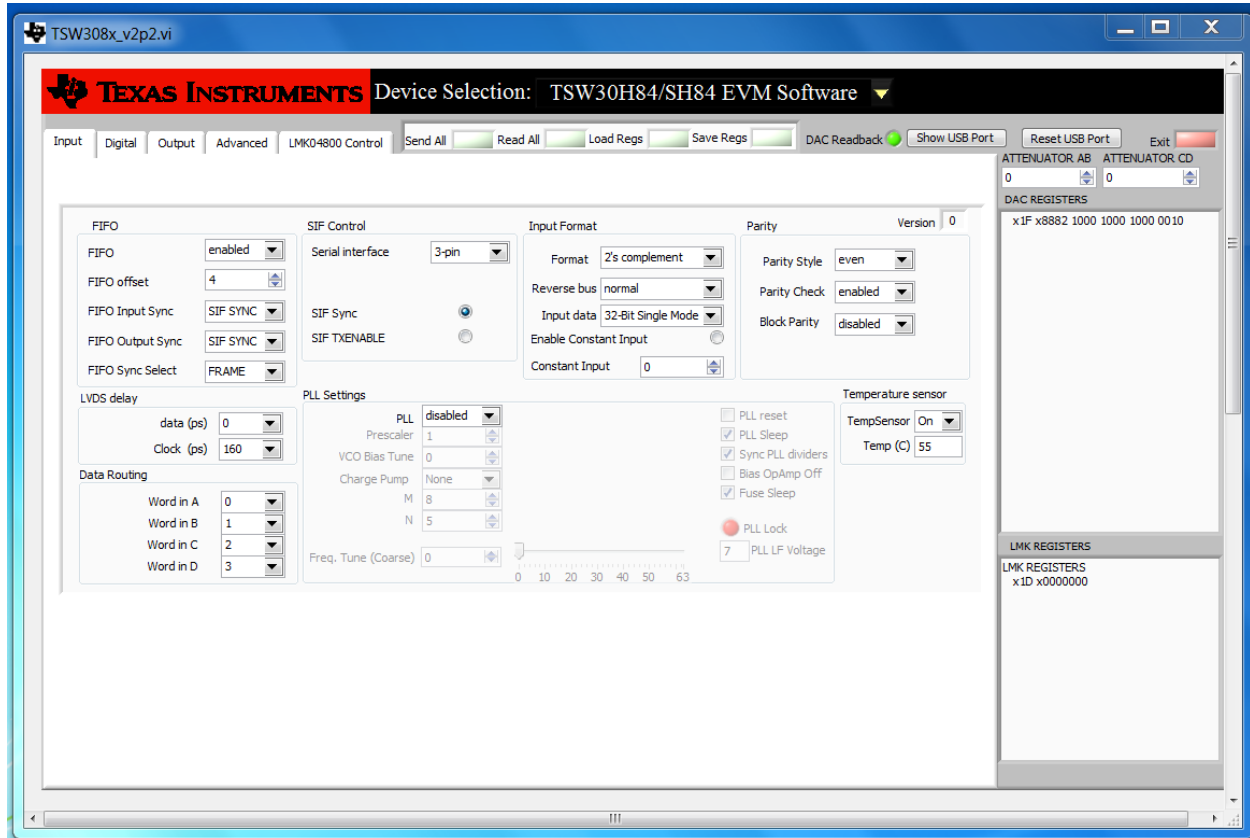
1. Turn on power to both boards, and press the reset button SW1 on the TSW30SH84EVM.
2. Start the TSW308x EVM GUI program. When the program starts, press the *RESET USB Port* button in the GUI, and verify USB communication.
3. Select the appropriate EVM platform on the software menu.



4. Click on LOAD REGS, browse to the installation folder, and load example files. The example files are located at C:\Program Files (x86)\Texas Instruments\TSW308x\Configuration Files\TSW30SH84_single_PLL_mode_internal_clock_correct_output. To configure the LMK04808B in single PLL mode, select the file in the LMK04808 PLL Mode 10MHz reference folder. To configure the LMK04808B in clock distribution mode, select the file in the LMK04808 Clock Distribution Mode folder. For the TSW30SH84, the files contain settings for 2x interpolation with the DAC34SH84 running at 1474.56 MSPS. The data rate for each DAC is at 737.28 MSPS. The NCO is enabled at 30 MHz.

5. Click on Send All to write all of the values to the devices. If the LMK04808B is programmed properly in single PLL mode, the LMK LOCK LED (D7) near the device will be illuminated. The updated register configuration for the LMK04808B now appears.

Input Setting



Digital Settings

TSW308x_v2p2.vi

TEXAS INSTRUMENTS Device Selection: TSW30H84/SH84 EVM Software

Input Digital Output Advanced LMK04800 Control Send All Read All Load Regs Save Regs DAC Readback Show USB Port Reset USB Port Exit

Digital Filters

Interpolation: 2x

Digital Mixer

☒ Enable Mixer

Mixer: Bypass

Inverse sinx/x filter

☐ Compensate AB

☐ Compensate CD

☐ Clock Receiver Sleep

☒ Clock Divider Sync

Clock Div Sync source: FRAME

Group Delay A: 0

Group Delay B: 0

Group Delay C: 0

Group Delay D: 0

DAC Gain: 10

SIF Sync: ☒

Offset Adjustment

OffsetAB adjust: disabled

Offset A: 0

Offset B: 0

OffsetAB Sync: REGWR

OffsetCD adjust: disabled

Offset C: 0

Offset D: 0

OffsetCD Sync: REGWR

QMC

QMC Correct AB: enabled

QMC GainA: 1446

QMC GainB: 1446

QMC PhaseAB: 0

CorrectAB Sync: REGWR

☐ Sync AB

QMC Correct CD: enabled

QMC GainC: 1446

QMC GainD: 1446

QMC PhaseCD: 0

CorrectCD Sync: REGWR

☐ Sync CD

NCO

☒ Enable NCO Update freq:

F sample [MHz]: 1474.5600

NCO Freq _AB [MHz]: 30.0000

NCO Freq _CD [MHz]: 30.0000

Gain: 0 dB

NCO Acc Sync: SIF SYNC

NCO DDS AB: 87381333

Phase Offset AB: 0

MixAB Sync: SIF SYNC

NCO DDS CD: 87381333

Phase Offset CD: 0

MixCD Sync: SIF SYNC

ATTENUATOR AB: 0

ATTENUATOR CD: 0

DAC REGISTERS

x1F x8882 1000 1000 1000 0010

LMK REGISTERS

x1D x0000000

Output Settings

TSW308x_v2p2.vi

TEXAS INSTRUMENTS Device Selection: TSW30H84/SH84 EVM Software

Input Digital Output Advanced LMK04800 Control Send All Read All Load Regs Save Regs DAC Readback Show USB Port Reset USB Port Exit

Output Options

Reference: internal

Output AB Delay: 0

Output CD Delay: 0

Complement A: disabled

Complement B: disabled

Complement C: disabled

Complement D: disabled

DAC Gain

☐ DACA Sleep

☐ DACB Sleep

☐ DACC Sleep

☐ DACD Sleep

☐ Rev Interface

DAC Gain: 10

DAC: QDAC

DACA Decoder: Thermometer

Output Shutoff

☒ DACCLK Gone

☒ DATACLK Gone

☒ FIFO Collision

☐ Clock monitor sync

Data Routing

Word out A: 0

Word out B: 1

Word out C: 2

Word out D: 3

ATTENUATOR AB: 0

ATTENUATOR CD: 0

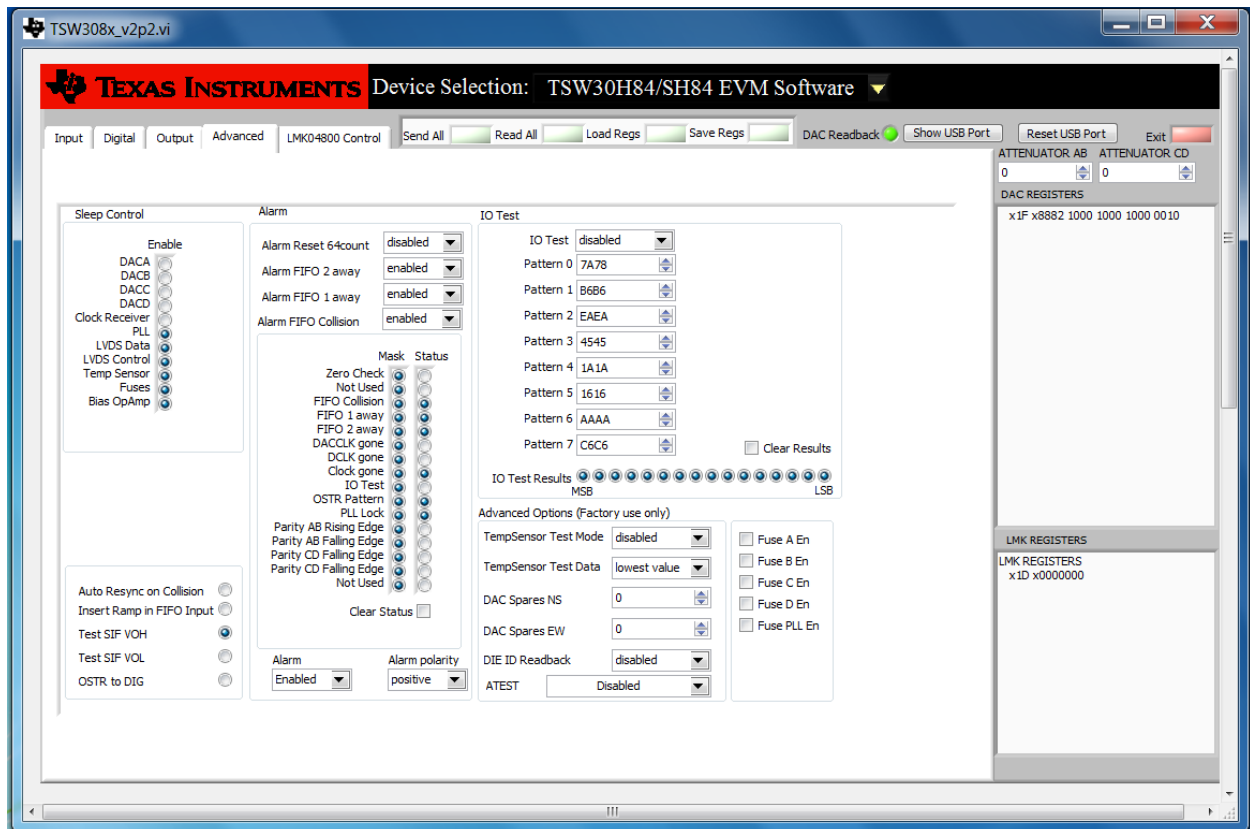
DAC REGISTERS

x1F x8882 1000 1000 1000 0010

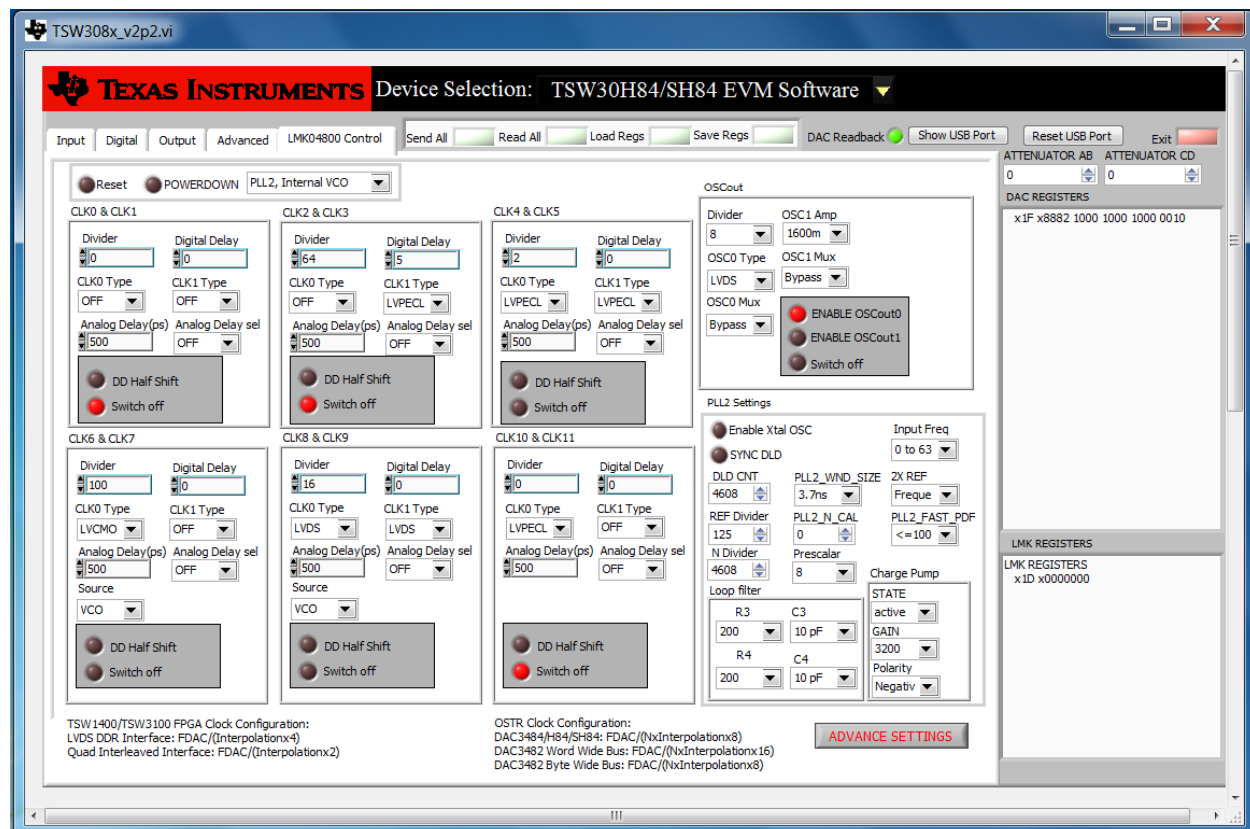
LMK REGISTERS

x1D x0000000

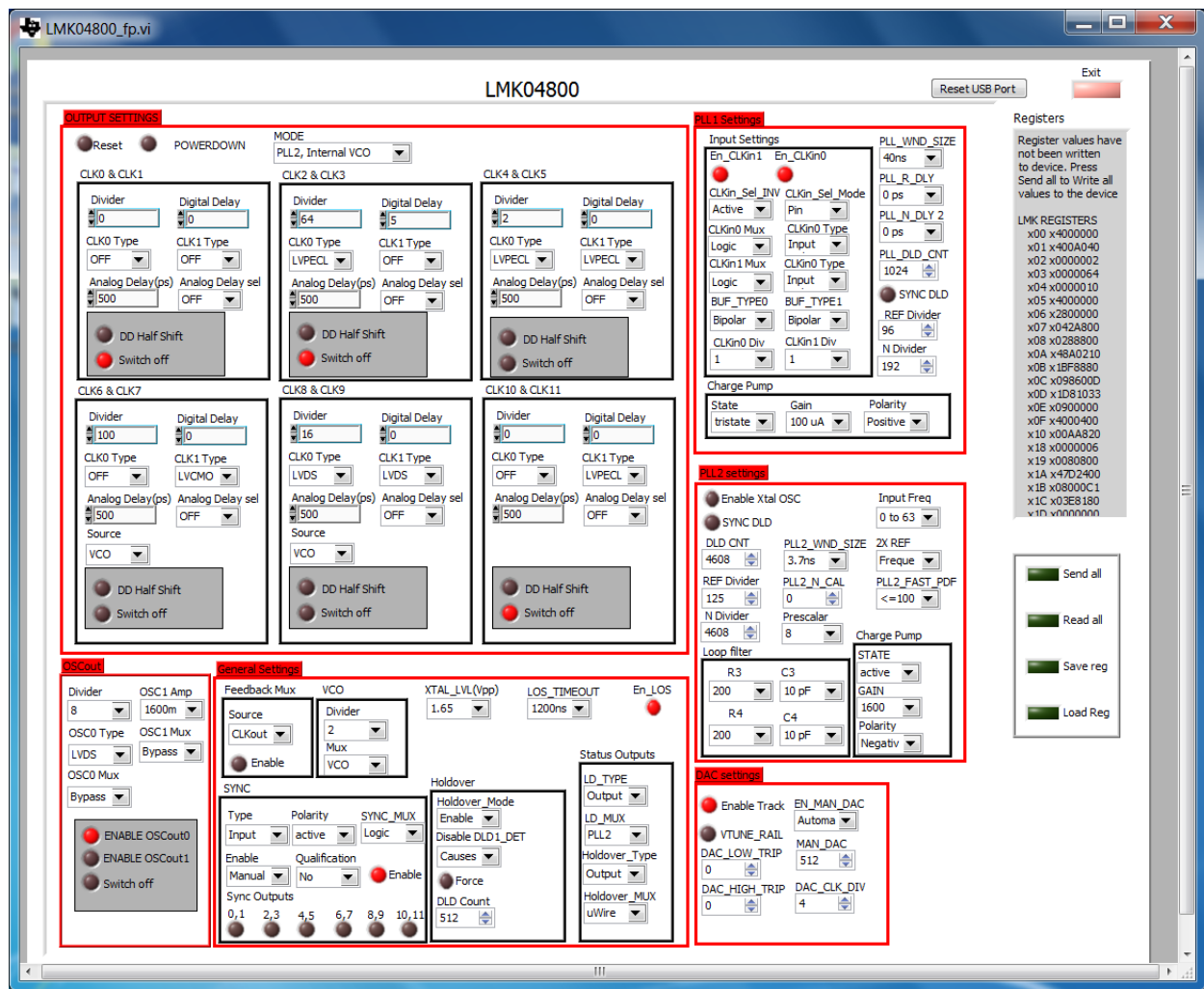
Advanced Settings



LMK04800 Control Settings

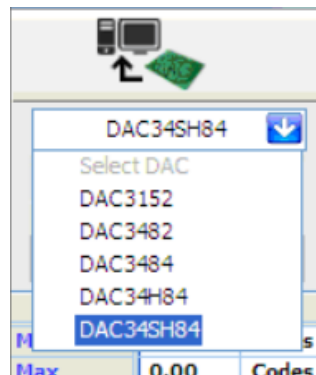


LMK04800 advance Settings



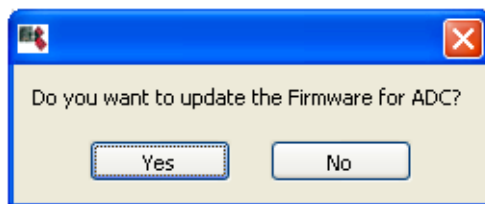
TSW1400 GUI Setup Procedure

1. Start the High Speed Converter Pro GUI program. When the program starts, select the DAC tab and then select DAC34SH84 device in the "Select DAC" menu



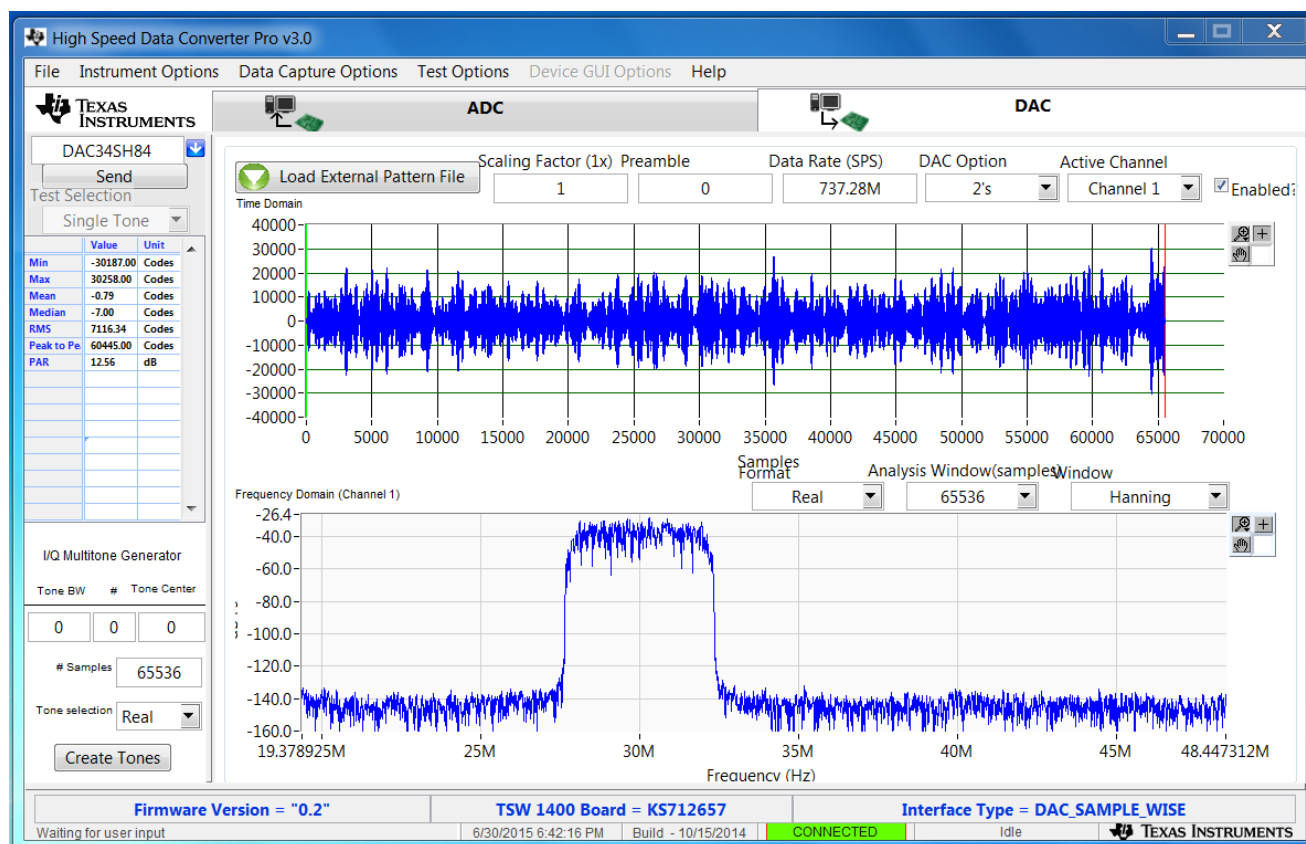
Select DAC34SH84 in the High Speed Converter Pro GUI Program-

2. When prompted Load DAC Firmware?, select YES.



Load DAC Firmware Prompt

3. Click on the button labeled “Load External Pattern File”, located near the top left of the GUI



Load File to Transfer into TSW1400

4. Select the file "WCDMA_TM1_complexIF30MHz_Fdata737.28MHz_1000.tsw" under C:\Program

Files\Texas Instruments\High Speed Data Converter Pro\Testfiles

5. Enter 737.28M for the "Data Rate" and 2's complement for the "DAC Option".

6. Select Hanning for "Window".

7. In the “DAC Selection” panel on the left side of the GUI, click on “Send” to load the data into memory.

8. Toggle the SIF SYNC button of the TSW308x EVM GUI to synchronize the appropriate digital blocks, if the example file with NCO setting is used.
9. Verify the spectrum using the spectrum analyzer at the two RF outputs, J7 and J9, of the TSW30SH84.
10. With 1780 MHz of LO, the expect results are shown as

