



ADC32RF80\_LMF\_484

Capture

Test Selection

Single Tone

	Value	Unit
SNR	0	dBFS
SFDR	0	dBFS
THD	0	dBFS
SINAD	0	dBFS
ENOB	0	Bits
Fund.	0	dBFS
Next Spur	0	dBFS
HD2	0	dBFS
HD3	0	dBFS
HD4	0	dBFS
HD5	0	dBFS
NSD/Hz	-Inf	dBFS/Hz
Phase	0	Rad
	dBFS	Hz
M1	0	0.00E+0
M2	0	1.00E+6

**Test Parameters**

Auto Calculation of Coherent Frequencies

Analysis Window (samples)

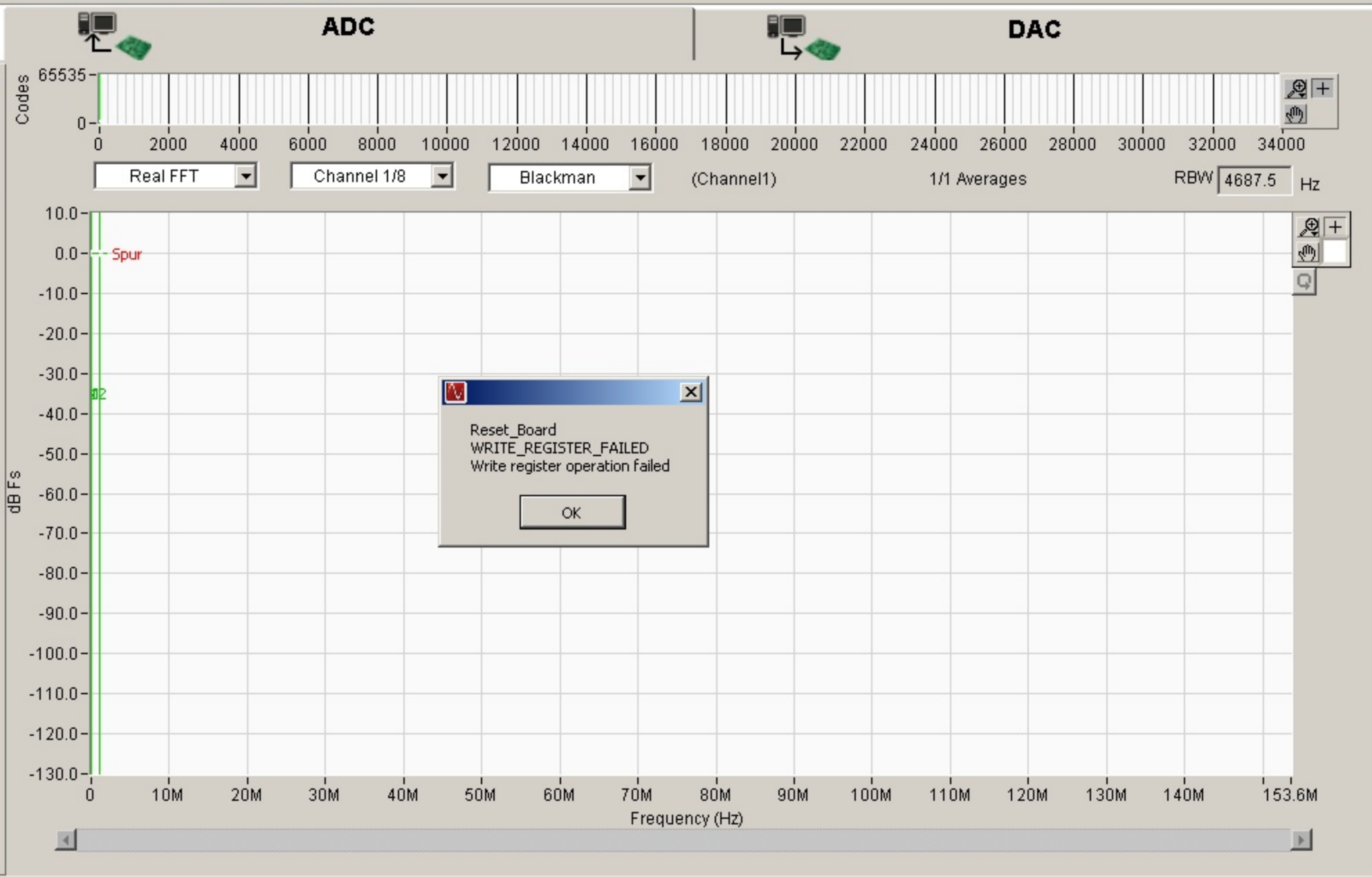
65536

ADC Output Data Rate

307.2M

ADC Input Target Frequency

0.000000000



Reset\_Board  
WRITE\_REGISTER\_FAILED  
Write register operation failed

OK

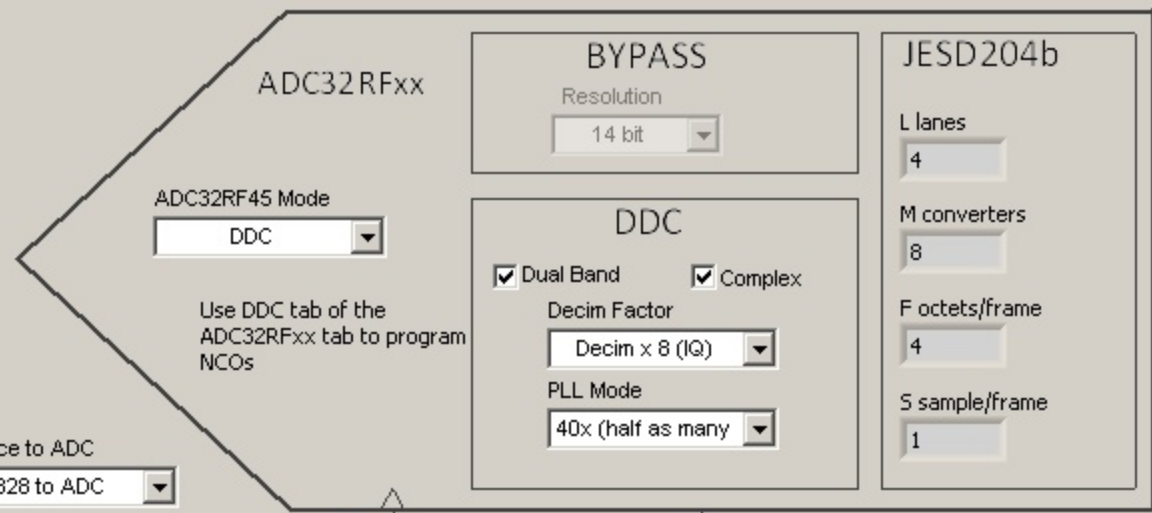
# ADC32RFxx EVM GUI

Select the device

- Block Diagram
- Quick Setup
- ADC32RFxx
- LMK04828
- LMX2582
- Low Level View

USB Status ● Reconnect?

Nyquist Zone



Clock Source to ADC



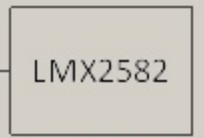
Not used

10M Sync (optional)

Internal Clk Freq



Jumper JP3 'INT'



Install C431/432

SYSREF

HSDCPro Device Selection

**PROGRAM EVM**

EVM Programmed

CLK and SYSREF to FMC