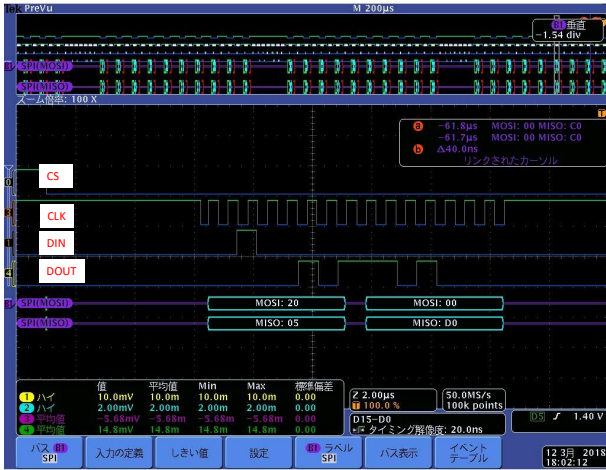
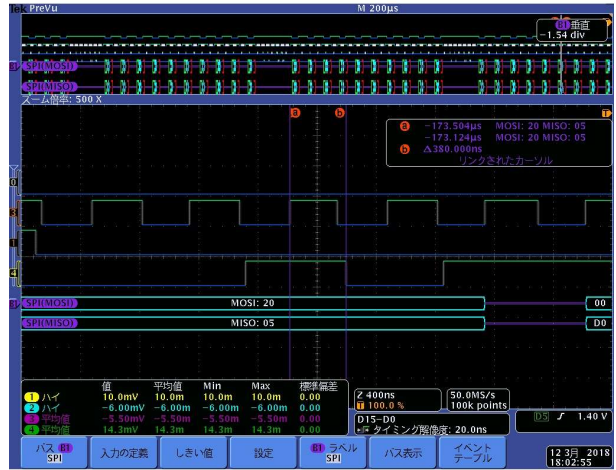


□ Normal Waveform □



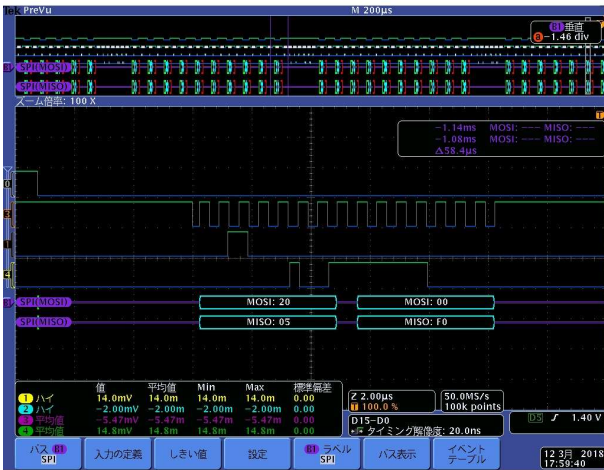
MSO4034 - 16:39:23 2018/03/12

□ Normal Waveform(Extended image) □



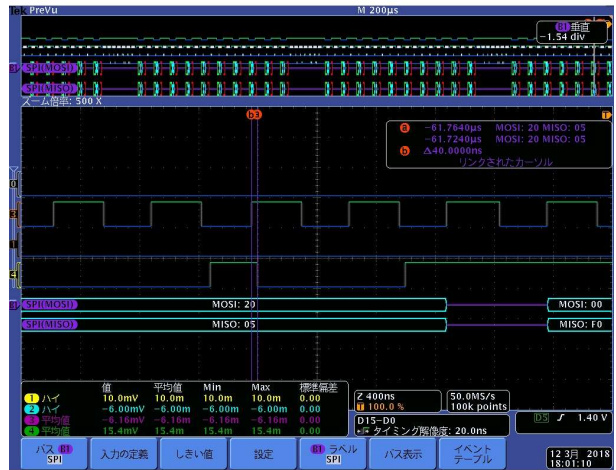
MSO4034 - 16:40:05 2018/03/12

■ Bad Waveform ■



MSO4034 - 16:36:50 2018/03/12

■ Bad Waveform(Extended image) ■



MSO4034 - 16:38:20 2018/03/12

Information:  
The DOUT waveform is observed at the FPGA output after the AD converter,  
so DOUT only includes delay of 10 ns.