

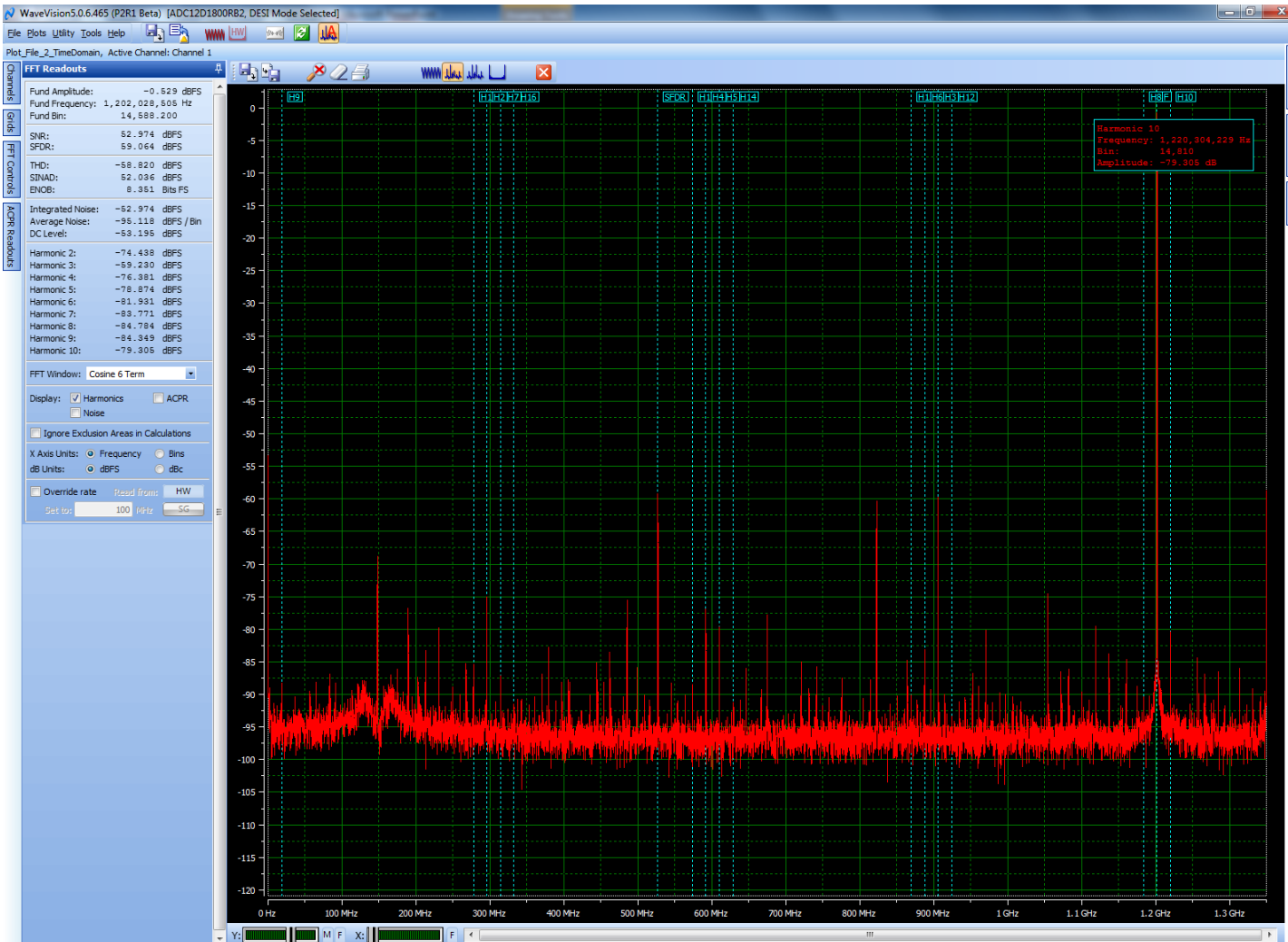
ADC12D1800 DESI 2.7GSPS

December 14 2018

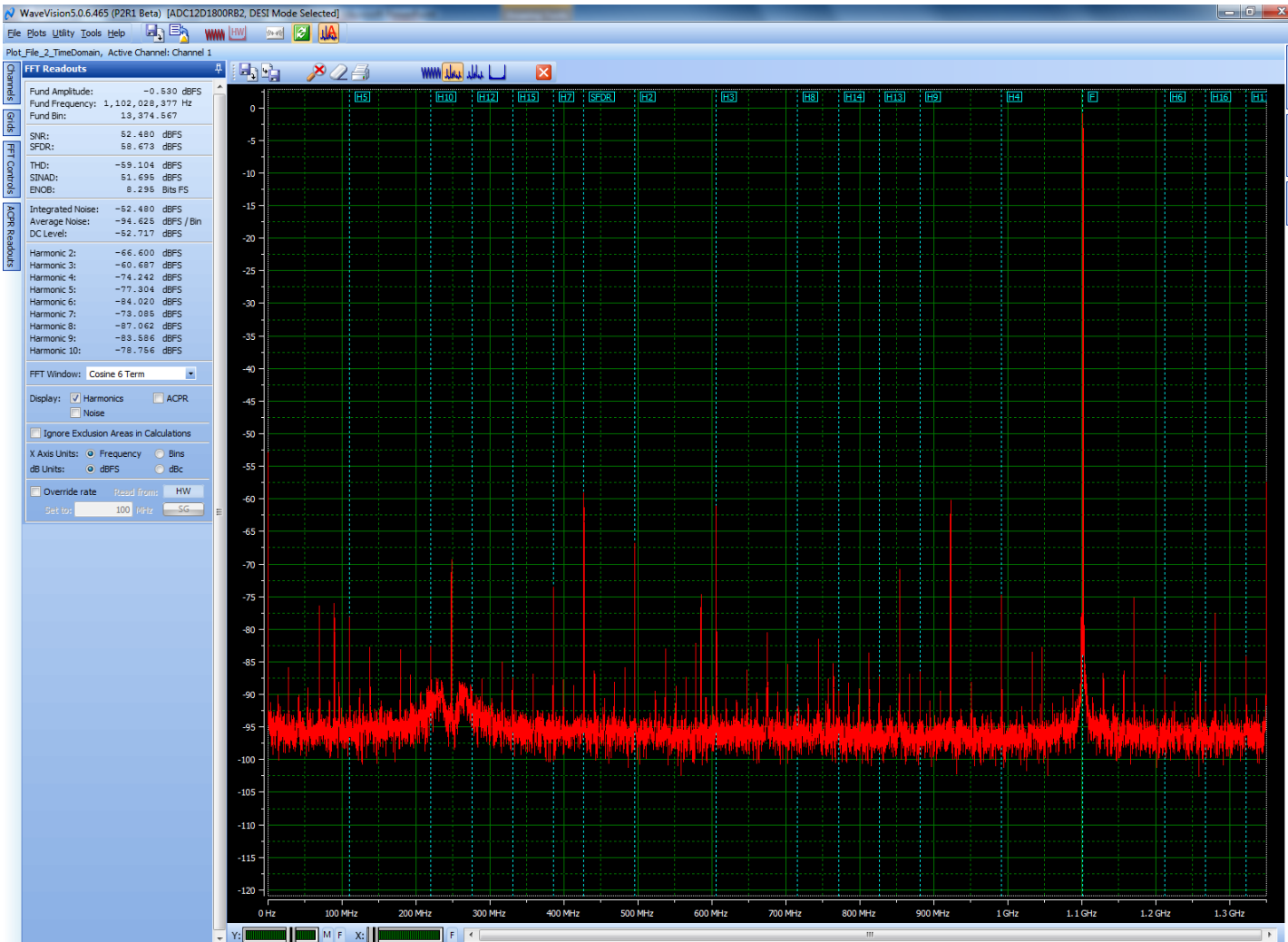
Setup

- ADC12D1800 with ext clock
- Fclk= 1350 MHz @ 5dBm via tunable BPF
- DES mode so $F_s = 2700$ MSPS
- $F_{in} = 1497.97\text{MHz}$ to 2497.97MHz via tunable BFP
- DESI using Hyperlabs balun
- F_{in} folds 1202.03 MHz to 202.03 MHz
- FS Range for I and Q at Default Settings
- One-time adjustment of DES timing adjust
- WV5 FFT averaging on default settings

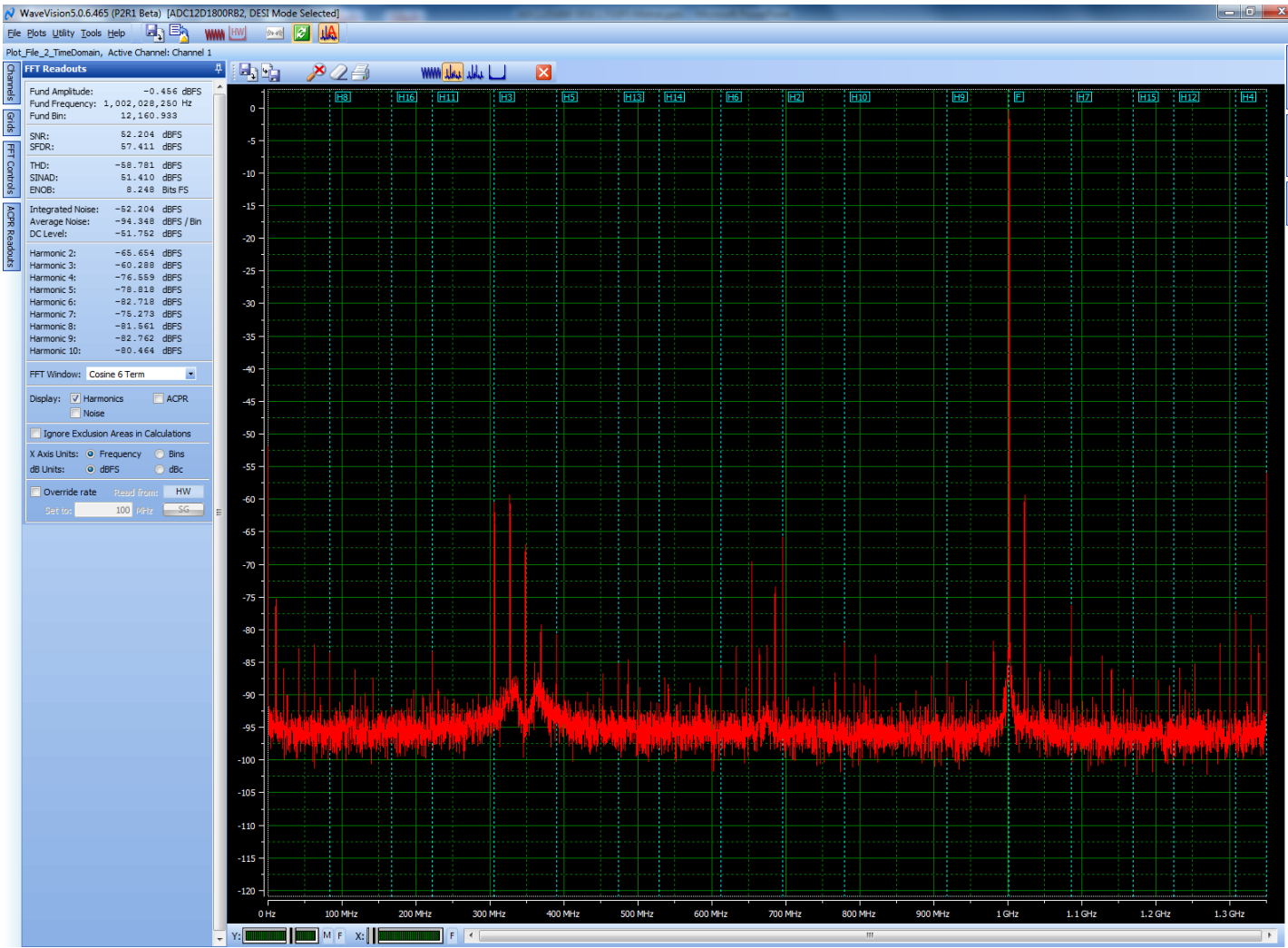
1497.97 MHz Pgen 8.8dBm



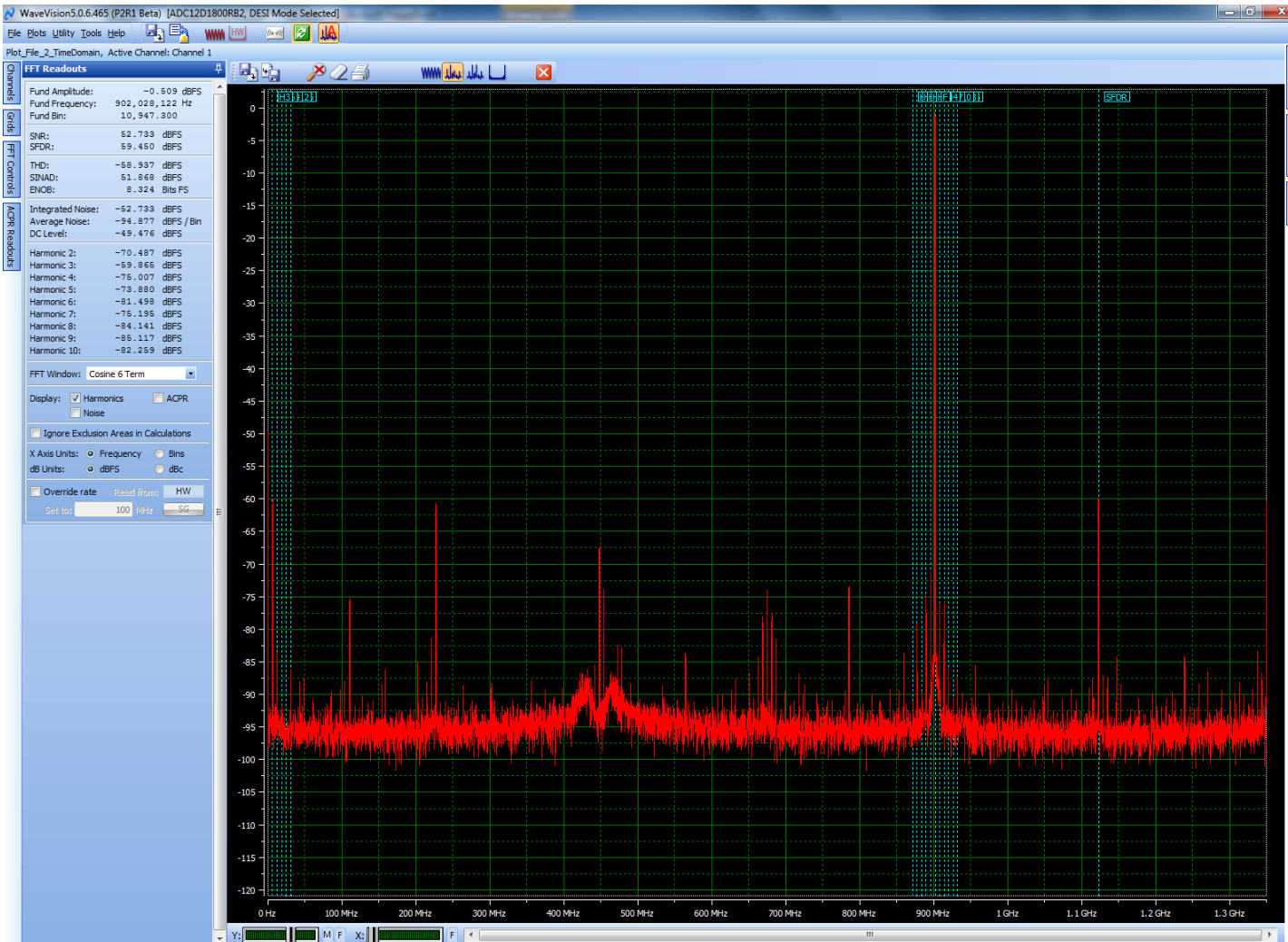
1597.97 MHz Pgen 9.9dBm



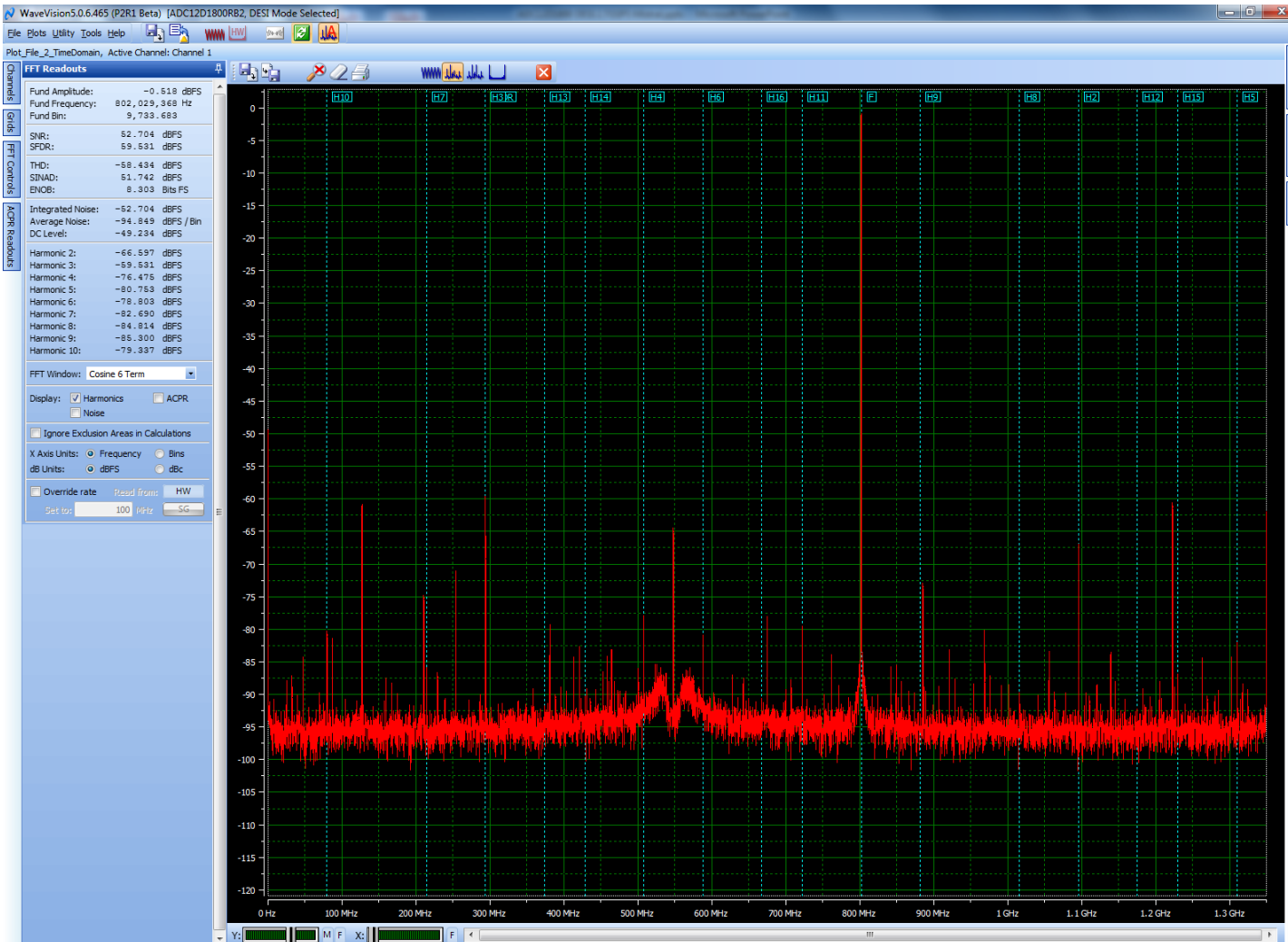
1697.97 MHz Pgen 11.8dBm



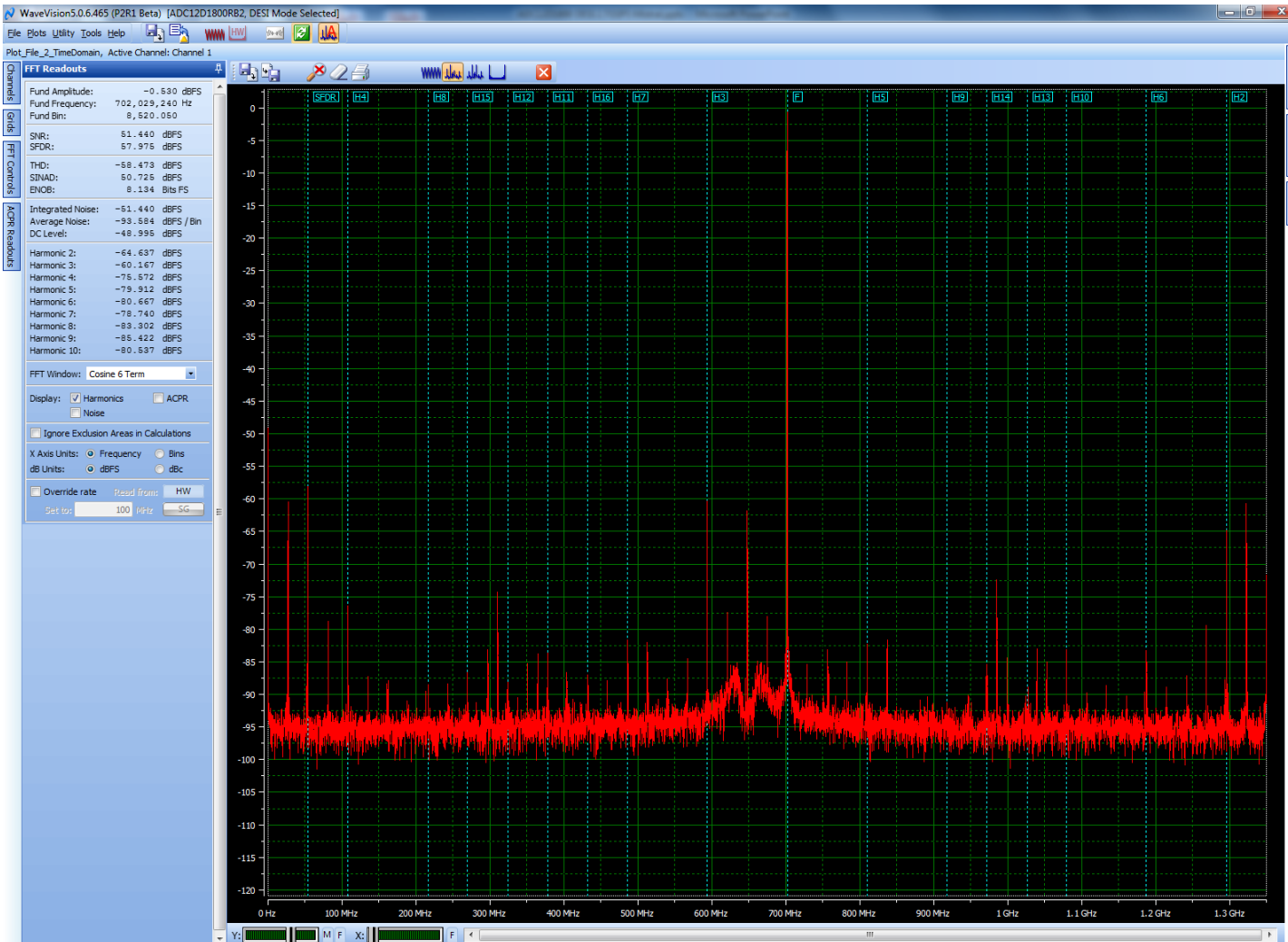
1797.97 MHz Pgen 11.5dBm



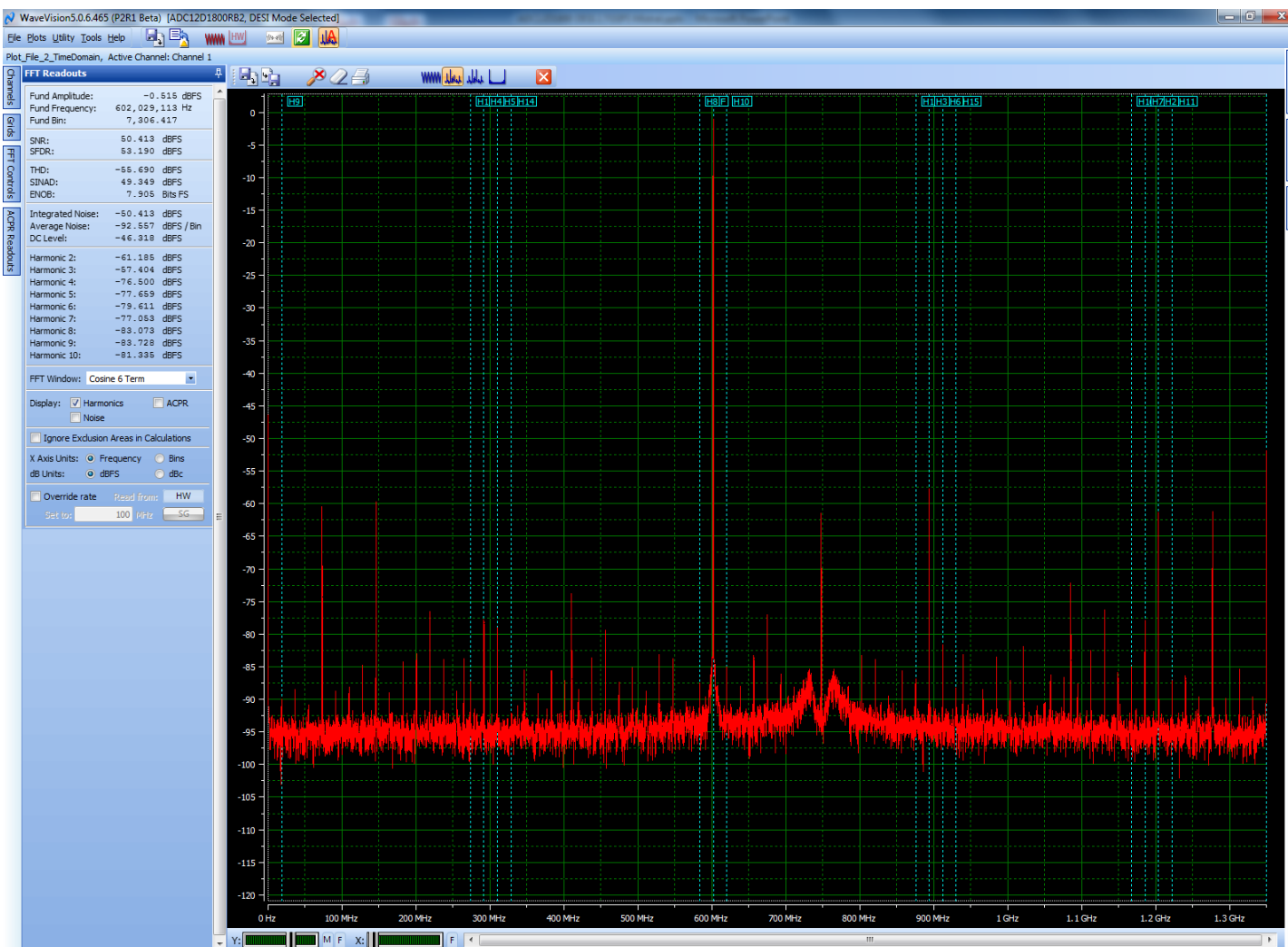
1897.97 MHz Pgen 11.1dBm



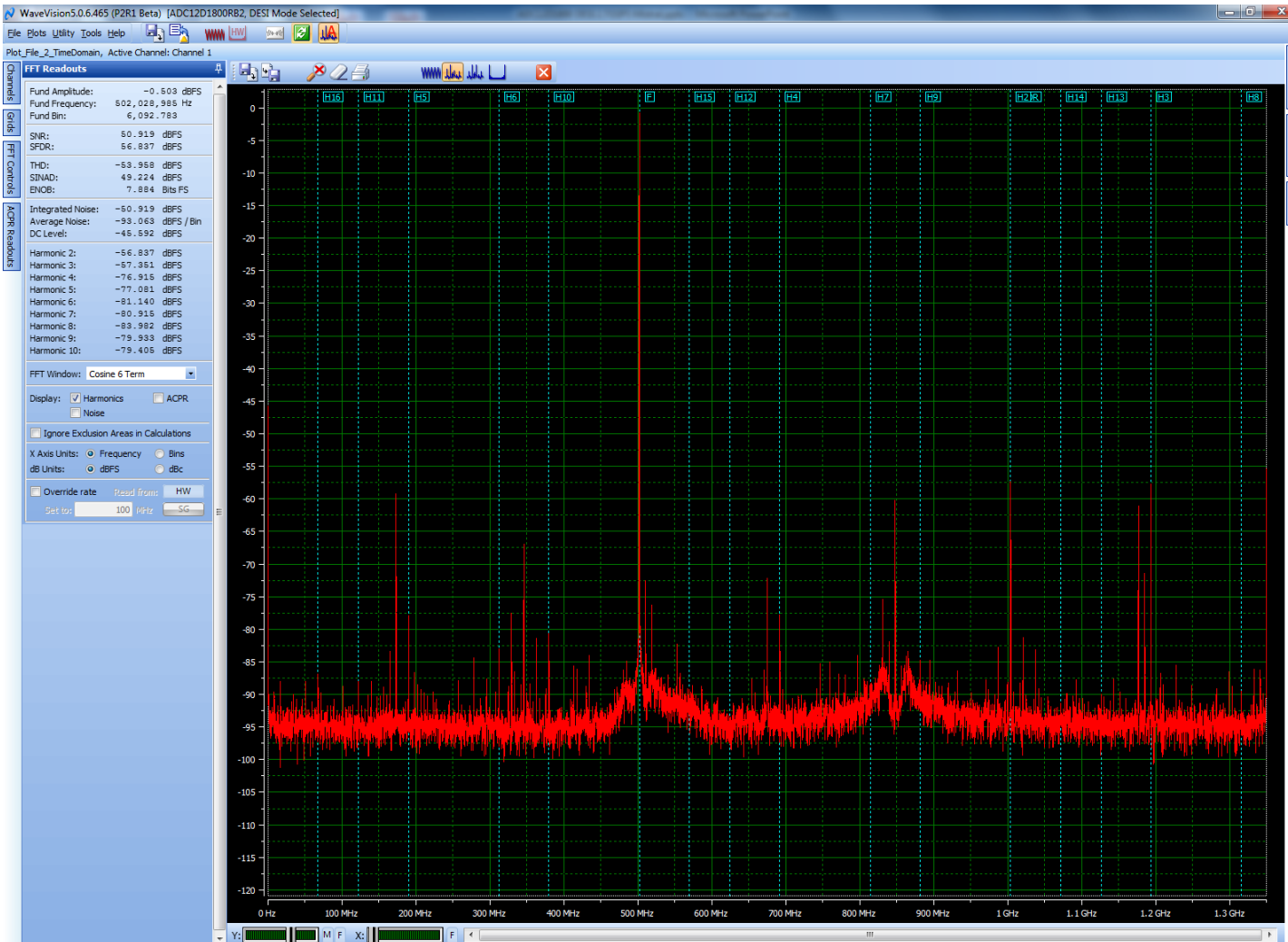
1997.97 MHz Pgen 12.6dBm



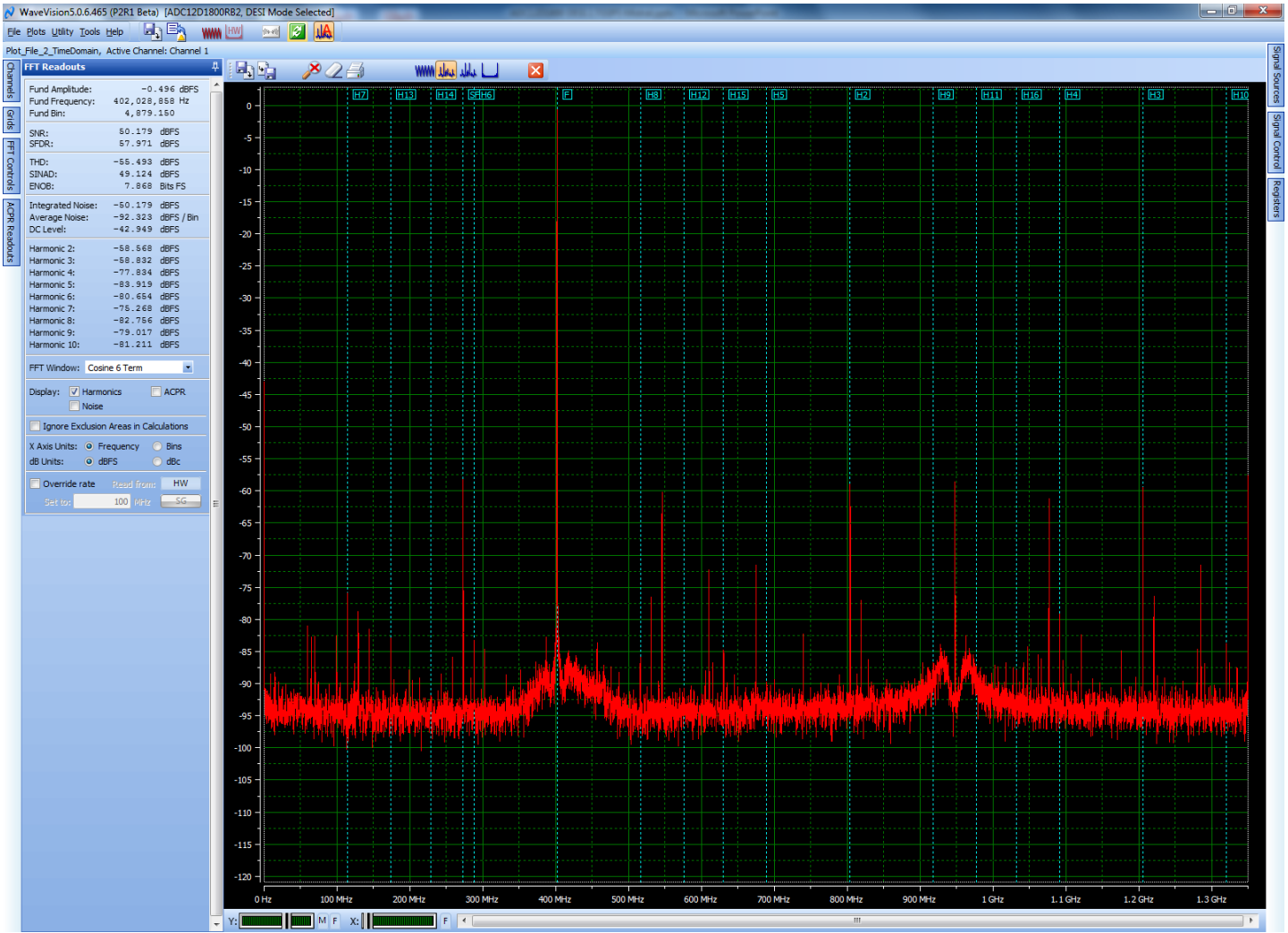
2097.97 MHz Pgen 13.0dBm



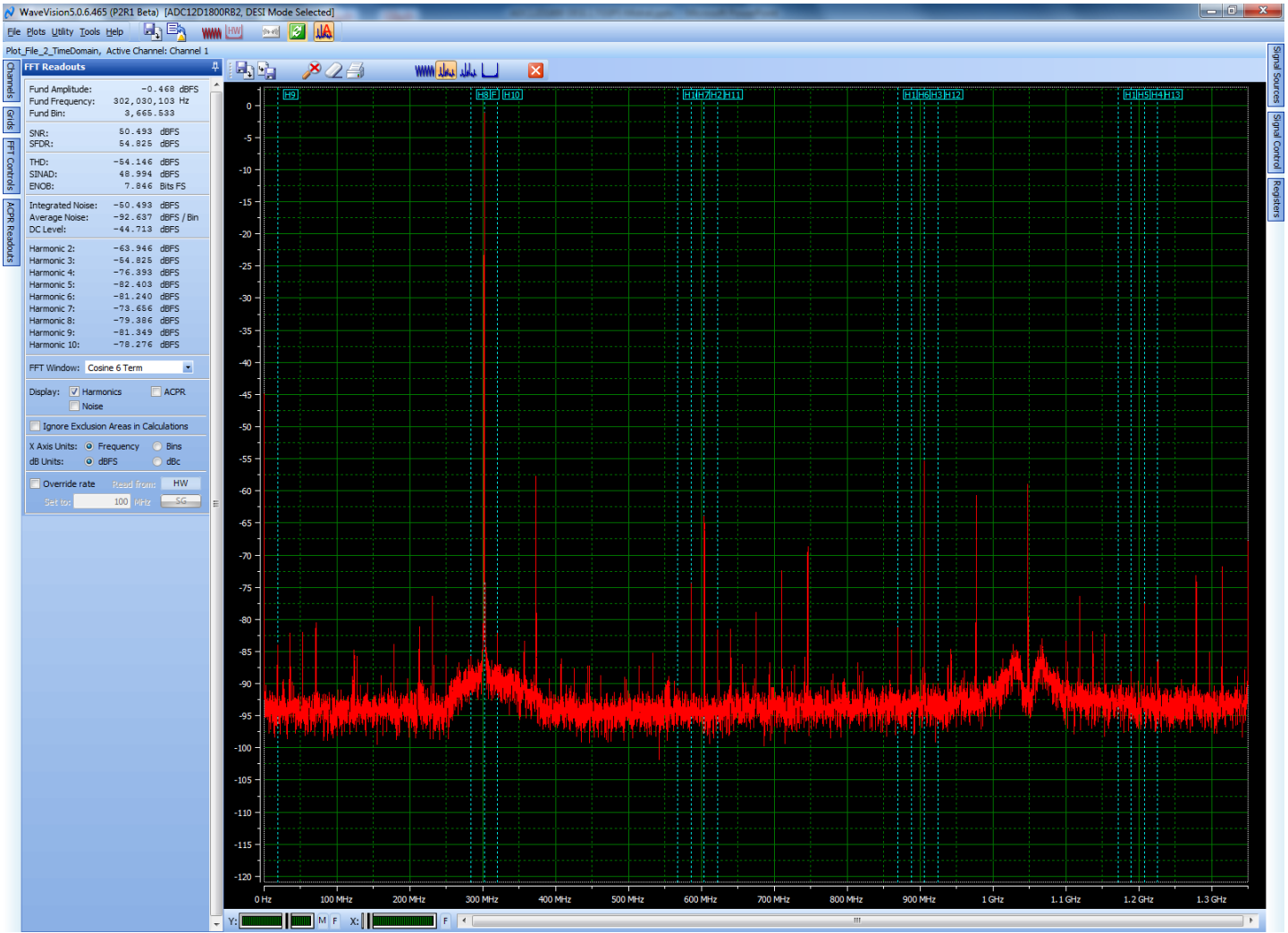
2197.97 MHz Pgen 12.9dBm



2297.97 MHz Pgen dBm



2397.97 MHz Pgen 16.2dBm



2497.97 MHz Pgen 14.8dBm

