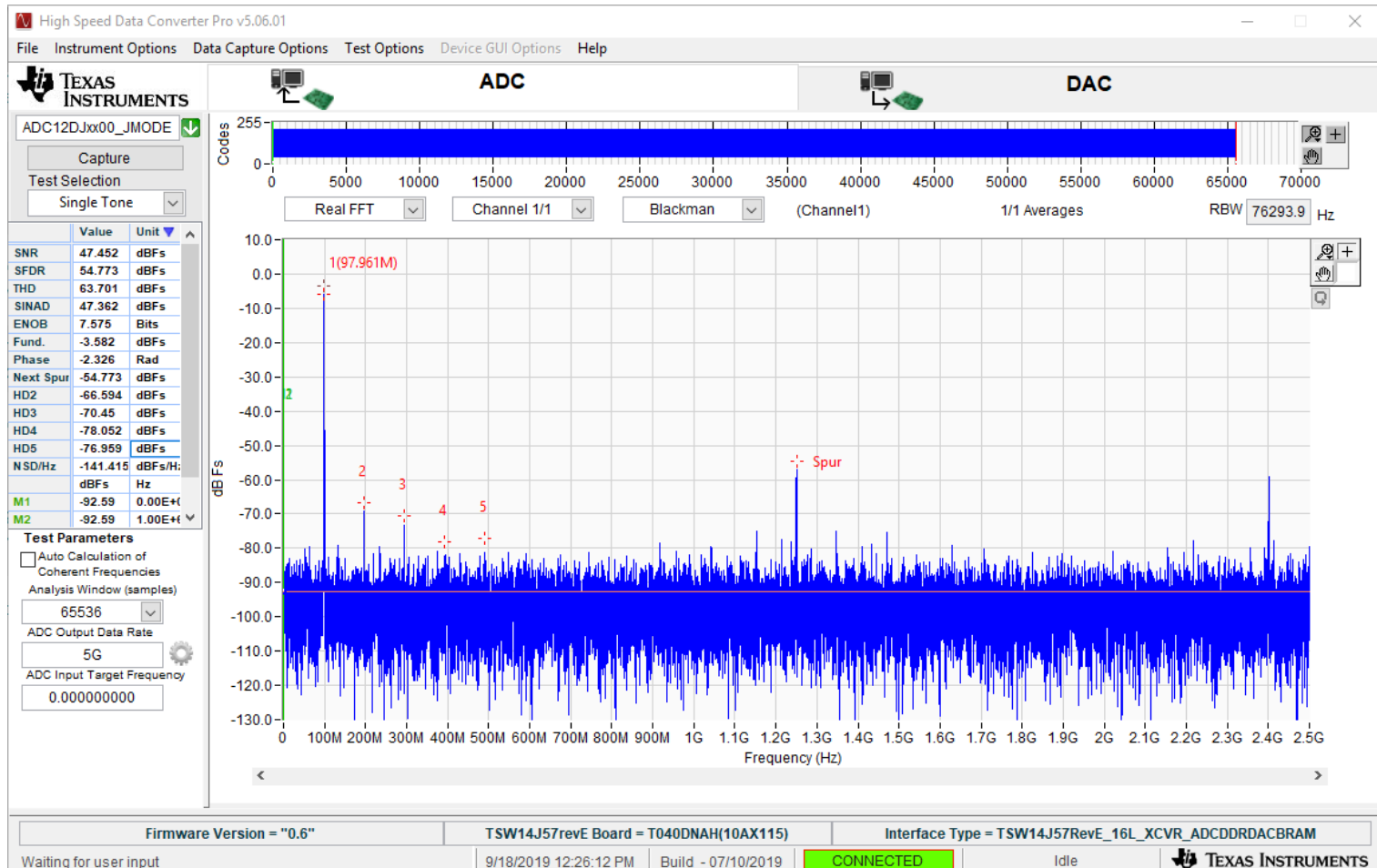


ADC12DJ3200 JMODE5

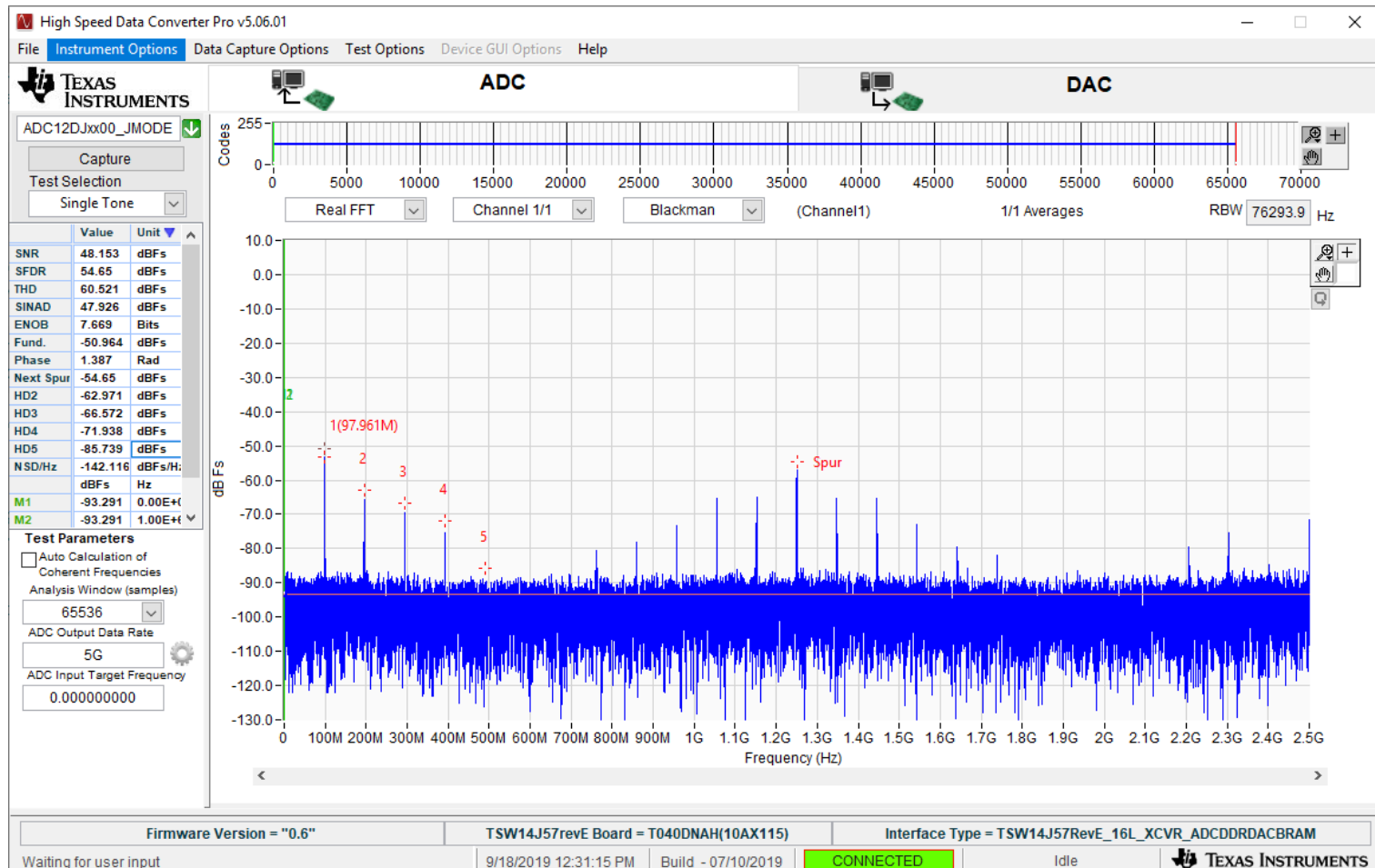
$F_s = 5\text{GHz}$

$F_{in} = 100\text{MHz}, 1250\text{MHz}$ and
 2380MHz

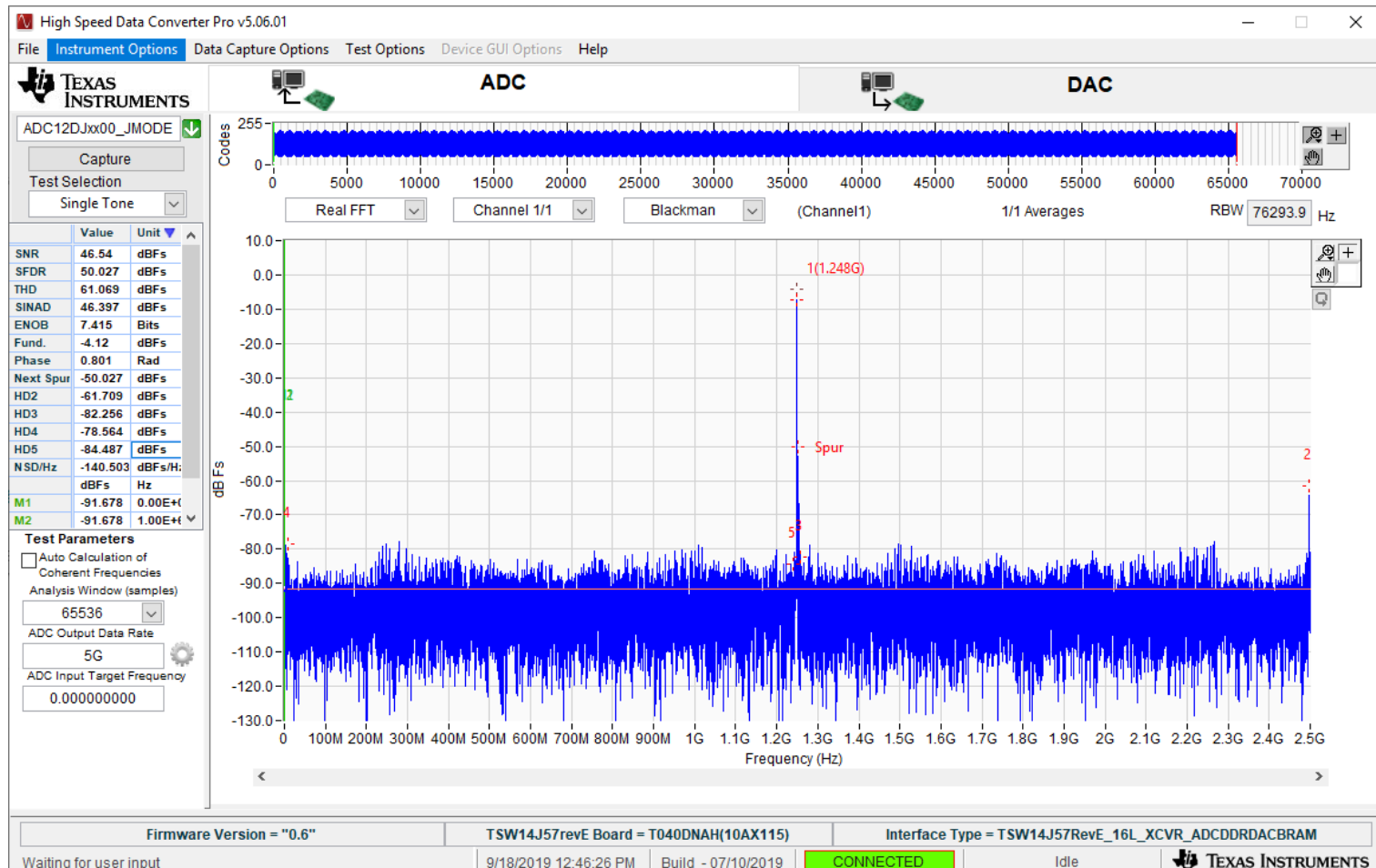
Fin = 100MHz, Ain = 0 dBm



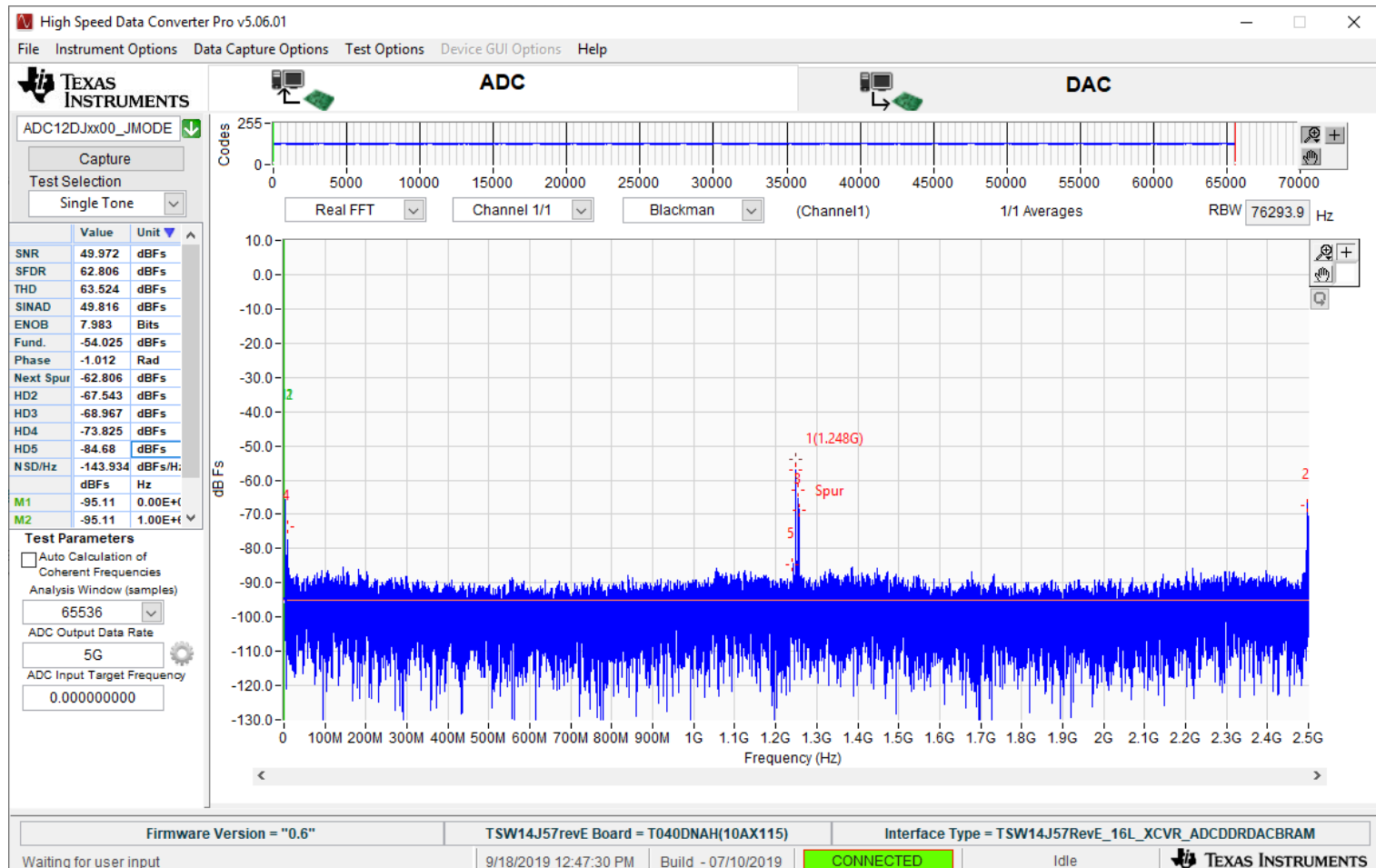
$F_{in} = 100\text{MHz}$, $A_{in} = -45\text{ dBm}$



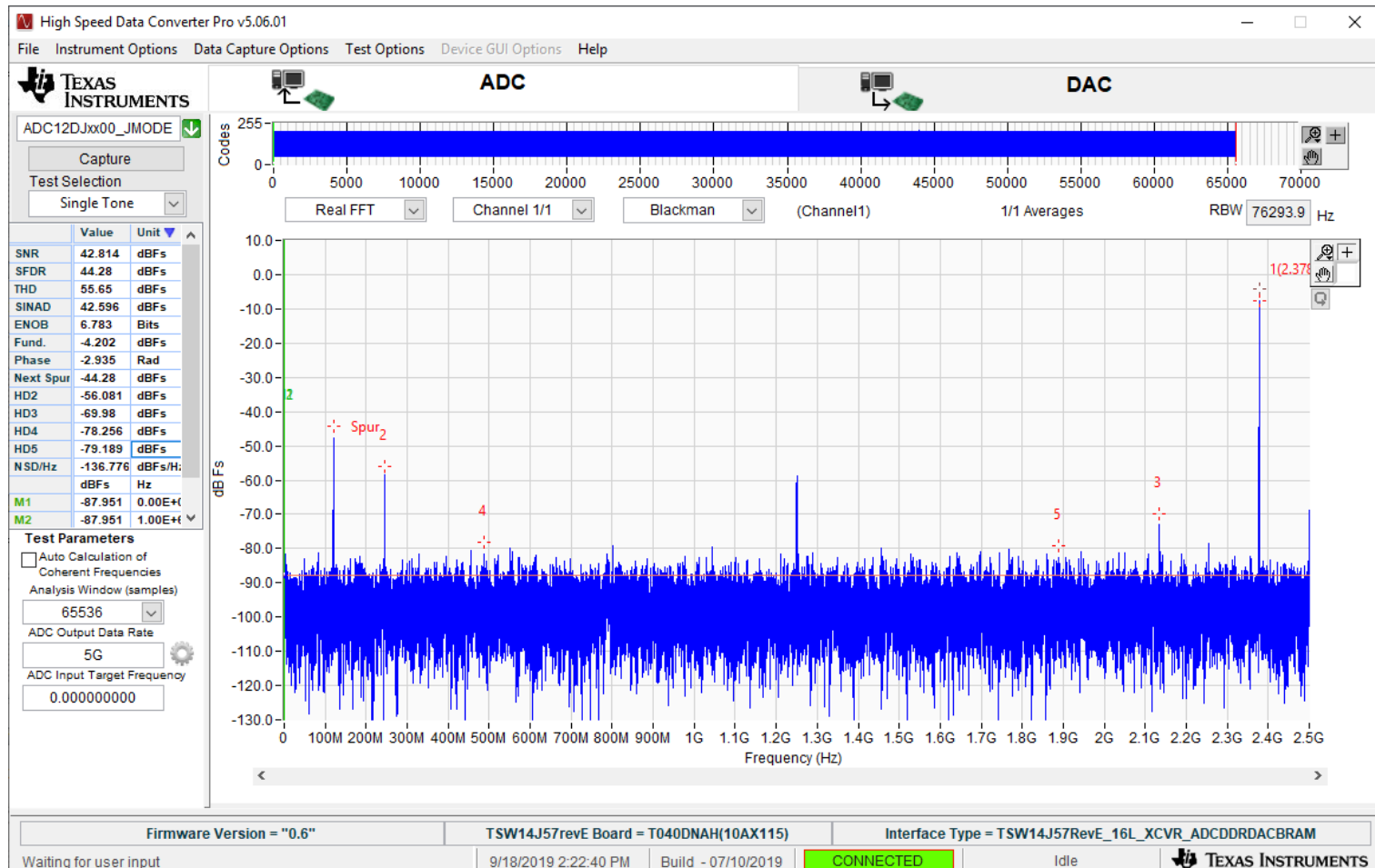
$F_{in} = 1250\text{MHz}$, $A_{in} = 0\text{ dBm}$



Fin = 1250MHz, Ain = -45 dBm



$F_{in} = 2380\text{MHz}$, $A_{in} = 0\text{ dBm}$



Fin = 2380MHz, Ain = -45 dBm

