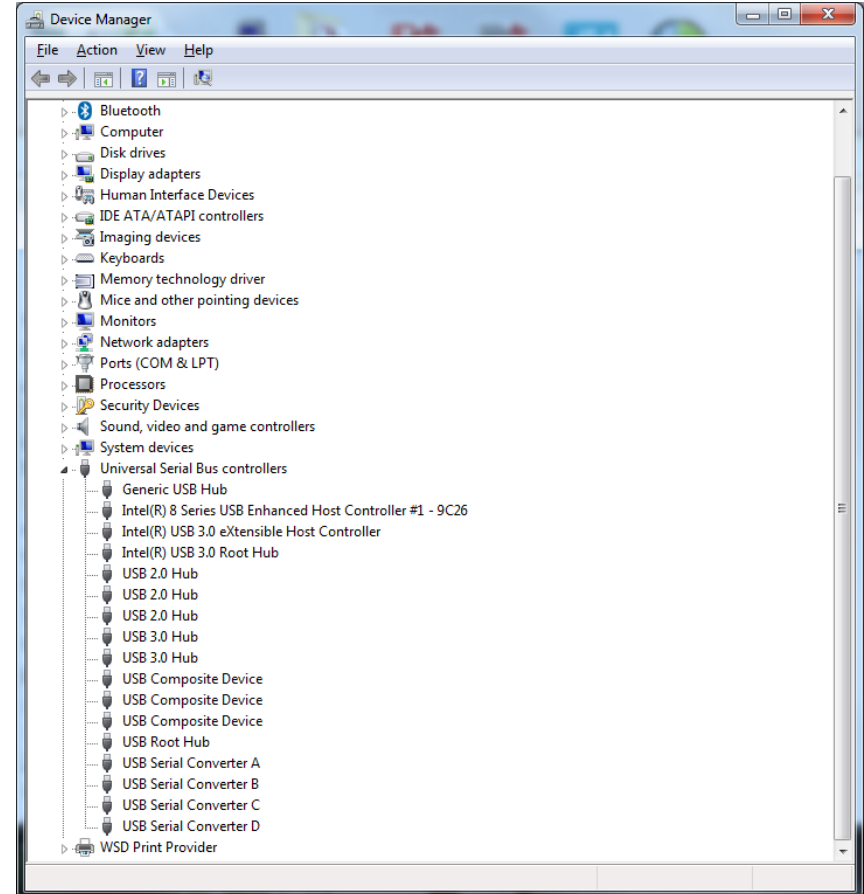
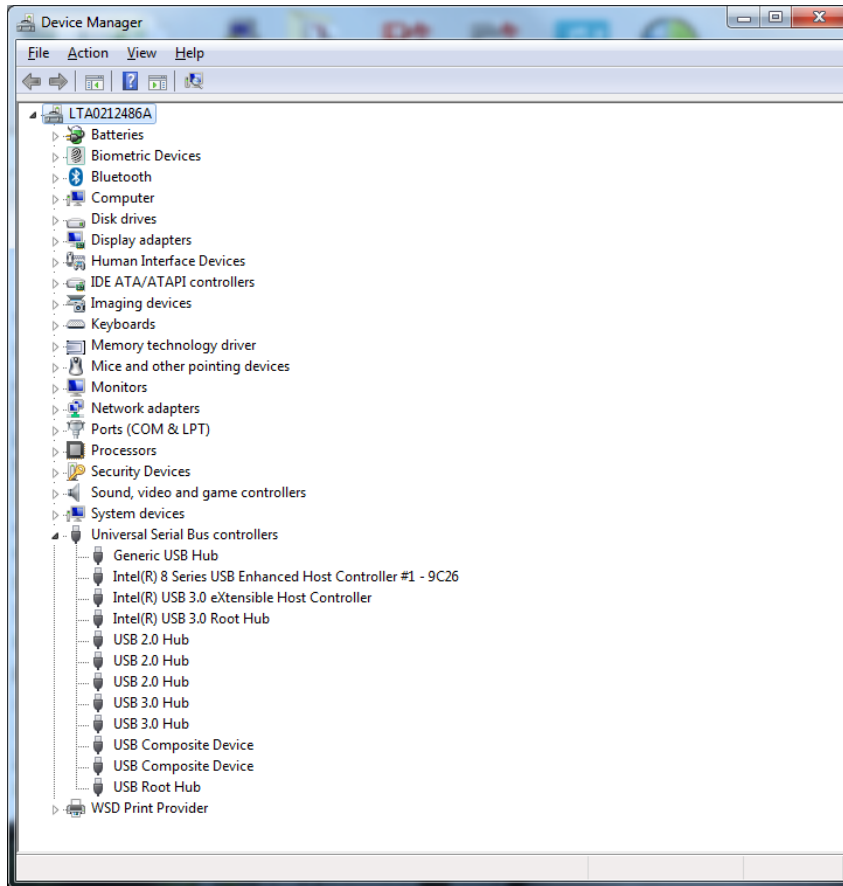


ADC3244 + TSW1400 bringup

USB enumeration for TSW1400 Serial Converter A, B, C, D



Start HSDC Pro as Administrator

Should detect TSW1400 with serial number – Click OK

The screenshot displays the High Speed Data Converter Pro v4.42 software interface. A 'Select Board' dialog box is open, prompting the user to 'Select The Serial number of the Device'. The dialog box contains a list of serial numbers, with 'TIWABT57-TSW1400' selected. Below the list, there is a checkbox for 'Connect to KCU105' and a field for 'Select/Enter IP Address - Port Number'. The 'OK' button is highlighted with a green checkmark, and the 'Cancel' button is highlighted with a red X.

The background interface shows a Real FFT plot with a peak at approximately 65536 Hz. The plot is titled 'Codes' and 'dBFS'. The x-axis is 'Frequency (Hz)' ranging from 0 to 1.0. The y-axis is 'dBFS' ranging from -130.0 to 10.0. The plot shows a main peak at 65536 Hz and a smaller peak at approximately 100m Hz labeled 'Spur'. The plot is titled 'Real FFT', 'Channel 1/8', 'Blackman', '(Channel1)', '1/1 Averages', and 'RBW 0 Hz'.

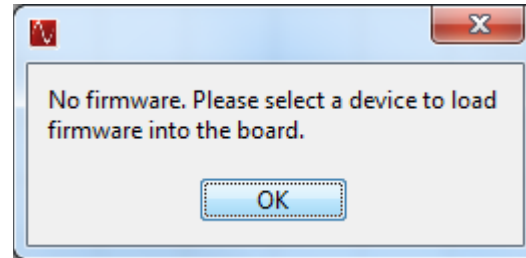
The left sidebar contains a 'Capture' button, 'Test Selection' set to 'Single Tone', and a table of test parameters:

	Value	Unit
SNR	0.00	dBFS
SFDR	0.00	dBFS
THD	0.00	dBFS
SINAD	0.00	dBFS
ENOB	0.00	Bits
Fund.	0.00	dBFS
Next Spur	0.00	dBFS
HD2	0.00	dBFS
HD3	0.00	dBFS
HD4	0.00	dBFS
HD5	0.00	dBFS
NSD/Hz	NaN	dBFS/Hz
M1	0.00	0.00E+0
M2	0.00	1.00E+0
Delta	0.00	1.00E+0

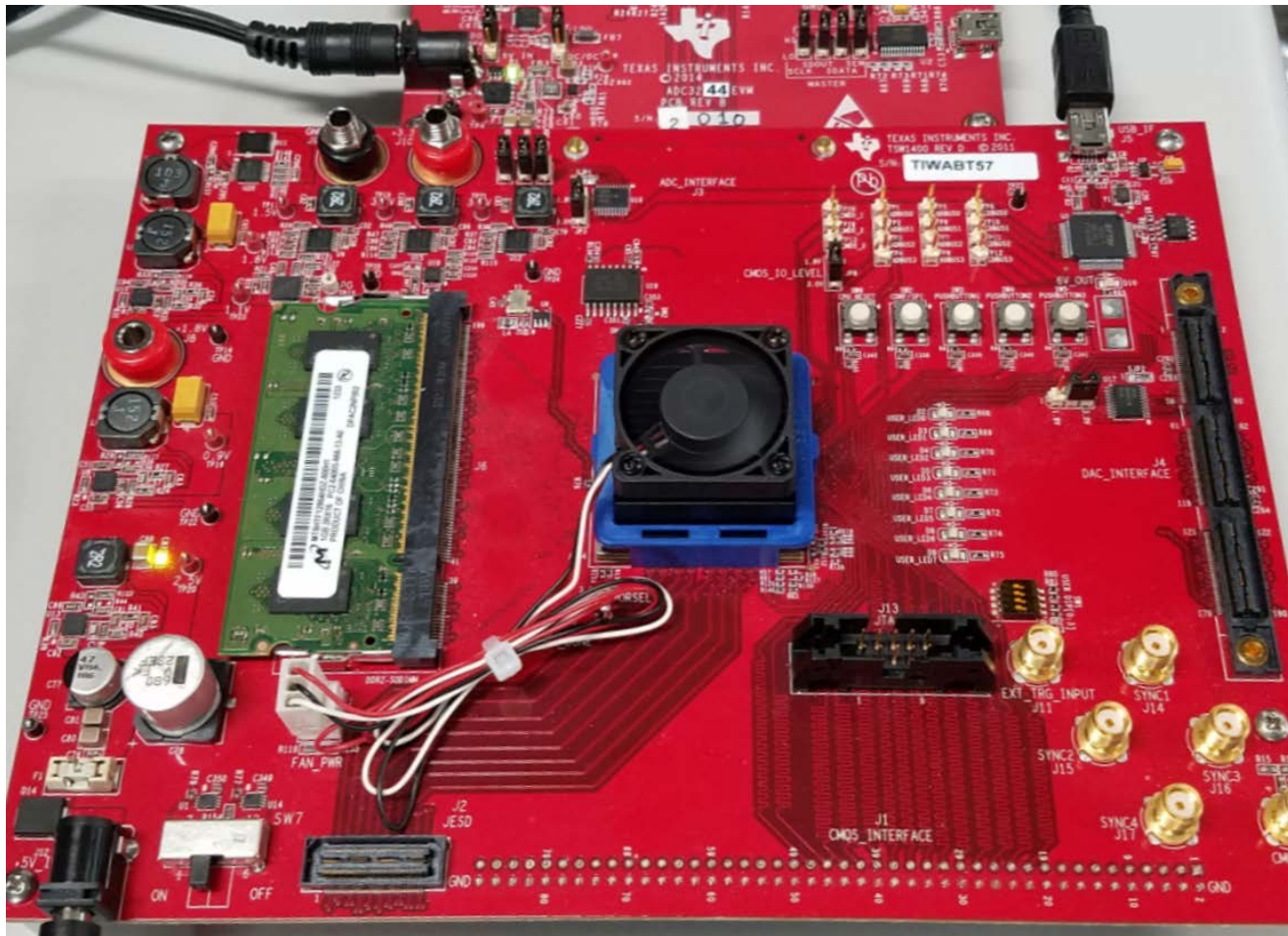
Below the table are 'Test Parameters' including 'Auto Calculation of Coherent Frequencies' (unchecked), 'Analysis Window (samples)' set to 65536, 'ADC Output Data Rate' set to 0, and 'ADC Input Target Frequency' set to 0.000000000.

The bottom status bar shows 'Firmware Ver= ""', 'Firmware Type = ""', 'Firmware Type = ""', 'Device info details', '5/23/2017 11:55:36 AM', 'Build - 11/16/2016', 'NOT CONNECTED', and the Texas Instruments logo.

Should get a msg
No Firmware...
Click OK



Picture of Hard ware and LED status at this point



HSDC Pro

Select ADC, ADC324x_2W_14bit

Click Yes – wait 30s for FPGA to load

The screenshot displays the High Speed Data Converter Pro v4.42 software interface. The main window is titled "High Speed Data Converter Pro v4.42" and features a menu bar with "File", "Instrument Options", "Data Capture Options", "Test Options", "Device GUI Options", and "Help". The interface is divided into two main sections: "ADC" and "DAC".

On the left side, there is a control panel for the "ADC324x_2W_14bit" device. It includes a "Capture" button, a "Test Selection" dropdown menu set to "Single Tone", and a table of test parameters. The table has columns for "Value" and "Unit".

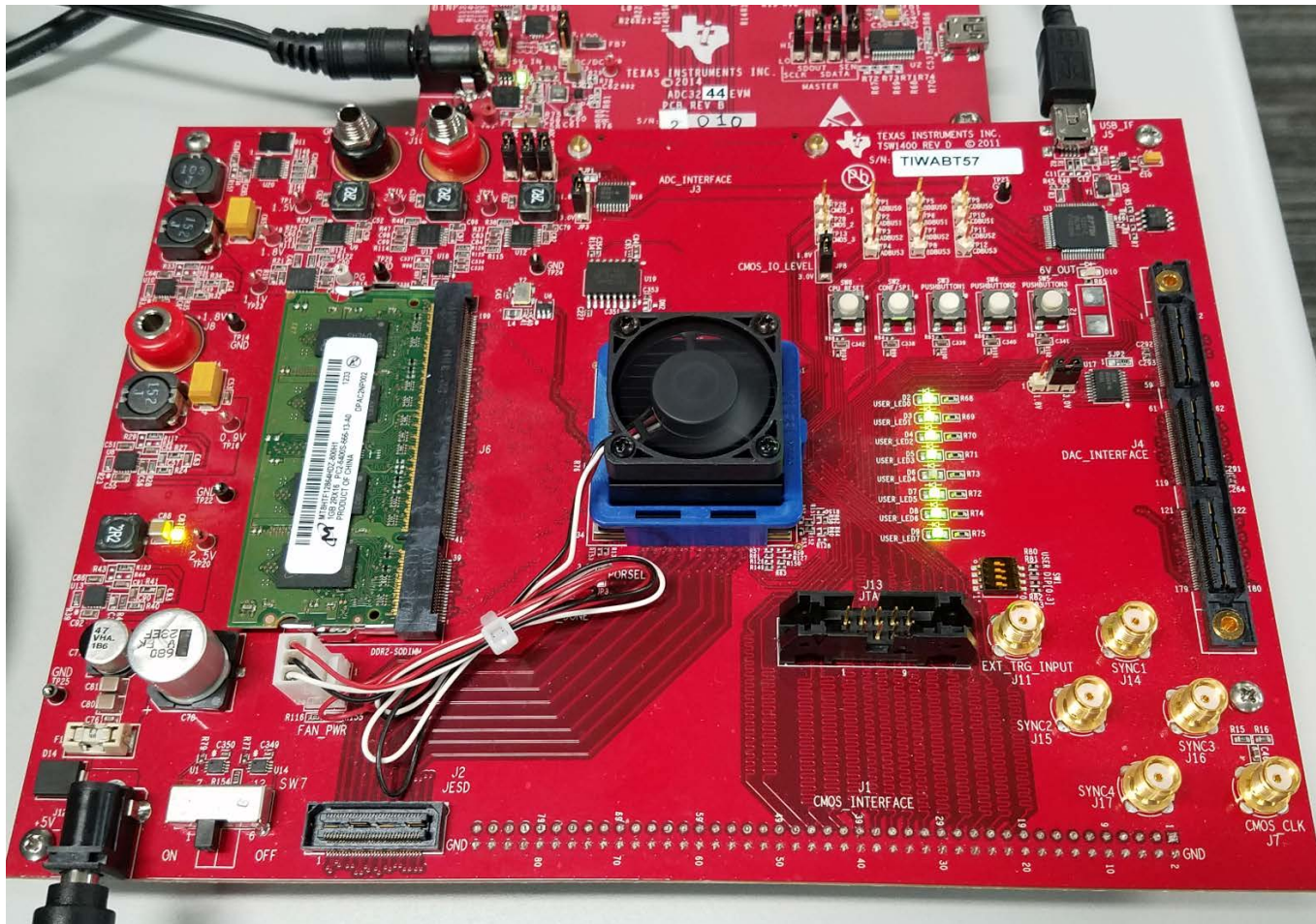
	Value	Unit
SNR	0.00	dBFS
SFDR	0.00	dBFS
THD	0.00	dBFS
SINAD	0.00	dBFS
ENOB	0.00	Bits
Fund.	0.00	dBFS
Next Spur	0.00	dBFS
HD2	0.00	dBFS
HD3	0.00	dBFS
HD4	0.00	dBFS
HD5	0.00	dBFS
NSD/Hz	NaN	dBFS/Hz
		dBFS Hz
M1	0.00	0.00E+0
M2	0.00	1.00E+0
Delta	0.00	1.00E+0

Below the table, there are "Test Parameters" including "Auto Calculation of Coherent Frequencies" (checked), "Analysis Window (samples)" set to 65536, "ADC Output Data Rate" set to 0, and "ADC Input Target Frequency" set to 0.000000000.

The main display area shows a Real FFT plot. The top plot shows "Codes" vs "Frequency (Hz)" from 0 to 70000. The bottom plot shows "dBFS" vs "Frequency (Hz)" from 0 to 1. A "Spur" is labeled at approximately 50mHz. A dialog box is overlaid on the plot, asking "Do you want to update the Firmware for ADC?" with "Yes" and "No" buttons.

At the bottom of the interface, there is a status bar showing "Firmware Version = '15.15'", "TSW1400 Board = TIWABT57", "Interface Type =", "Update Firmware Version", "5/23/2017 12:00:30 PM", "Build - 11/16/2016", a green "CONNECTED" indicator, and the Texas Instruments logo.

Picture of Hardware and LED after firmware is loaded
All LED are on except D6, if ADC clock is off D5 will also be off



HSDC Pro

Set clock to 125M

Click Capture, no input, just noise floor

