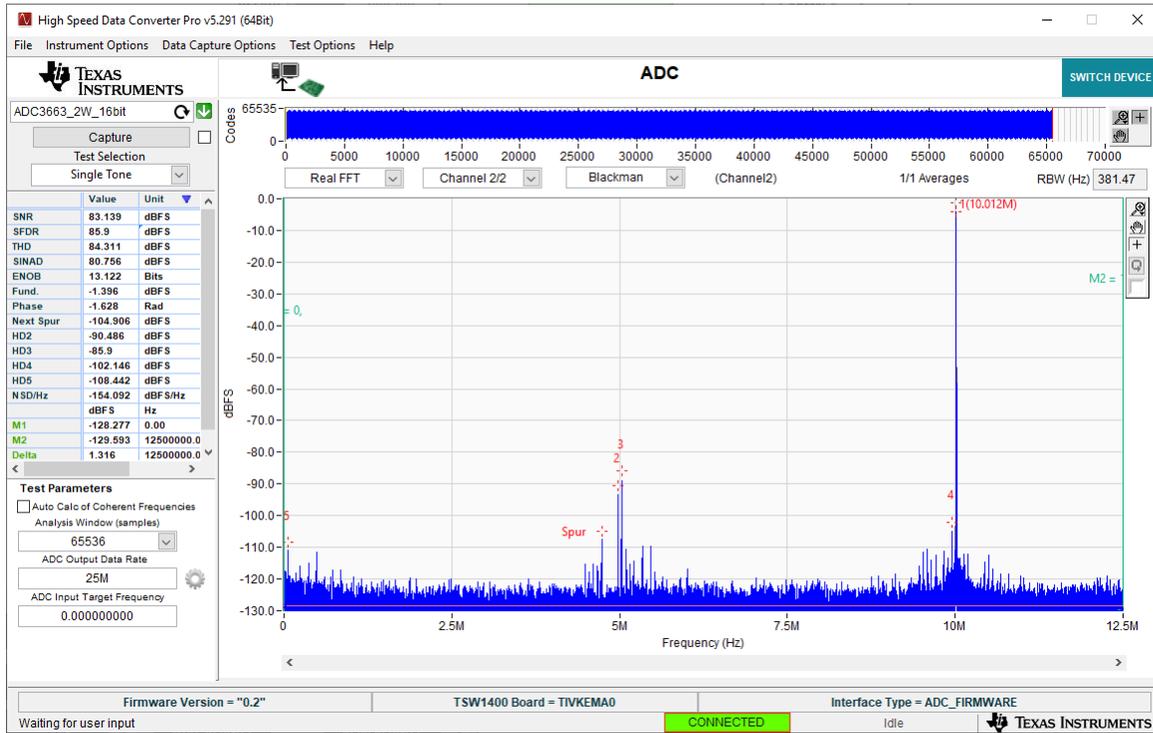


Sample rate: 25 MSPS, DCLK 100 MSPS



ADC35XX EVM GUI

REFBUF VREF AIN BIN CLK

Voltage Reference VREF, Control and PDI Control & Power Down

Analog Inputs and Clock Digital Downconverter Output Interface

Output Info

Device Variant: ADC3663 Resolution: 16 bit

Mode: 2 Wire DDC: Bypass

CDC Clock Enable

Fs (MHz): 25.0 DCLKIN (MHz): 100.0

Power Measurement

VDDA (mA): 0.0 IOVDD (mA): 0.0 Power (mW): 0.0

Home Reset Load Config File Save Config File

Address: 0x0011

Write Data: 0x00 Read Data: 0x00

Write Register Read Register

Reconnect USB Connected

Log: Register value of 0x11 is 0x0

