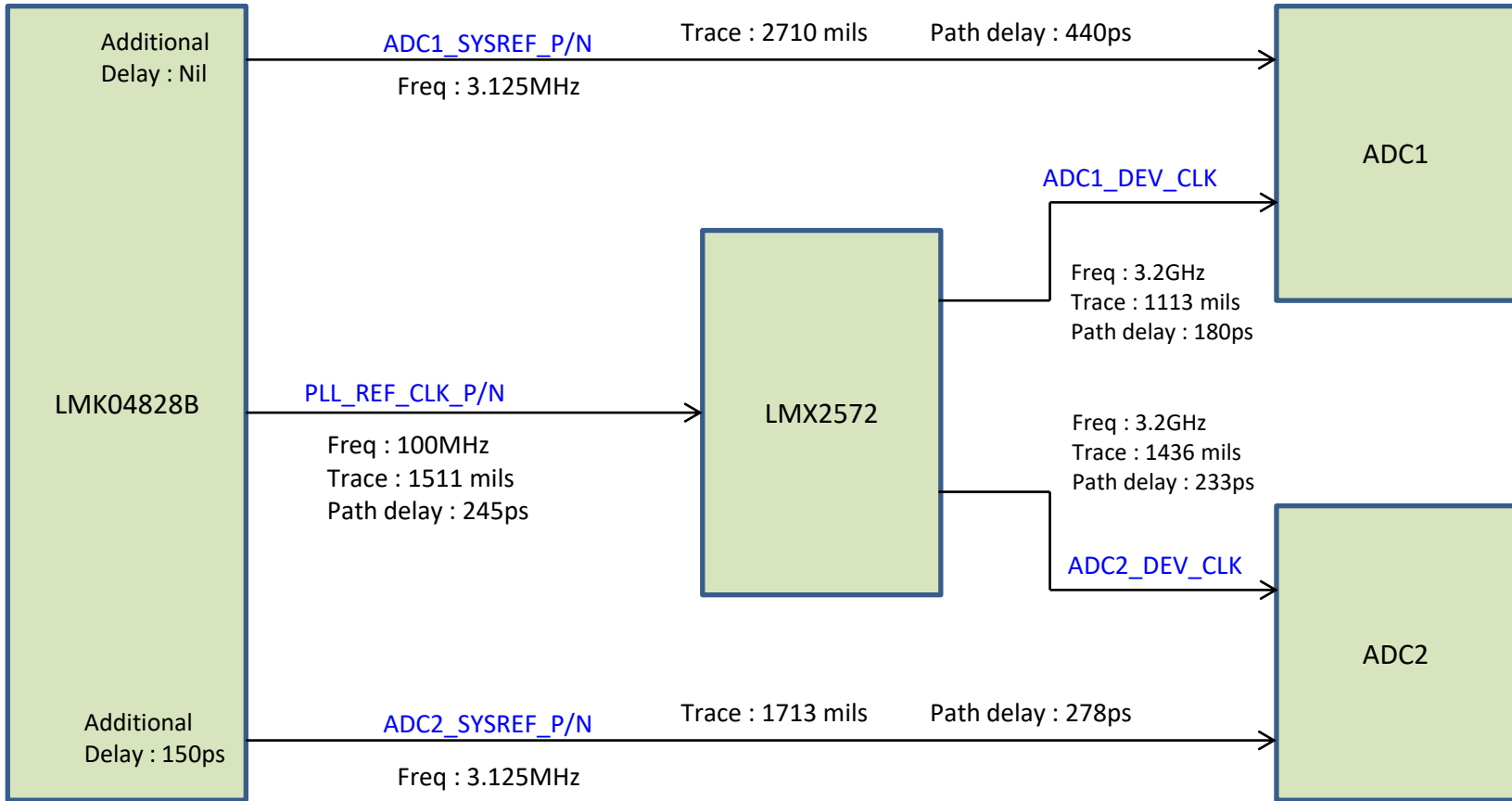


Clock Distribution Diagram



Timing diagram

