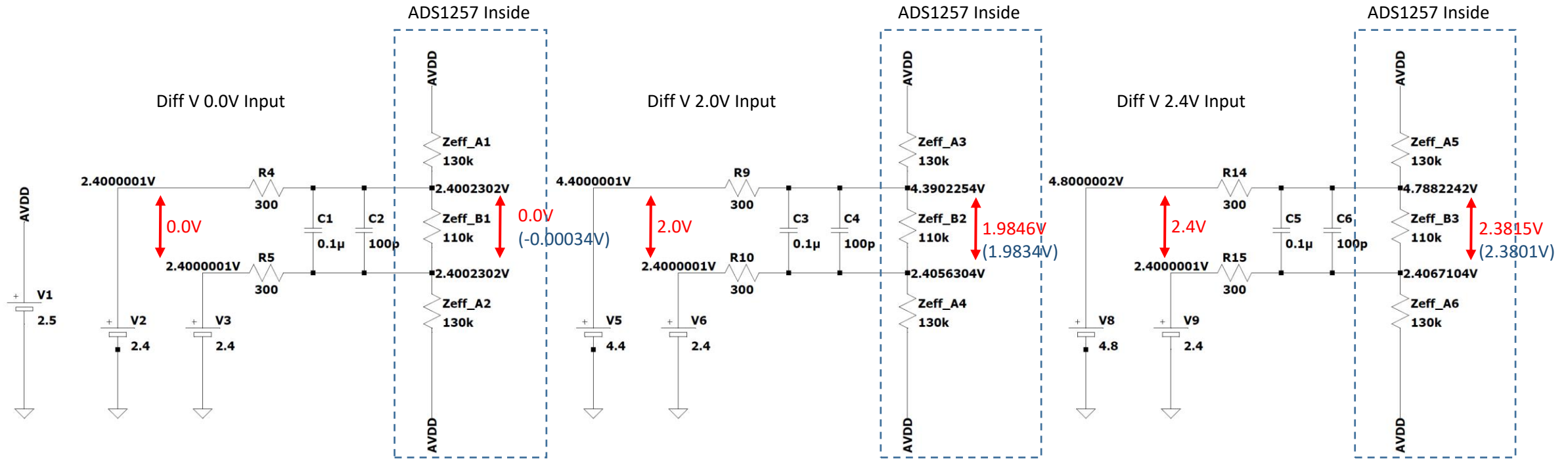


ADS1257 PGA=2 、Execute only SELFCAL

- The actual circuit has an input buffer (OP284), but it is ignored here for calculation.
- The input differential voltage 3 patterns are shown.



The actual AVDD is 5V.  
Represents AVDD / 2 = 2.5V.

Red letters are simulation values.  
Blue letters are ADC output value (voltage conversion).