Hi Ryan,

I did the internal programming as instructed and below you can see Channel 1 no longer saturated. Is 49f2 in the correct data range?

Below is the Internal capture I’d like to walk through.

Index 11 - Read register 2 before I change it. Data Sheet says second opcode number of registers to read - 1. Register 2 and 3 read - Ok

Index 13 - Write data to register 2. Second opcode says number of registers to write – 1 but registers 2 and 3 get written to?

The rest of the capture contains the internal programming implementation.

