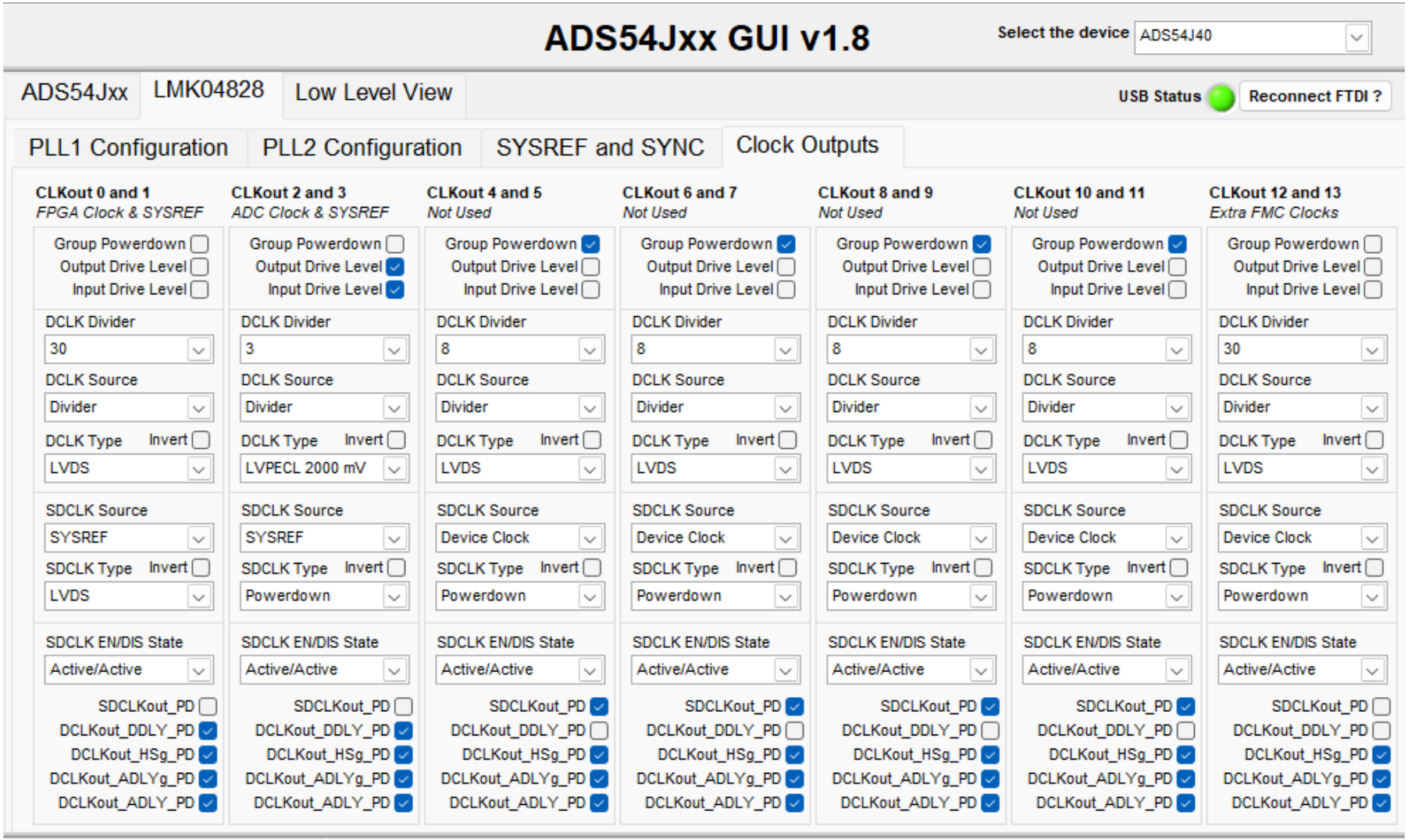
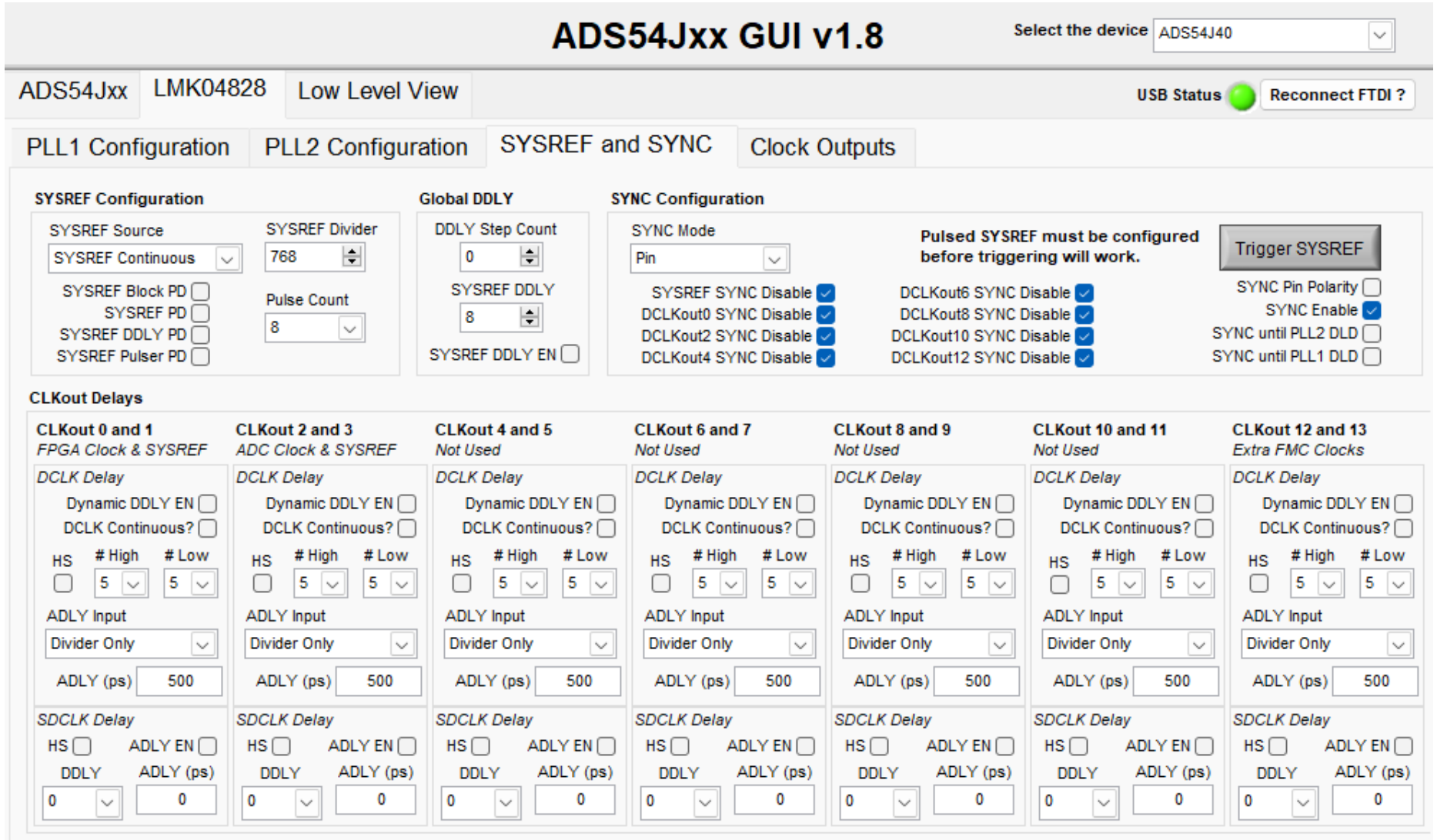
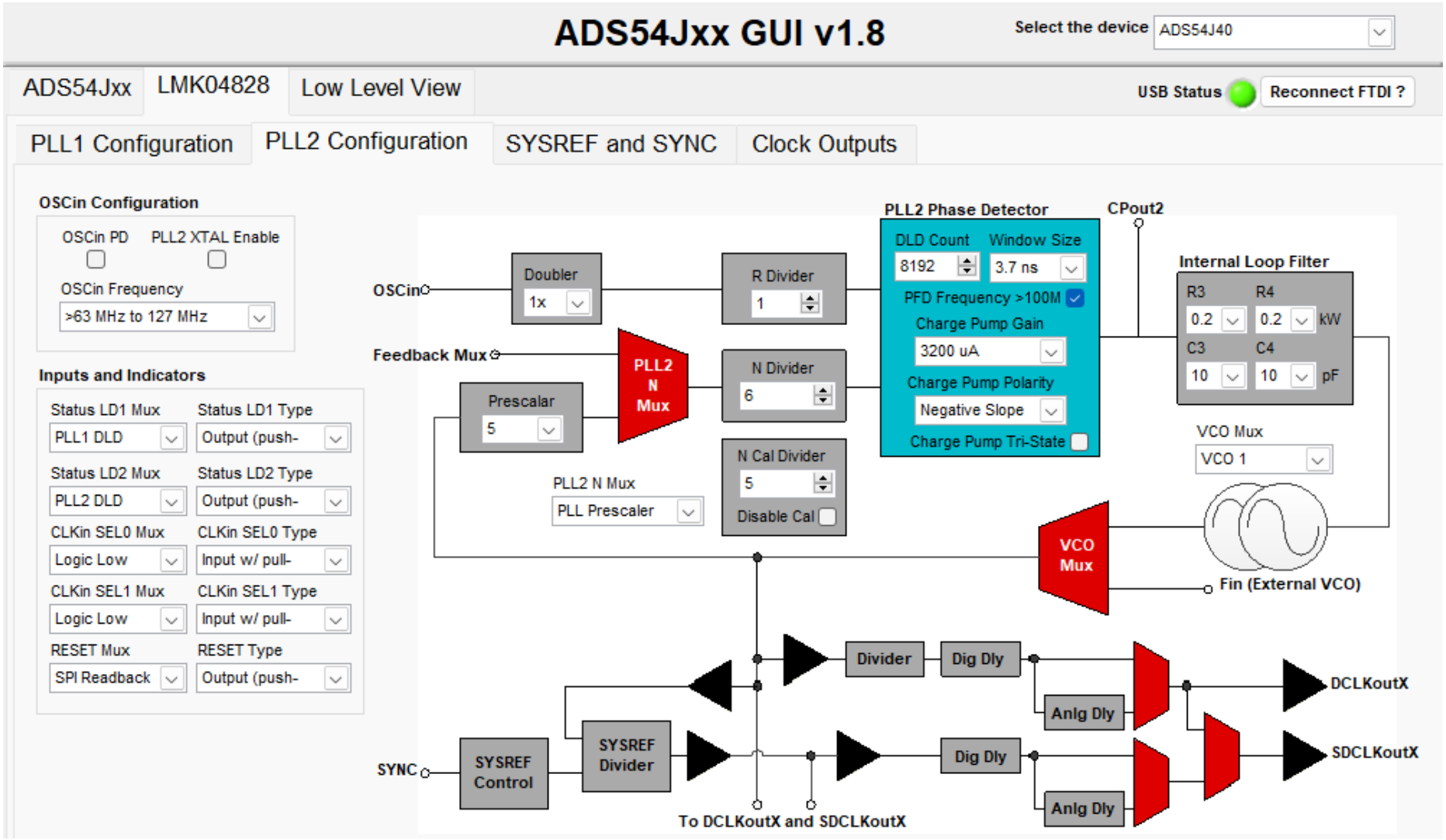
4. LMK04828



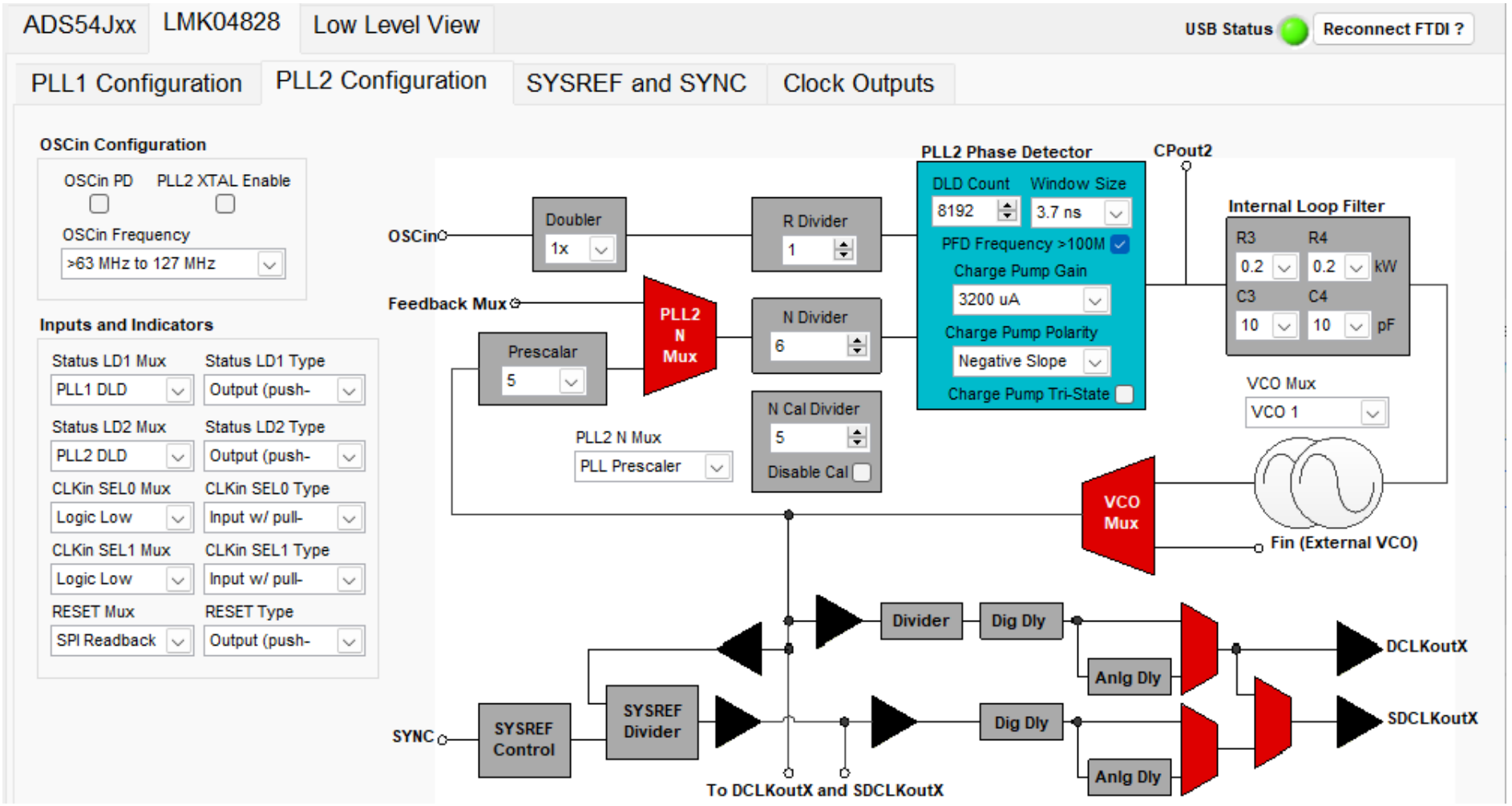
4. LMK04828



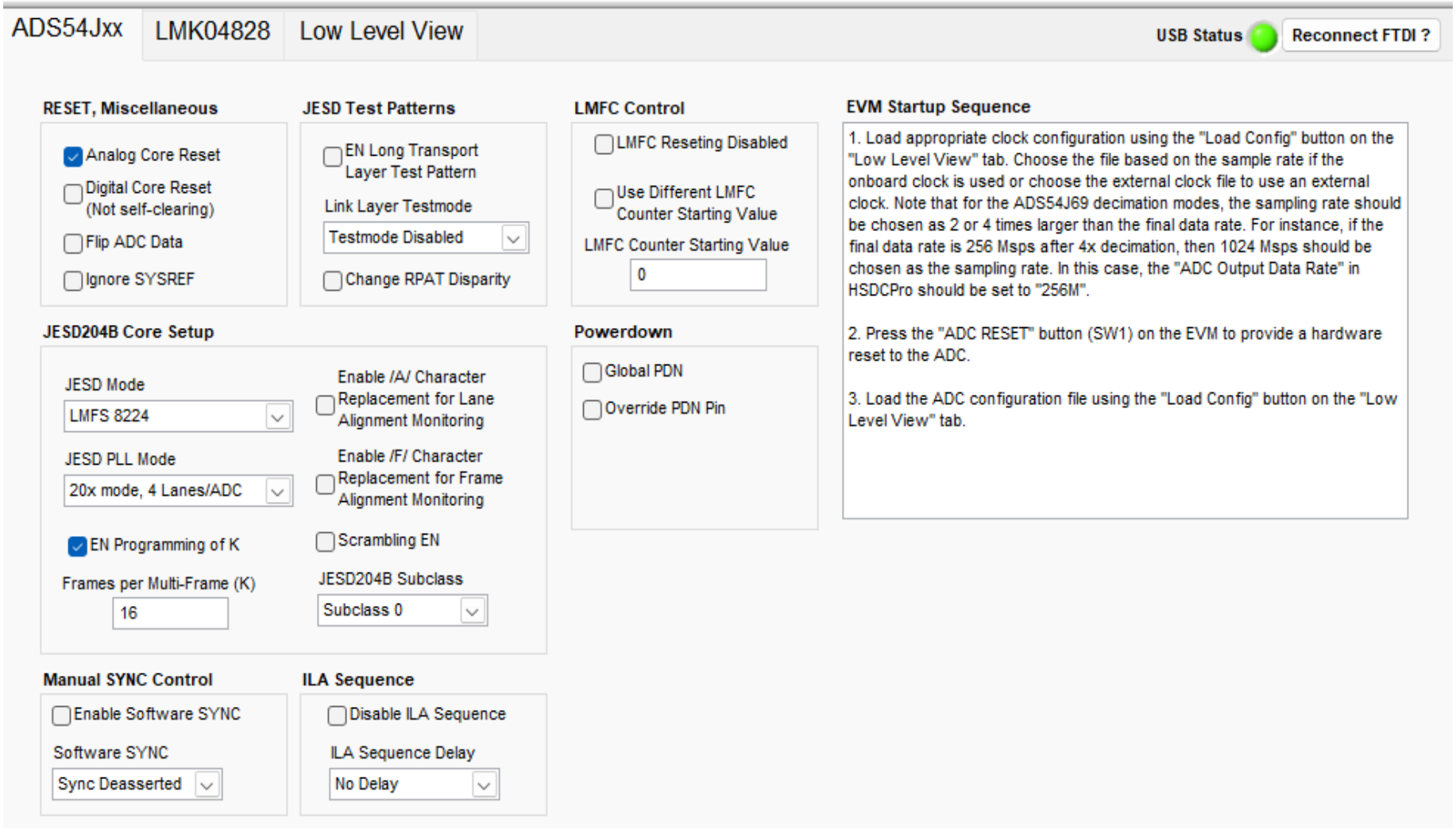
4. LMK04828



4. LMK04828



4. ADJ54J69



5. Measure LMK04828 by 1Gsps Oscilloscope

1. DCLKOUT0(FPGA_JESD_CLK) : 100MHz/0.2V
2. SDCLKOUT1(FPGA_JESD_SYSREF) : 3.9MHz/
3. DCLKOUT2(CLK) : not measured
4. SDCLKOUT3(SYSREF) : not measured
5. DCLKOUT12(CLK_LAO) : 100MHz/0.2V