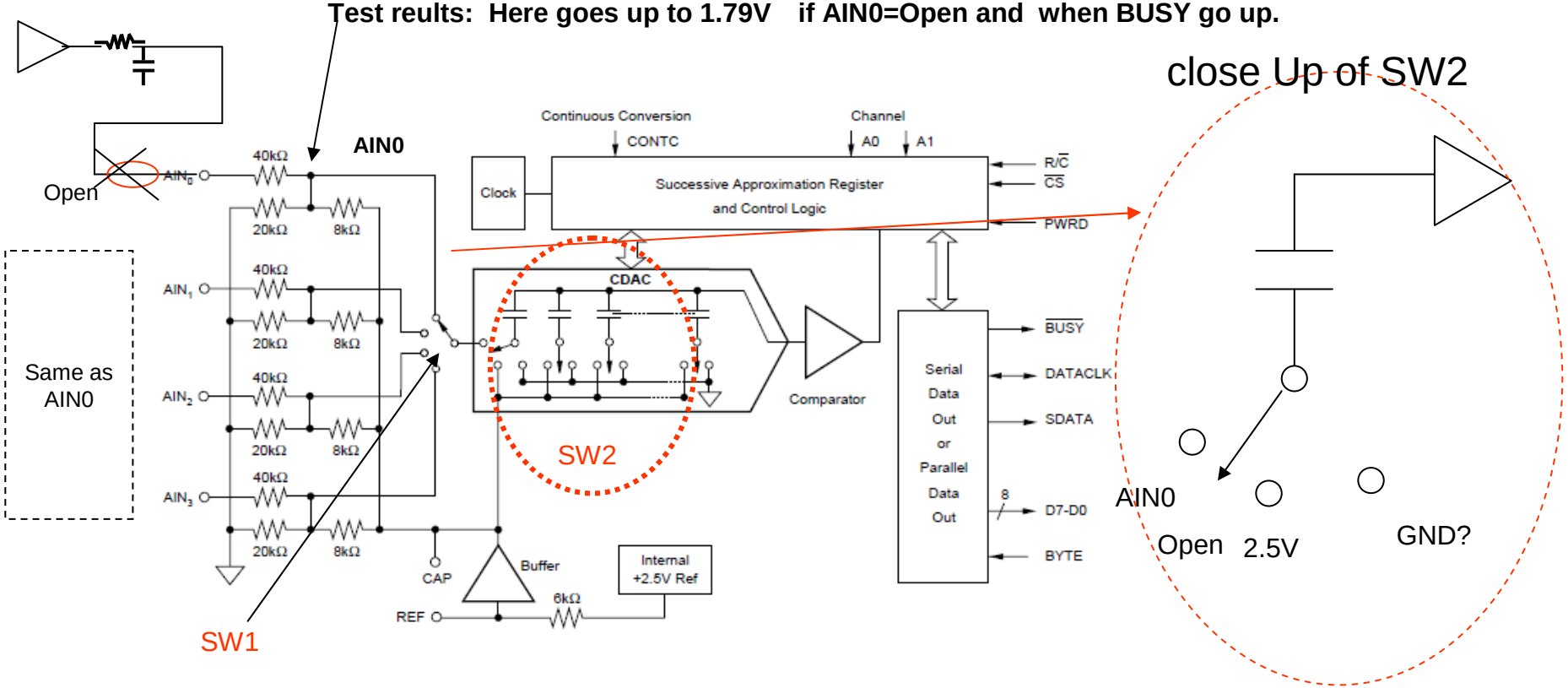
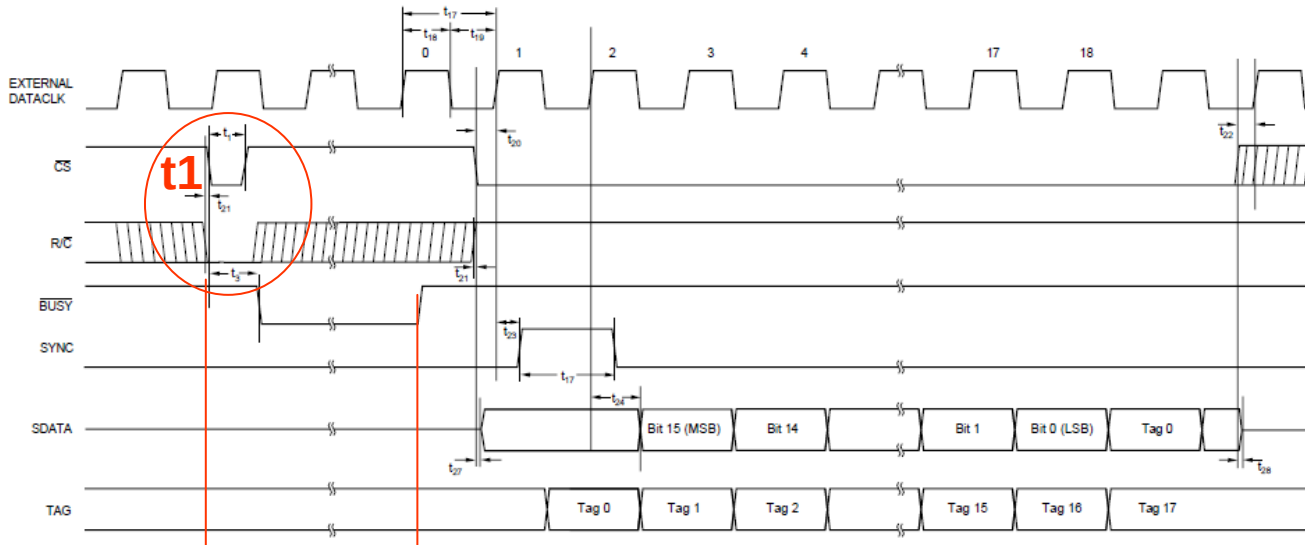
Test reults: Here goes up to 1.79V if AIN0=Open and when BUSY go up.





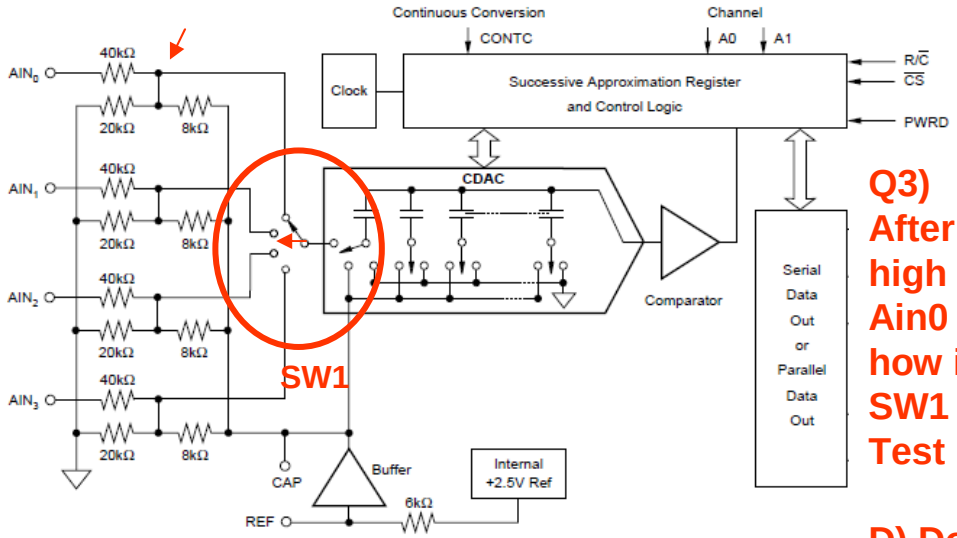
(A) R/C is ↓)

(B) BUSY is ↑

Q1 ,,,,(A)
What is the SW2's Status after t1?

Q2,,,,,(B)
What is the status of SW2 when BUSY goes Up?

Test Result, Here is 1.79V to 62mV in case (E)



Q3)
After the BUSY go up and R/C goes high to low (★), customer changes Ain0 to Ain1…..In this time, how is the state of previous channel(Ain0)? SW1 Just Oepn? (If so, it should be 1.79V), but Test result is 62mV.

D) Do you have any criteria (max limit) for tX? Even though t1 is within spec(max 12us), it may be long in the customer's case

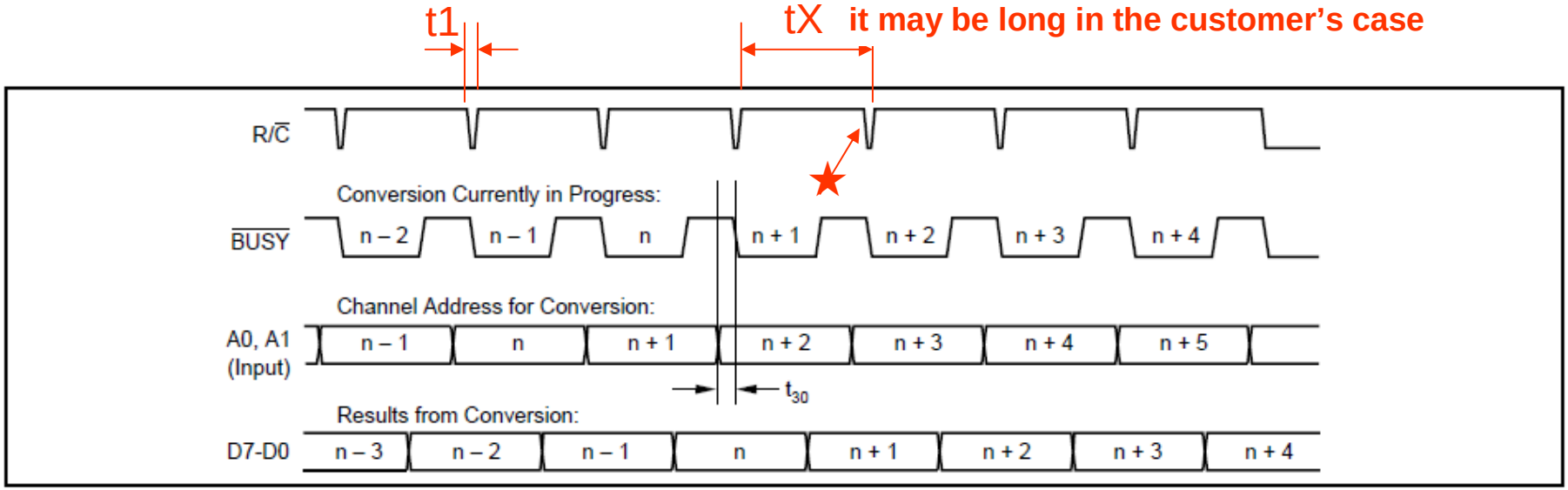
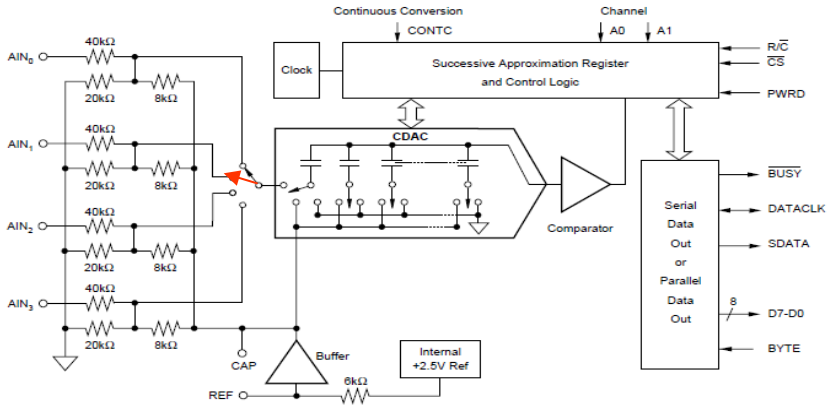


FIGURE 8. Channel Addressing in Normal Conversion Mode (CONTC and $\overline{CS}$ LOW).



Problem:

After conversion of Ain1, and then there are delay to next Ain2 channel conversion due to MCU setting .
(i.e. next R/C rise/fall is delay)