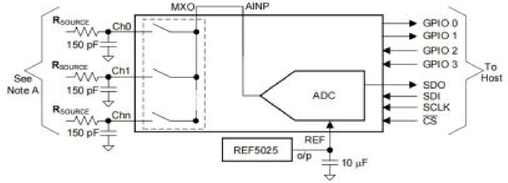
MXO 和 AIN 兼容两种方案：

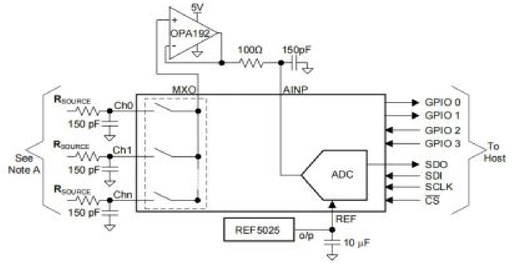
1. MXO 和 AIN 短接，默认料单：



A. A restriction on the source impedance exists. R_{SOURCE} ≤ 100 Ω for the 1xV_{REF} 12-bit settling at 1 MSPS or R_{SOURCE} ≤ 250 Ω for the 2xV_{REF} 12-bit settling at 1 MSPS.

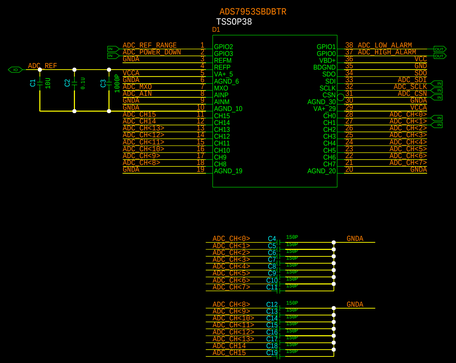
Figure 63. Application Diagram for an Unbuffered MXO

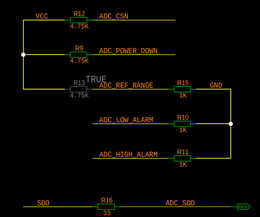
2. MXO 和 AIN 间增加运放，料单去掉 R14，增加 R18，R20，C27：

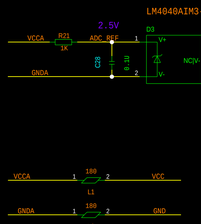


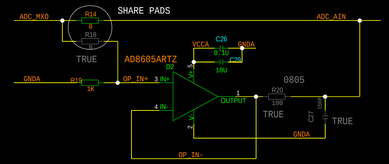
A. Restriction on the source impedance exists. $R_{(SOURCE)}$ ≤ 500 Ω for a 12-bit settling at 1 MSPS with both 1xV_{REF} and 2xV_{REF} ranges.

Figure 66. Application Diagram for an OPA192 Buffered MXO

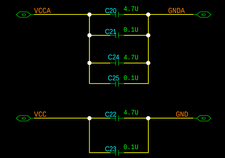








4.7U AND 0.1U CAPACITORS ARE PUTTED AT EACH VCCA PIN



注意：

1. VCC=3.3V，VCCA=3.3V，AIN Range=1xVREF；
2. 上电时序建议：VCCA>=VCC。

	MIN	MAX	UNIT
AINP or CHn to AGND	–0.3	VA +0.3	V

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DRAWING

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ZTE CORPORATION	MODEL:	SHEET 1 OF 2	
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