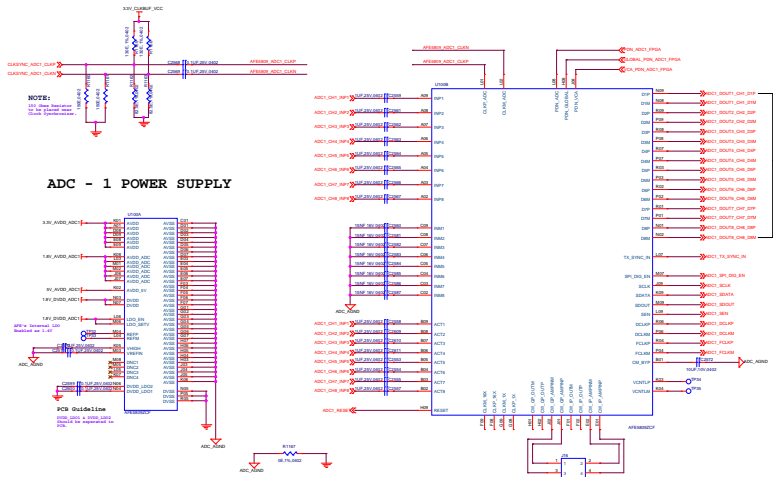


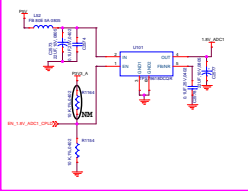
AFE5809 ADC - 1



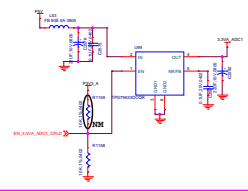
CAD NOTE:
DIP_M to
DIP_M signals
must be
routed
differential &
Length
Matched.

ADC1 SUPPLY SECTION

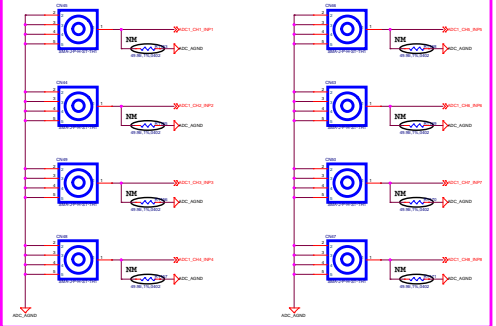
+1.8V LDO SUPPLY FOR ADC - 1
0.315A Required



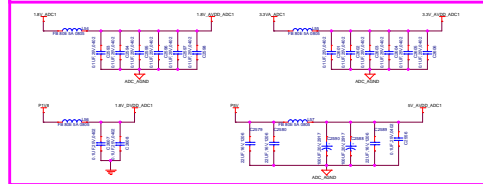
+3.3V LDO SUPPLY FOR ADC - 1
0.375A Required



ADC - 1 INPUT FROM SMA CONNECTOR



AFE5809 ADC - 1 DECOUPLING



KINTEX-7 FPGA SECTION

