



Ch\_0 DAC\_ALARM  
 Ch\_1 DAC\_LATCH  
 Ch\_2 SPI1\_SCK  
 Ch\_3 SPI1\_MOSI  
 Ch\_4 SPI1\_MISO  
 Ch\_5 ~EEPROM\_CS

Here Alarm Triggers about 4 ms after EEPROM Write Byte function has completed and DAC8760 read status register command (last two frames)

The DAC Status register returns 0x00 meaning no ALARM yet!

Normally EEPROM write process takes between 10 - 12.5 ms

There is no activity on the DAC\_LATCH line prior to ALARM triggers for at least 4ms.