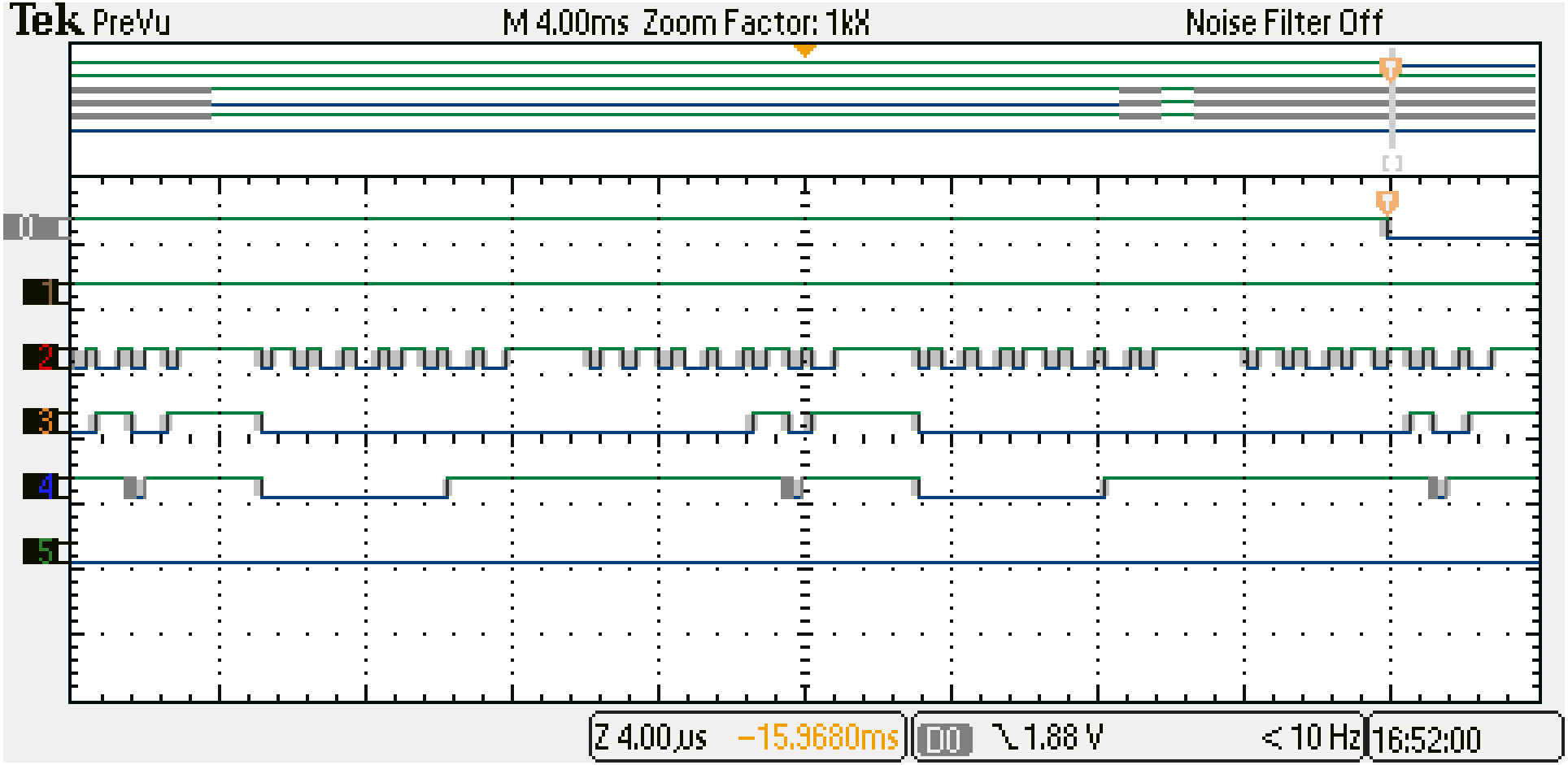

Ch_0	DAC_ALARM
Ch_1	DAC_LATCH
Ch_2	SPI1_SCK
Ch_3	SPI1_MOSI
Ch_4	SPI1_MISO
Ch_5	~EEPROM_CS

Here Alarm Triggers in the midle of EEPROM Write Byte function

In order to find out if Write process is completed CPU reads EEPROM status register (code 0x05) in the loop until it gets 0x00

Normally EEPROM write process takes between 10 - 12.5 ms

There is no activity on the DAC_LATCH line prior to ALARM triggers for at least 36 ms. When ALARMS triggers the EE write process continue without any problem