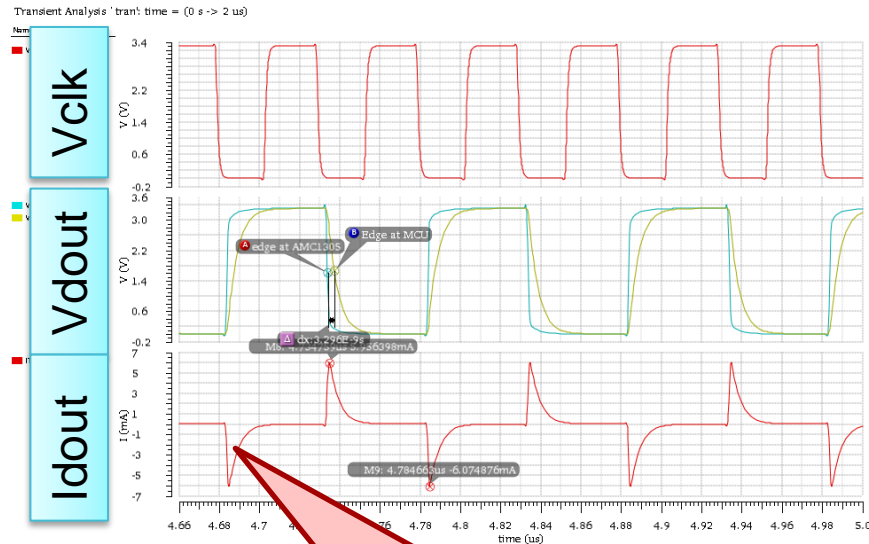
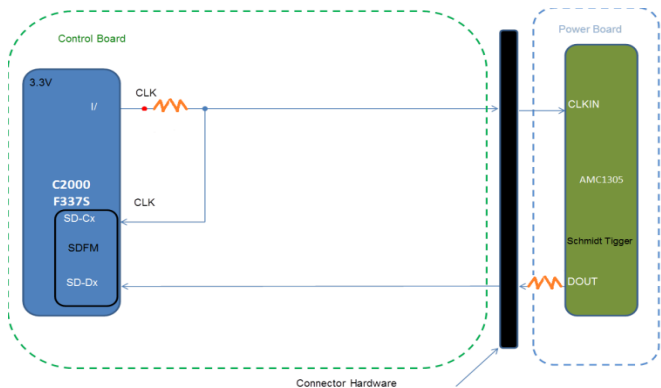


# Design Recommendation

- Effective EMI filtering of digital lines should be implemented by adding a series Resistor in CLK and DOUT lines and adjusting capacitance to achieve desired filtering.
  - PCB trace capacitance may be sufficient
  - Load of DOUT doesn't exceed ABSMAX



Current Peaks ~6mA  
@every DOUT toggle  
(400ohm + 10pF)

# TINA can also be used for Simulation



- Adjust R and C values to provide desired MCLK and DOUT edge rounding.
  - PCB trace capacitance can be supplemented with added capacitor to GND.
  - Series R on DOUT reduces transient loading on drive stage.

