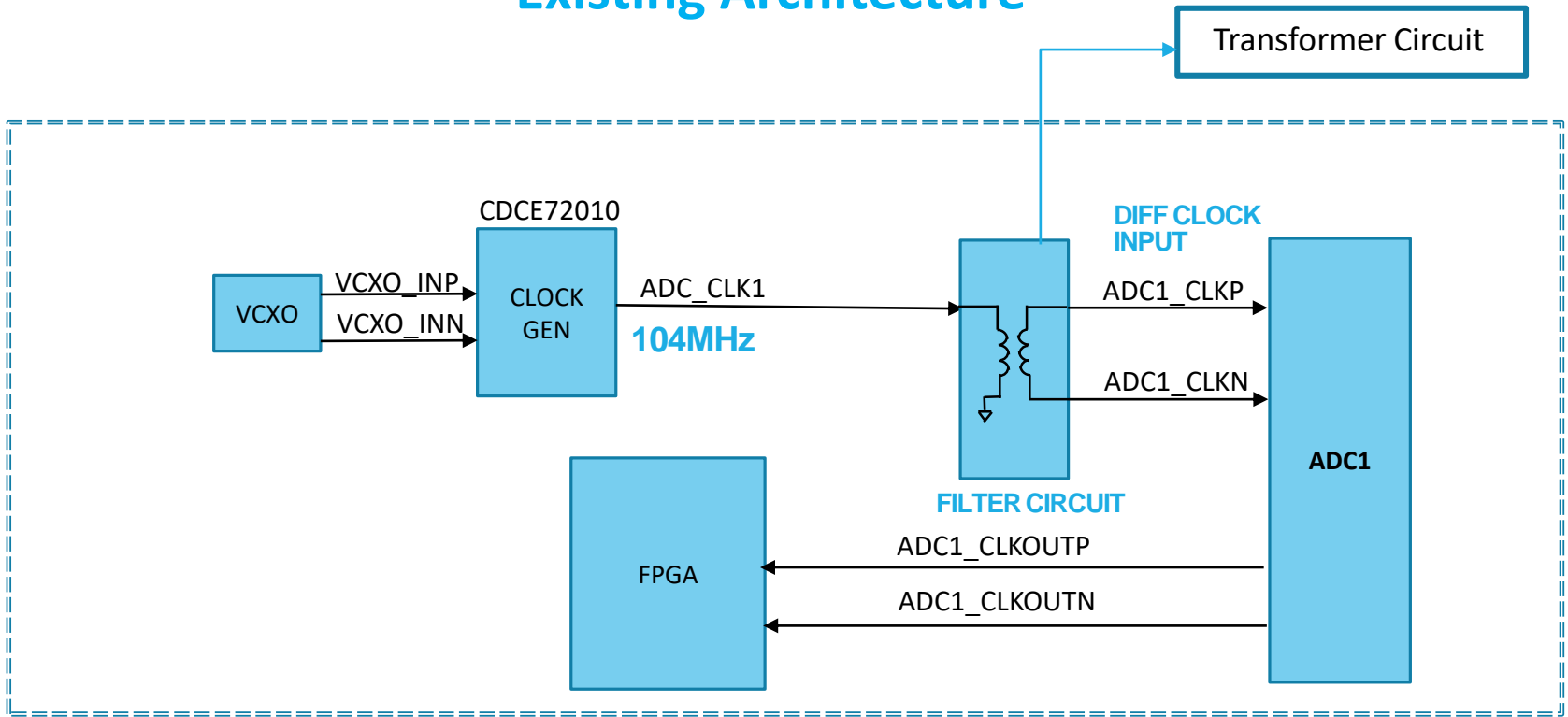


Existing Architecture



Planned Architecture

FPGA - ADC backplane connectors

