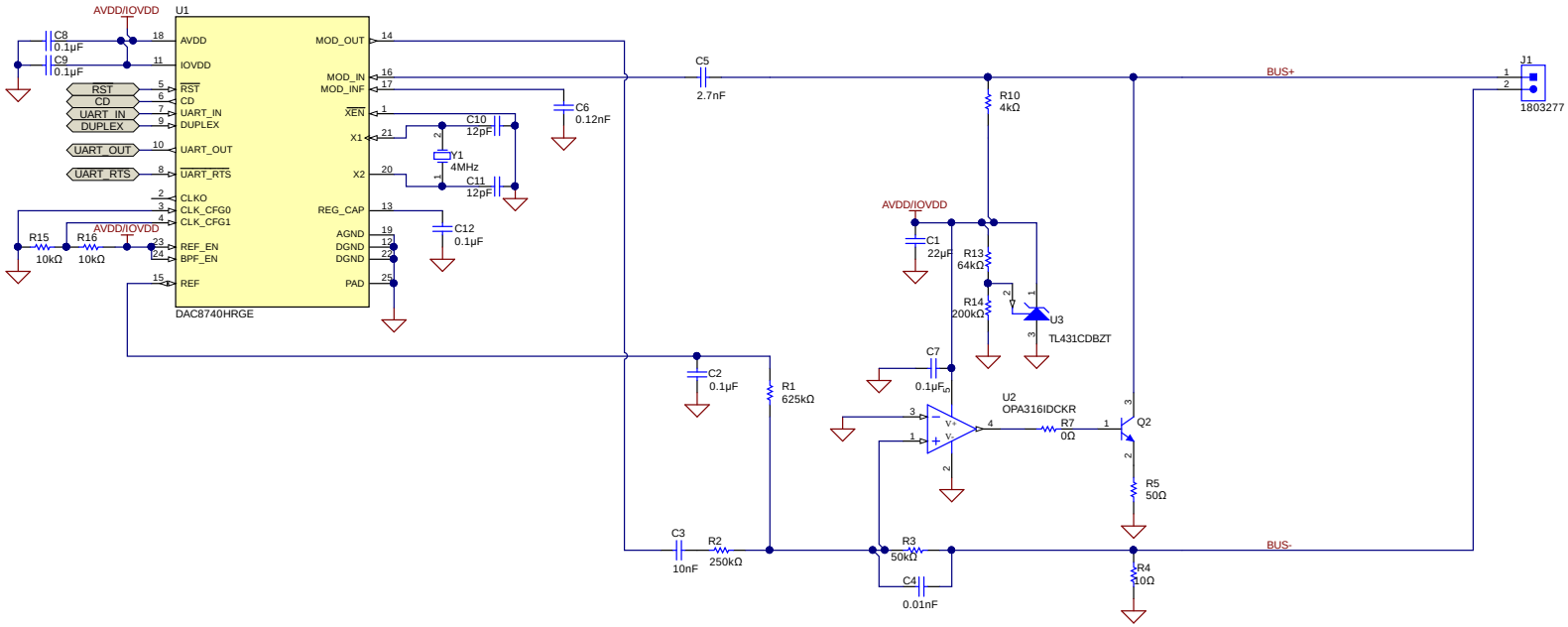

Orderable: EVM_orderable	Designed for:Example	Mod. Date: 11/23/2016
TID #: N/A	Project Title: DAC8740H-PAFF Discrete Loop Powered Transmi	
Number: DAC8740H-PAFF Rev: E1	Sheet Title:	
EVM Rev: Version control disabled	Assembly Variant:[No Variations]	Sheet:2 of 3
Drawn By:	File: DAC8740H_PAFF.SchDoc	Size: B
Engineer: FROST	Contact: http://www.ti.com/support	http://www.ti.com

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