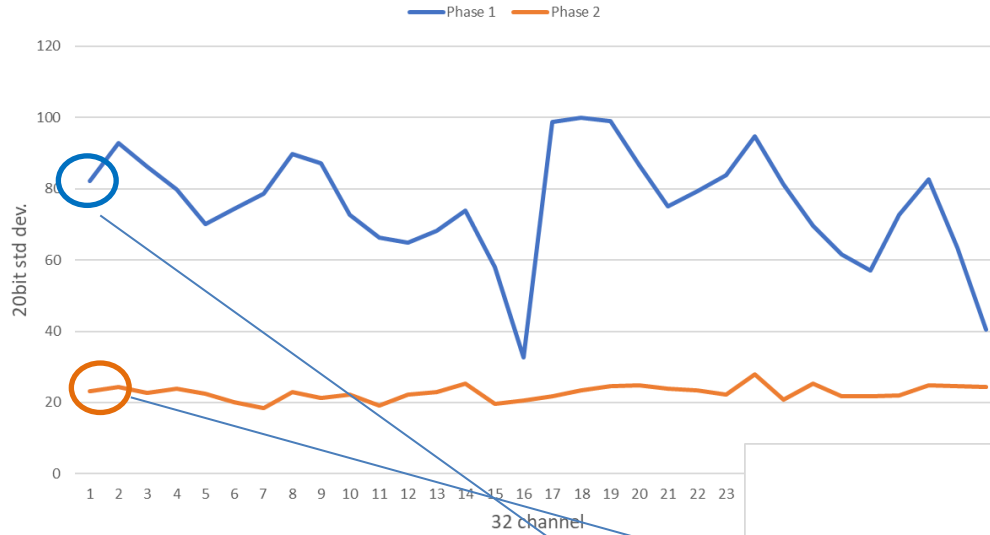


DDC232CK
Non continuous mode



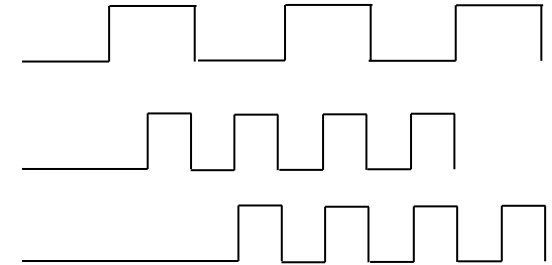
ダーク 20bit標準偏差(12.5pC,100us)



CLK

DCLK phase1

DCLK phase2



Ch1 output

