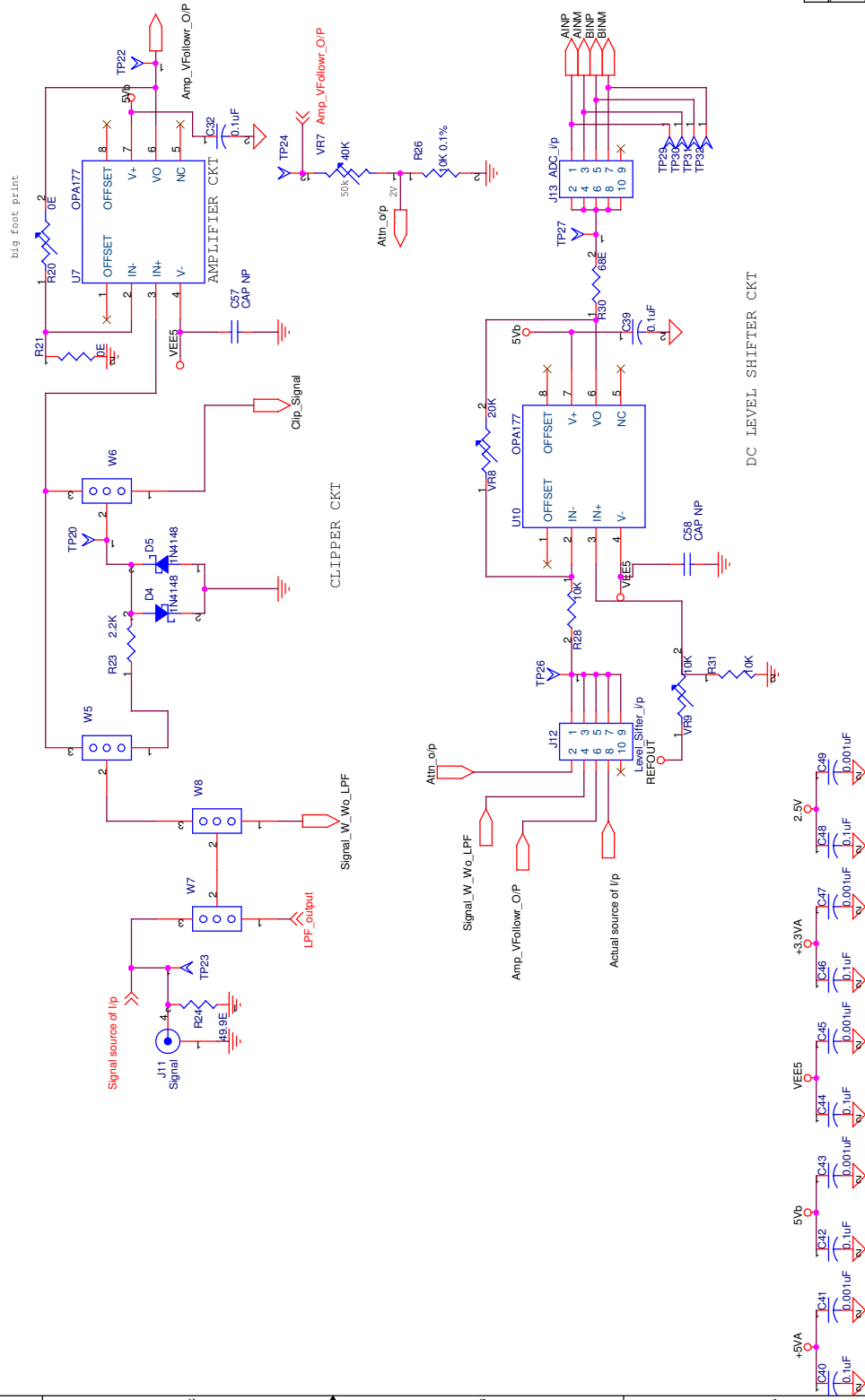




1. Actual source of signal contains a mixed signal of +/- 10V pickup pulse and a -300mV actual signal. This pickup pulse is unwanted signal so it is clipped to 1V with clipper circuit.
2. Now clipper output contains +/-1V pickup pulse and -300mV actual signal. Amplifier ckt amplifies the actual signal and pickup pulse with a gain of 8.
3. Amplified output is applied to potential divider. Potential divider is adjusted to set the Level shifter input to 2V @ Attn o/p. Attn o/p consists of both 2V pickup pulse and 0.8V amp actual signal.
4. J12 (1,2) jumper is put to apply this mixed signal input to level shifter. VR8 and VR9 are adjusted to level shift the signal to 1.5 to 3.5 V and is applied to one of the ADC analog input channels.



DECOUPLING CAPACITORS

TI\_ADC\_Circuit

Document Number

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Rev	4