

```

SPIWrite 0018,20,0,7 //macro=0x1;      Address(0x18[7:5])
SPIRead 00f0,0,0

//Read      MACRO_READY=0x1;      Address(0xf0[7:0])

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7 //MACRO_OPERAND_REG0=0x10002;
      Address(0xa0[7:0],0xa1[7:0],0xa2[7:0],0xa3[7:0],0xa4[7:0])
SPIWrite 00a2,01,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,02,0,7
SPIWrite 00a7,00,0,7 //MACRO_OPERAND_REG1=0x1;
      Address(0xa4[7:0],0xa5[7:0],0xa6[7:0],0xa7[7:0],0xa8[7:0])
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,01,0,7
SPIWrite 0193,a0,0,7 //MACRO_OPCODE=0xa0;
      Address(0x193[7:0],0x194[7:0])

WAIT 0.001
SPIRead 00f0,2,2

//Read      MACRO_DONE=0x1; Address(0xf0[7:2])

SPIPoll 00f0,2,2,4

SPIReadCheck 00f0,3,3,00

//Read      MACRO_ERROR=0x0;      Address(0xf0[7:3])

SPIRead 00f1,0,7

//Read      MACRO_ERROR_OPCODE=0x0;
      Address(0xf1[7:0],0xf2[7:0])

SPIRead 00f0,4,4

//Read      MACRO_ERROR_IN_OPCODE=0x0; Address(0xf0[7:4])

SPIRead 00f0,5,5

//Read      MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;
      Address(0xf0[7:5])

SPIRead 00f0,6,6

//Read      MACRO_ERROR_IN_OPERAND=0x0;      Address(0xf0[7:6])

SPIRead 00f0,7,7

```

```

//Read      MACRO_ERROR_IN_EXECUTION=0x0;      Address(0xf0[7:7])

SPIRead 00f3,0,7
SPIRead 00f2,0,7

//Read      MACRO_ERROR_EXTENDED_CODE=0x0;
            Address(0xf2[7:0],0xf3[7:0],0xf4[7:0])

SPIRead 00f7,0,7
SPIRead 00f6,0,7
SPIRead 00f5,0,7
SPIRead 00f4,0,7

//Read      MACRO_ERROR_EXTENDED_CODE_2=0x0;
            Address(0xf4[7:0],0xf5[7:0],0xf6[7:0],0xf7[7:0],0xf8[7:0])

WAIT 1
SPIRead 00f0,0,0

//Read      MACRO_READY=0x1;      Address(0xf0[7:0])

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7 //MACRO_OPERAND_REG0=0x10002;
            Address(0xa0[7:0],0xa1[7:0],0xa2[7:0],0xa3[7:0],0xa4[7:0])
SPIWrite 00a2,01,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,02,0,7
SPIWrite 00a7,00,0,7 //MACRO_OPERAND_REG1=0x101;
            Address(0xa4[7:0],0xa5[7:0],0xa6[7:0],0xa7[7:0],0xa8[7:0])
SPIWrite 00a6,00,0,7
SPIWrite 00a5,01,0,7
SPIWrite 00a4,01,0,7
SPIWrite 0193,a0,0,7 //MACRO_OPCODE=0xa0;
            Address(0x193[7:0],0x194[7:0])

WAIT 0.001
SPIRead 00f0,2,2

//Read      MACRO_DONE=0x1; Address(0xf0[7:2])

SPIPoll 00f0,2,2,4

SPIReadCheck 00f0,3,3,00

//Read      MACRO_ERROR=0x0;      Address(0xf0[7:3])

SPIRead 00f1,0,7

```

```

//Read      MACRO_ERROR_OPCODE=0x0;
             Address(0xf1[7:0],0xf2[7:0])

SPIRead 00f0,4,4

//Read      MACRO_ERROR_IN_OPCODE=0x0; Address(0xf0[7:4])

SPIRead 00f0,5,5

//Read      MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;
             Address(0xf0[7:5])

SPIRead 00f0,6,6

//Read      MACRO_ERROR_IN_OPERAND=0x0;      Address(0xf0[7:6])

SPIRead 00f0,7,7

//Read      MACRO_ERROR_IN_EXECUTION=0x0;      Address(0xf0[7:7])

SPIRead 00f3,0,7
SPIRead 00f2,0,7

//Read      MACRO_ERROR_EXTENDED_CODE=0x0;
             Address(0xf2[7:0],0xf3[7:0],0xf4[7:0])

SPIRead 00f7,0,7
SPIRead 00f6,0,7
SPIRead 00f5,0,7
SPIRead 00f4,0,7

//Read      MACRO_ERROR_EXTENDED_CODE_2=0x0;
             Address(0xf4[7:0],0xf5[7:0],0xf6[7:0],0xf7[7:0],0xf8[7:0])

SPIWrite 0018,00,0,7 //macro=0x0;      Address(0x18[7:5])

```