

lead I configuration

```
0 Write register opcode1
    0x0041,
1. Write register opcode2
    0x0018,
2. CONFIG1 0x0001
    0x00A6, // 0x00A6 for 500 sps 0x00A5 for 1K sps
3. CONFIG2
    0x0010,
4. CONFIG3
    0x00EC, ///0x00DC, 4V ref
5. LOFF
    0x004F,
6. CH1SET (PGA gain = 6/4)
    ((0x0000 & 0x8F) | PGA_GAIN_BITS ),
7. CH2SET (PGA gain = 6/4)
    ((0x0000 & 0x8F) | PGA_GAIN_BITS ),
8. CH3SET (PGA gain = 6/4)
    ((0x0000 & 0x8F) | PGA_GAIN_BITS ),
9. CH4SET (PGA gain = 6/4)
    ((0x0006 & 0x8F) | PGA_GAIN_BITS ),
10. CH5SET (PGA gain = 6/4)
    ((0x0081 & 0x8F) | PGA_GAIN_BITS ),
11. CH6SET (PGA gain = 6/4)
    ((0x0081 & 0x8F) | PGA_GAIN_BITS ),
12. CH7SET (PGA gain = 6/4)
    ((0x0000 & 0x8F) | PGA_GAIN_BITS ),
13. CH8SET (PGA gain = 6/4)
    ((0x0000 & 0x8F) | PGA_GAIN_BITS ),
14. RLDSENSP (default)
    0x0002,
15. RLDSENSN (default)
    0x0002,
16. LOFF_SENSP
    0x001A,
17. LOFF_SENSN,
    0x0004,
18. LOFF_FLIP (default)
    0x0004,
19. LOFF_STATP (Read only)
    0x0000,
```

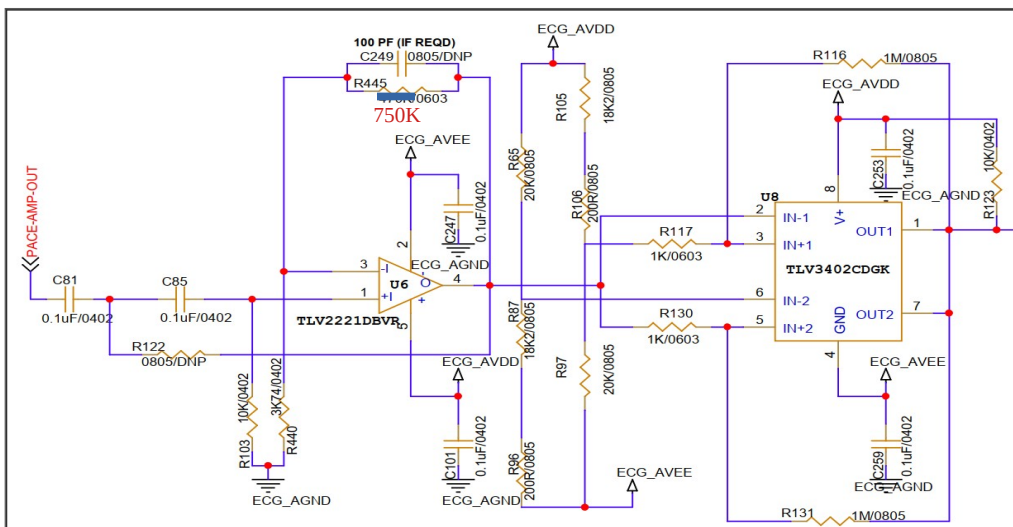
- 20. LOFF_STATN (Read only)
0x0000,
- 21. GPIO
0x000F,
- 22. PACE (default)
0x0005,
- 23. RESP (default)
0x00F2,
- 24. CONFIG4
0x0022,
- 25. WCT1
0x000C,
- 26. WCT2
0x00D3,

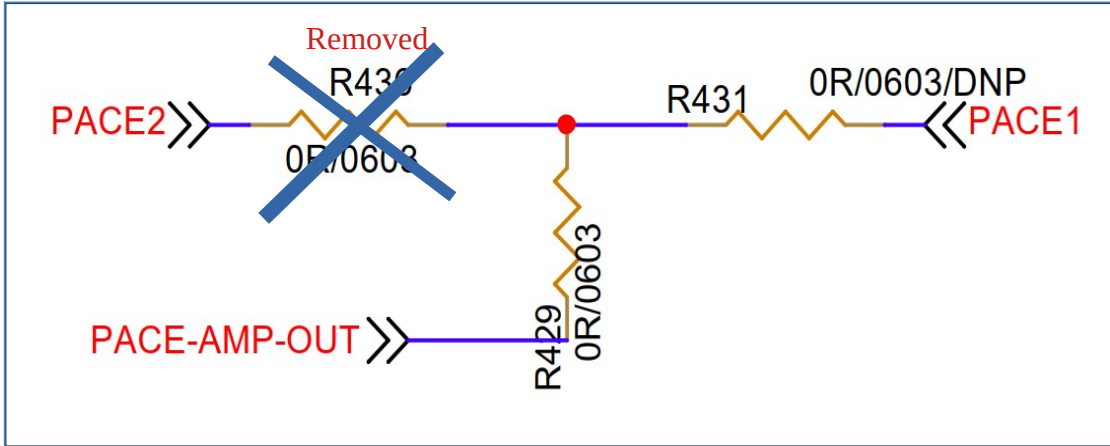
lead III configuration

- 0 Write register opcode1
0x0041,
- 1. Write register opcode2
0x0018,
- 2. CONFIG1 0x0001
0x00A6, for 500 sps and 0x00A5 for 1K sps
- 3. CONFIG2
0x0010,
- 4. CONFIG3
0x00EC, ///0x00DC, 4V ref
- 5. LOFF
0x004F,
- 6. CH1SET (PGA gain = 6/4)
((0x0000 & 0x8F) | PGA_GAIN_BITS),
- 7. CH2SET (PGA gain = 6/4)
((0x0000 & 0x8F) | PGA_GAIN_BITS),
- 8. CH3SET (PGA gain = 6/4)
((0x0007 & 0x8F) | PGA_GAIN_BITS),
- 9. CH4SET (PGA gain = 6/4)
((0x0000 & 0x8F) | PGA_GAIN_BITS),
- 10. CH5SET (PGA gain = 6/4)
((0x0081 & 0x8F) | PGA_GAIN_BITS),
- 11. CH6SET (PGA gain = 6/4)
((0x0081 & 0x8F) | PGA_GAIN_BITS),

12. CH7SET (PGA gain = 6/4)
((0x0000 & 0x8F) | PGA_GAIN_BITS),
13. CH8SET (PGA gain = 6/4)
((0x0000 & 0x8F) | PGA_GAIN_BITS),
14. RLDSENSP (default)
0x0008,
15. RLDSENSN (default)
0x0008,
16. LOFF_SENSP
0x001A,
17. LOFF_SENSN,
0x0004,
18. LOFF_FLIP (default)
0x0004,
19. LOFF_STATP (Read only)
0x0000,
20. LOFF_STATN (Read only)
0x0000,
21. GPIO
0x000F,
22. PACE (default)
0x000D,
23. RESP (default)
0x00F2,
24. CONFIG4
0x0022,
25. WCT1
0x000C,
26. WCT2
0x00D3,

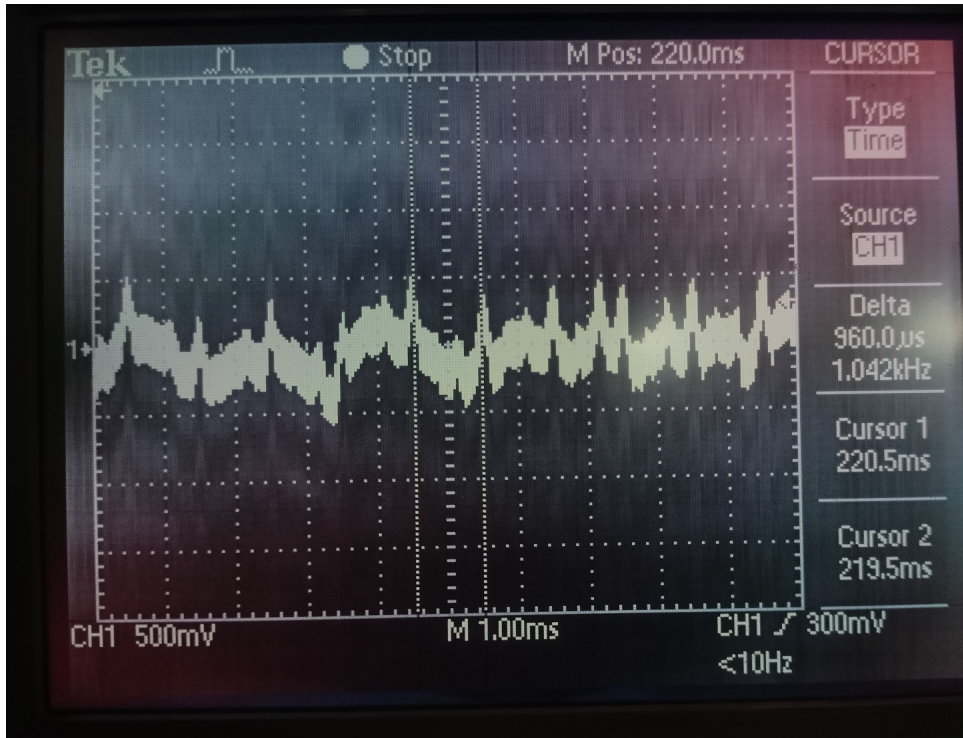
NOTE:- **PGA_GAIN_BITS = 4**



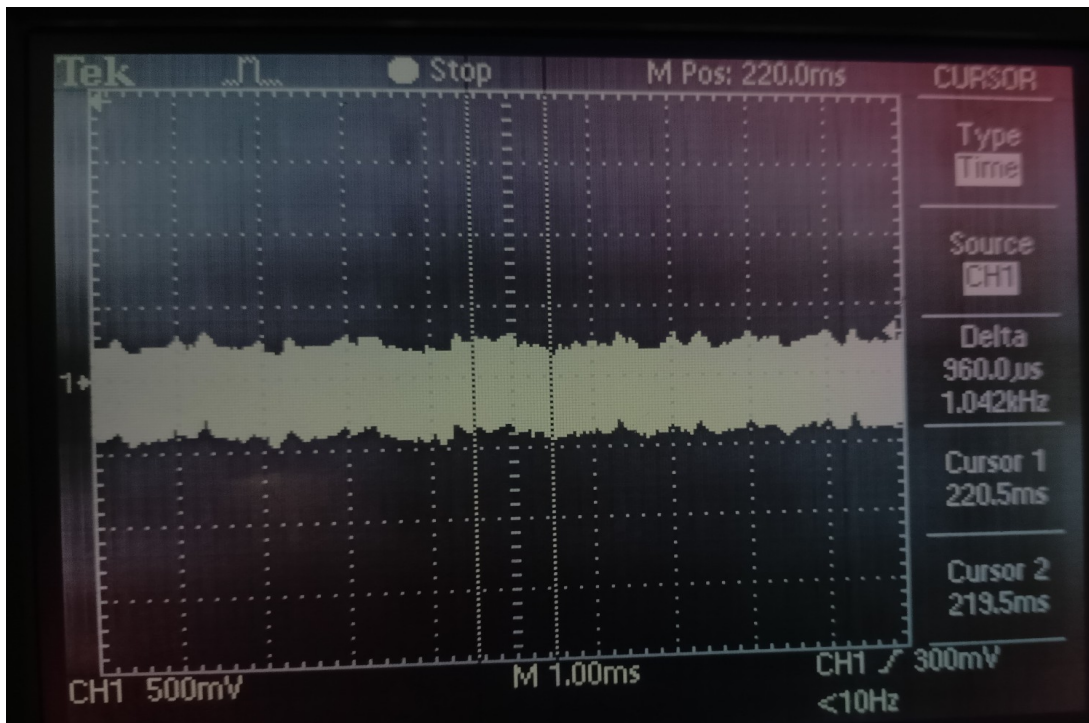


Images :

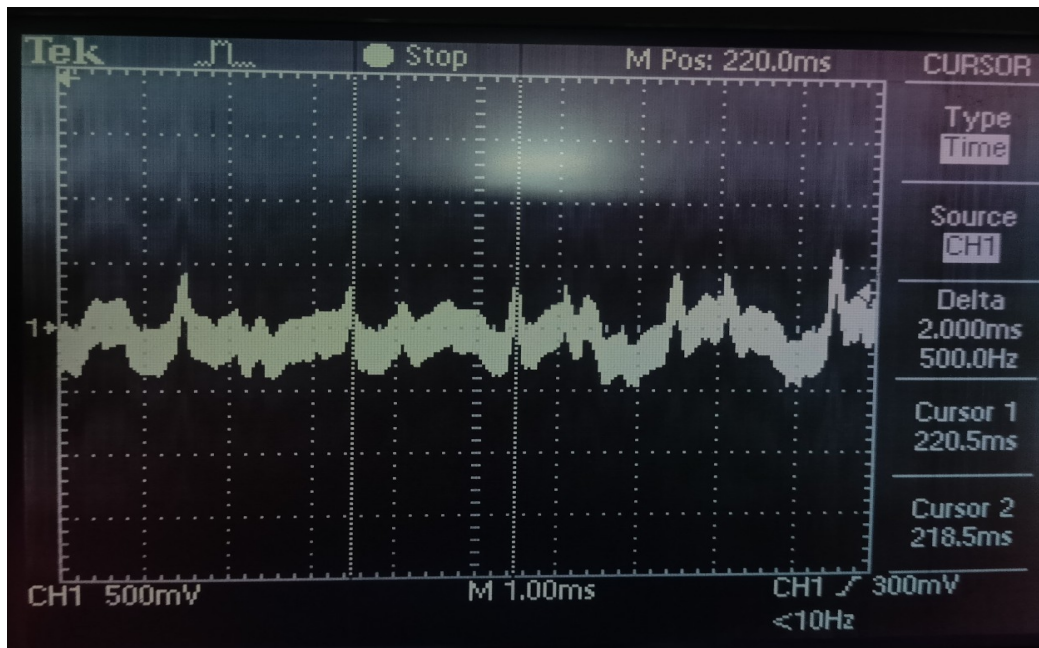
1) 1K SPS lead III (CH4)



2) 1K SPS lead I (CH2) :



3) 500 SPS lead III (CH4) :



3) 500 SPS lead I (CH2) :

