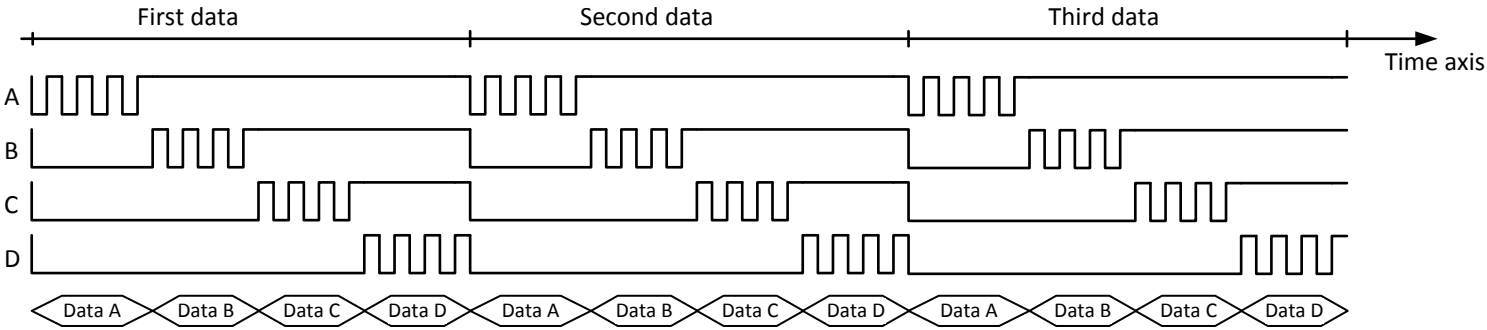


Conv_Clk timings of ADCs A,B,C and D



Conv_Clk and Read signal

