

AFE20408EVM

File Debug Help

Write Selected Write Modified Read Selected Read All Update Mode Immediate

Passes

- High Level Configuration
- AFE20408 DACs
- AFE20408 ADCs
- Low Level Configuration

Register Map

Register Name	Address	Default	Mode	Size	Value	15	14	13	12	11	10	9	8
GLOBAL													
NOP	0x00	0x0000	R/W	16	0x0000	0	0	0	0	0	0	0	0
PAGE	0x01	0x0000	R/W	16	0x0000	0	0	0	0	0	0	0	0
GEN_STATUS	0x03	0x4000	R	16	0x4000	1	0	0	0	0	0	0	0
ALARM_STATUS_0	0x04	0x0000	R	16	0x0000	0	0	0	0	0	0	0	0
ALARM_STATUS_1	0x05	0x0000	R	16	0x0000	0	0	0	0	0	0	0	0
PWR_STATUS_0	0x06	0x0001	R	16	0x0301	0	0	0	0	0	0	1	1
PWR_STATUS_1	0x07	0x0000	R	16	0x0000	0	0	0	0	0	0	0	0
PWR_EN	0x08	0x0000	R/W	16	0x0200	0	0	0	0	0	0	1	0
TRIGGER	0x10	0x0000	R/W	16	0x0000	0	0	0	0	0	0	0	0
GPIO_DATA	0x11	0x0001	R/W	16	0x0001	0	0	0	0	0	0	0	0
DRVEN_SW_EN	0x12	0x00FF	R/W	16	0x00FF	0	0	0	0	0	0	0	0
DRVEN	0x13	0x0000	R/W	16	0x0000	0	0	0	0	0	0	0	0
DAC_BCAST	0x14	0x0000	R/W	16	0x0000	0	0	0	0	0	0	0	0
GLOBAL_CFG	0x17	0x0000	R/W	16	0x0000	0	0	0	0	0	0	0	0
ADC_SENSE0	0x18	0x0000	R	16	0x0000	0	0	0	0	0	0	0	0
ADC_SENSE1	0x19	0x0000	R	16	0x0000	0	0	0	0	0	0	0	0
ADC_ADC0	0x1A	0x0000	R	16	0x0000	0	0	0	0	0	0	0	0
ADC_ADC1	0x1B	0x0000	R	16	0x0000	0	0	0	0	0	0	0	0
ADC_TMP	0x1C	0x0000	R	16	0x0000	0	0	0	0	0	0	0	0
GEN_CONFIG													
CHIP_ID	0x40	0x7908	R	16	0x2480	0	1	0	0	1	0	0	0
CHIP_VER	0x41	0x0000	R	16	0x0000	0	0	0	0	0	0	0	0
SDO_EN	0x42	0x0000	R/W	16	0x0001	0	0	0	0	0	0	0	0
GEN_CFG_0	0x44	0x0010	R/W	16	0x0010	0	0	0	0	0	0	0	0
GEN_CFG_1	0x45	0x1101	R/W	16	0x1101	0	0	1	0	0	0	0	1
ALARMOUT_SRC_0	0x48	0x0000	R/W	16	0x0000	0	0	0	0	0	0	0	0
ALARMOUT_SRC_1	0x49	0x1833	R/W	16	0x1833	0	0	1	1	0	0	0	0
ALARM_STATUS_0_E	0x4C	0x0000	R/W	16	0x0000	0	0	0	0	0	0	0	0
ALARM_STATUS_1_E	0x4D	0x0000	R/W	16	0x0000	0	0	0	0	0	0	0	0
PAON_SRC_0	0x50	0x0000	R/W	16	0x0000	0	0	0	0	0	0	0	0

Field View

Field Name	Field Value
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Register Description

Idle

CONNECTED TEXAS INSTRUMENTS

AFE20408EVM

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AFE20408 DACs

DACA0

DRVEN_DAC0 DRVEN Source Power Enable

☒ DRVEN_SW_EN_DAC0 ☒ PDAC0

☐ DRVEN0_EN_DAC0 ☐ DAC0_BUFFER

☐ DRVEN1_EN_DAC0 ☐ *0000

☐ FLEXIO_EN_DAC0

CLAMP_SEL_OUTA0

VSSA

Note: If DACA1 is used as OUTA0 CLAMP voltage, DACA1 does not respond to DRVEN functions.

DACA1

DRVEN_DAC1 DRVEN Source Power Enable

☒ DRVEN_SW_EN_DAC1 ☒ PDAC1

☐ DRVEN0_EN_DAC1 ☐ DAC1_BUFFER

☐ DRVEN1_EN_DAC1 ☐ *0000

☐ FLEXIO_EN_DAC1

CLAMP_SEL_OUTA1

VSSA

Note: If DACA2 is used as OUTA1 CLAMP voltage, DACA2 does not respond to DRVEN functions.

DACA2

DRVEN_DAC2 DRVEN Source Power Enable

☒ DRVEN_SW_EN_DAC2 ☒ PDAC2

☐ DRVEN0_EN_DAC2 ☐ DAC2_BUFFER

☐ DRVEN1_EN_DAC2 ☐ *0000

☐ FLEXIO_EN_DAC2

CLAMP_SEL_OUTA2

VSSA

Note: If DACA3 is used as OUTA2 CLAMP voltage, DACA3 does not respond to DRVEN functions.

DACA3

DRVEN_DAC3 DRVEN Source Power Enable

☒ DRVEN_SW_EN_DAC3 ☒ PDAC3

☐ DRVEN0_EN_DAC3 ☐ DAC3_BUFFER

☐ DRVEN1_EN_DAC3 ☐ *0000

☐ FLEXIO_EN_DAC3

CLAMP_SEL_OUTA3

VSSA

Note: If DACA4 is used as OUTA3 CLAMP voltage, DACA4 does not respond to DRVEN functions.

DRVENx HW Input Pins

DRVEN0 DRVEN1

☒ HIGH ☒ HIGH

PAON

OFF

AFE20408 ADCs

DACB0

DRVEN_DACB0 DRVEN Source Power Enable

☒ DRVEN_SW_EN_DACB0 ☒ PDACB0

☐ DRVEN0_EN_DACB0 ☐ DACB0_BUFFER

☐ DRVEN1_EN_DACB0 ☐ *0000

☐ FLEXIO_EN_DACB0

CLAMP_SEL_OUTB0

VSSA

Note: If DACB1 is used as OUTB0 CLAMP voltage, DACB1 does not respond to DRVEN functions.

DACB1

DRVEN_DACB1 DRVEN Source Power Enable

☒ DRVEN_SW_EN_DACB1 ☒ PDACB1

☐ DRVEN0_EN_DACB1 ☐ DACB1_BUFFER

☐ DRVEN1_EN_DACB1 ☐ *0000

☐ FLEXIO_EN_DACB1

CLAMP_SEL_OUTB1

VSSA

Note: If DACB2 is used as OUTB1 CLAMP voltage, DACB2 does not respond to DRVEN functions.

DACB2

DRVEN_DACB2 DRVEN Source Power Enable

☒ DRVEN_SW_EN_DACB2 ☒ PDACB2

☐ DRVEN0_EN_DACB2 ☐ DACB2_BUFFER

☐ DRVEN1_EN_DACB2 ☐ *0000

☐ FLEXIO_EN_DACB2

CLAMP_SEL_OUTB2

VSSA

Note: If DACB3 is used as OUTB2 CLAMP voltage, DACB3 does not respond to DRVEN functions.

DACB3

DRVEN_DACB3 DRVEN Source Power Enable

☒ DRVEN_SW_EN_DACB3 ☒ PDACB3

☐ DRVEN0_EN_DACB3 ☐ DACB3_BUFFER

☐ DRVEN1_EN_DACB3 ☐ *0000

☐ FLEXIO_EN_DACB3

CLAMP_SEL_OUTB3

VSSA

Note: If DACB4 is used as OUTB3 CLAMP voltage, DACB4 does not respond to DRVEN functions.

DRVENx HW Input Pins

DRVEN0 DRVEN1

☒ HIGH ☒ HIGH

PAON

OFF

PAON_STS VDDI_STS READ STATUS

GA LR NVM_CRC_ERR GTHERM_ALR

GALARMIN_ALR GADC_ALR

GTMP_ALR GSENSE_ALR

GREF_ALR GVCCVSS_ALR

VCCB_STS VCCB_ALR VSSB_ALR

High Range 2 Mid Range 2 Low Range 2

Idle

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