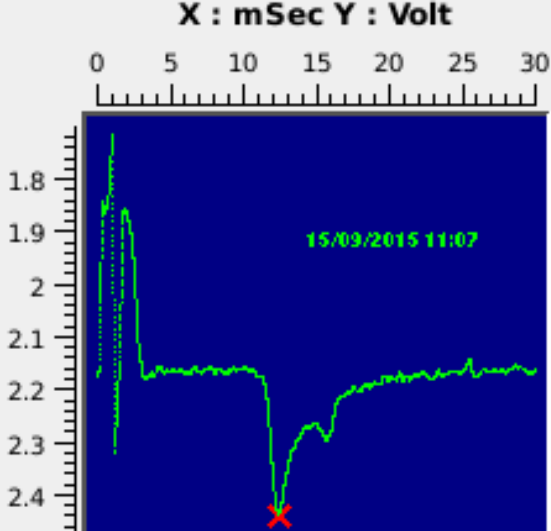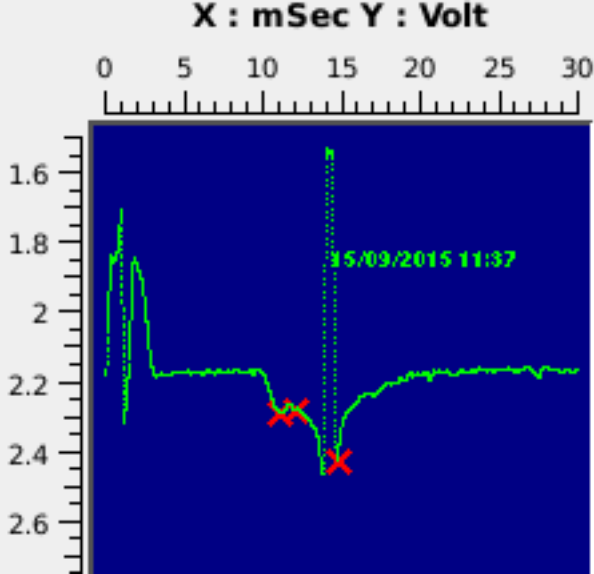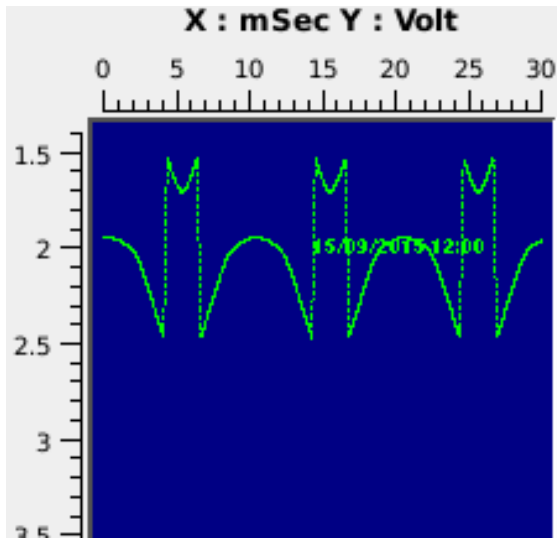Still what I feel is ADC is not functioning in full scale even after bypassing the U7 (amplifier). ADC is properly converting data when input signal is between 1.5v and 2.5v as shown below.

Today when actual signal with 525mv signal is applied waveform displayed is as shown below. Input voltage beyond 2.5v is converted as 1.5v+ and displayed accordingly.

To confirm, we have applied 1.05v signal from function generator. In that case also waveform is displayed in the clipped form at 2.5v and clipped portion is displayed from 1.5v onwards as shown below.

Can we conclude ADC has gone bad or some changes need to be done to the ADC interface board?

Please suggest us.

Earlier I am saying about this display of dotted lines as "intermittent captured data is not smooth".

We are very much grateful, if we get a solution for this problem.