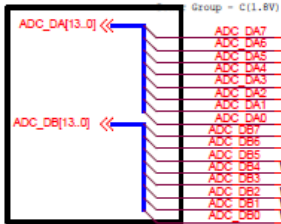


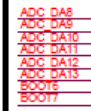
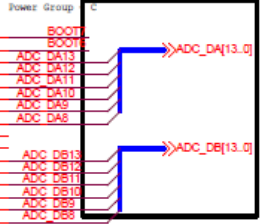
U68

TMS320C6746E2WTX



VPIF DIN15_VSYN(UHPI_HD7/UPP_CH1_D7
 VPIF DIN14_HSYN(UHPI_HD6/UPP_CH1_D6
 VPIF DIN13_FIELD(UHPI_HD5/UPP_CH1_D5
 VPIF DIN12_UHPI_HD4/UPP_CH1_D4
 VPIF DIN11_UHPI_HD3/UPP_CH1_D3
 VPIF DIN10_UHPI_HD2/UPP_CH1_D2
 VPIF DIN9_UHPI_HD1/UPP_CH1_D1
 VPIF DIN8_UHPI_HD0/UPP_CH1_D0/GPIO[8]
 VPIF DIN7_UHPI_HD15/UPP_CH1_D15/RMII_TXD[1]
 VPIF DIN6_UHPI_HD14/UPP_CH1_D14/RMII_TXD[0]
 VPIF DIN5_UHPI_HD13/UPP_CH1_D13/RMII_TXEN
 VPIF DIN4_UHPI_HD12/UPP_CH1_D12/RMII_RXD[1]
 VPIF DIN3_UHPI_HD11/UPP_CH1_D11/RMII_RXD[0]
 VPIF DIN2_UHPI_HD10/UPP_CH1_D10/RMII_RXER
 VPIF DIN1_UHPI_HD9/UPP_CH1_D9/RMII_MFZ_50_CLK
 VPIF DIN0_UHPI_HD8/UPP_CH1_D8/RMII_CRS_DV

VPIF DOUT15_LCD_D_15/UPP_XD7/GPIO[7]@BOOT7
 VPIF DOUT14_LCD_D_14/UPP_XD6/GPIO[6]@BOOT6
 VPIF DOUT13_LCD_D_13/UPP_XD5/GPIO[5]@BOOT5
 VPIF DOUT12_LCD_D_12/UPP_XD4/GPIO[4]@BOOT4
 VPIF DOUT11_LCD_D_11/UPP_XD3/GPIO[3]@BOOT3
 VPIF DOUT10_LCD_D_10/UPP_XD2/GPIO[2]@BOOT2
 VPIF DOUT9_LCD_D_9/UPP_XD1/GPIO[1]@BOOT1
 VPIF DOUT8_LCD_D_8/UPP_XD0/GPIO[0]@BOOT0
 VPIF DOUT7_LCD_D_7/UPP_XD15/GPIO[15]
 VPIF DOUT6_LCD_D_6/UPP_XD14/GPIO[14]
 VPIF DOUT5_LCD_D_5/UPP_XD13/GPIO[13]
 VPIF DOUT4_LCD_D_4/UPP_XD12/GPIO[12]
 VPIF DOUT3_LCD_D_3/UPP_XD11/GPIO[11]
 VPIF DOUT2_LCD_D_2/UPP_XD10/GPIO[10]
 VPIF DOUT1_LCD_D_1/UPP_XD9/GPIO[9]
 VPIF DOUT0_LCD_D_0/UPP_XD8/GPIO[8]
 MMCSD1_D5_LCD_HSYN(GPIO[8])
 MMCSD1_D4_LCD_VSYN(GPIO[8])
 MMCSD1_D5_LCD_MCLK(GPIO[8])
 MMCSD1_D7_LCD_PCLK(GPIO[11])
 LCD_AC_ENB_CSn(GPIO[0])



Power Group - C(1.8V)
 J3
 H3
 V15
 W16

VPIF CLKIN3_MMCSD1_D1/GPIO[2]
 VPIF CLKIN2_MMCSD1_D3/GPIO[4]
 VPIF CLKIN1_UHPI_HDS1n/GPIO[6]
 VPIF CLKIN0_UHPI_HCSn/GPIO[7]_UPP_2XTXCLK

VPIF CLK03/GPIO[1]
 VPIF CLK02/MMCSD1_D2/GPIO[3]
 MMCSD1_D0/UPP_CH0_CLK/GPIO[15]
 MMCSD1_CLK0/UPP_CH0_START/GPIO[14]
 MMCSD1_CMD/UPP_CH0_ENABLE/GPIO[13]
 UPP_CFD_WAIT/GPIO[12]
 UHPI_HCNT1/UPP_CH1_CLK/GPIO[11]
 UHPI_HCNT1/UPP_CH1_START/GPIO[10]
 UHPI_HRWL/UPP_CH1_ENABLE/GPIO[9]
 UHPI_HRWL/UPP_CH1_WAIT/GPIO[8]
 UHPI_HRWL/UPP_CH1_WAIT/GPIO[8]
 UHPI_HRDY/GPIO[13]

Power Group - C(1.8V)
 K4 LED1
 K3 LED2

Power Group - A(3.3V)
 D19
 C16
 C18
 C17
 C19
 D18
 D17
 E17
 E16
 D17

SPI0_CLK/EPWM0_AVGPIO[15]_MII_RXCLK
 SPI0_SOMI/EPWM0_SYNC/GPIO[6]
 SPI0_SIMO/EPWM0_SYNC/GPIO[6]
 SPI0_ENA/EPWM0_BMII_RXDV
 SPI0_SCSn_5/UART0_RXD/GPIO[4]
 SPI0_SCSn_4/UART0_TXD/GPIO[3]
 SPI0_SCSn_3/UART0_CTSn/GPIO[2]
 SPI0_SCSn_2/UART0_RTSn/GPIO[1]
 SPI0_SCSn_1/Timer64P0_OUT12/GPIO[17]
 SPI0_SCSn_0/Timer64P1_OUT12/GPIO[16]

AXR6/MCBSPO_CLKR0/GPIO[14]
 AXR5/MCBSPO_CLKX0/GPIO[13]
 AXR0/ECAP0_APWM0/GPIO[7]
 AXR3/MCBSPO_FSR0/GPIO[12]
 AXR2/MCBSPO_DR0/GPIO[10]
 AXR1/MCBSPO_DX0/GPIO[9]

Power Group - C(1.8V)
 G1
 G2
 J4
 T33
 U17
 W15
 U16
 T15
 R16
 R17

Power Group - A(3.3V)
 G19
 H17
 G17
 H16
 G16
 H15
 F17
 F16
 F18
 F19
 E19

SPI1_CLK/GPIO[13]
 SPI1_SOMI/GPIO[11]
 SPI1_SIMO/GPIO[10]
 SPI1_ENA/GPIO[12]
 SPI1_SCSn_7/2CD_SCL/Timer64P2_OUT12/GPIO[15]
 SPI1_SCSn_6/2CD_SDA/Timer64P3_OUT12/GPIO[14]
 SPI1_SCSn_5/UART2_RXD/2C1_SCL/GPIO[13]
 SPI1_SCSn_4/UART2_TXD/2C1_SDA/GPIO[12]
 SPI1_SCSn_3/UART1_RXD/SATA_LED/GPIO[11]
 SPI1_SCSn_2/UART1_TXD/SATA_CP_POD/GPIO[10]
 SPI1_SCSn_1/EPWM1_AVGPIO[15]_Timer64P2_IN12
 SPI1_SCSn_0/EPWM1_B/GPIO[14]_Timer64P3_IN12

AXR14/MCBSP1_CLKR1/GPIO[6]
 AXR13/MCBSP1_CLKX1/GPIO[5]
 AXR8/CLKS1/ECAP1_APWM1/GPIO[0]
 AXR12/MCBSP1_FSR1/GPIO[4]
 AXR11/MCBSP1_FSR1/GPIO[3]
 AXR10/MCBSP1_DR1/GPIO[2]
 AXR9/MCBSP1_DX1/GPIO[1]

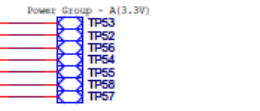
Power Group - A(3.3V)
 C1
 D3
 D1
 E3
 E2
 E1

Power Group - A(3.3V)
 F4
 D6
 D8
 A4
 B6
 C6
 A3
 A2
 B1
 A1

RSDV/RTC_ALARM/UART2_CTSn/GPIO[8]
 AMUTE/UART2_RTSn/GPIO[9]
 AXR7/EPWM1_T2D/GPIO[15]
 AXR15/EPWM0_T2D/ECAP2_APWM2/GPIO[7]
 AFSX/GPIO[12]
 AFSR/GPIO[13]
 AHCLK/USB_REFCLK/UART1_CTSn/GPIO[10]
 AHCLK/UART1_RTSn/GPIO[11]
 ACLKX/GPIO[14]
 ACLKX/GPIO[15]

AXR14/MCBSP1_CLKR1/GPIO[6]
 AXR13/MCBSP1_CLKX1/GPIO[5]
 AXR8/CLKS1/ECAP1_APWM1/GPIO[0]
 AXR12/MCBSP1_FSR1/GPIO[4]
 AXR11/MCBSP1_FSR1/GPIO[3]
 AXR10/MCBSP1_DR1/GPIO[2]
 AXR9/MCBSP1_DX1/GPIO[1]

Power Group - A(3.3V)
 B4
 B3
 E4
 C4
 C5
 D4
 C3



Power Group - A(3.3V)
 N1
 N2
 J2
 J1
 N2
 L2
 N1
 M3