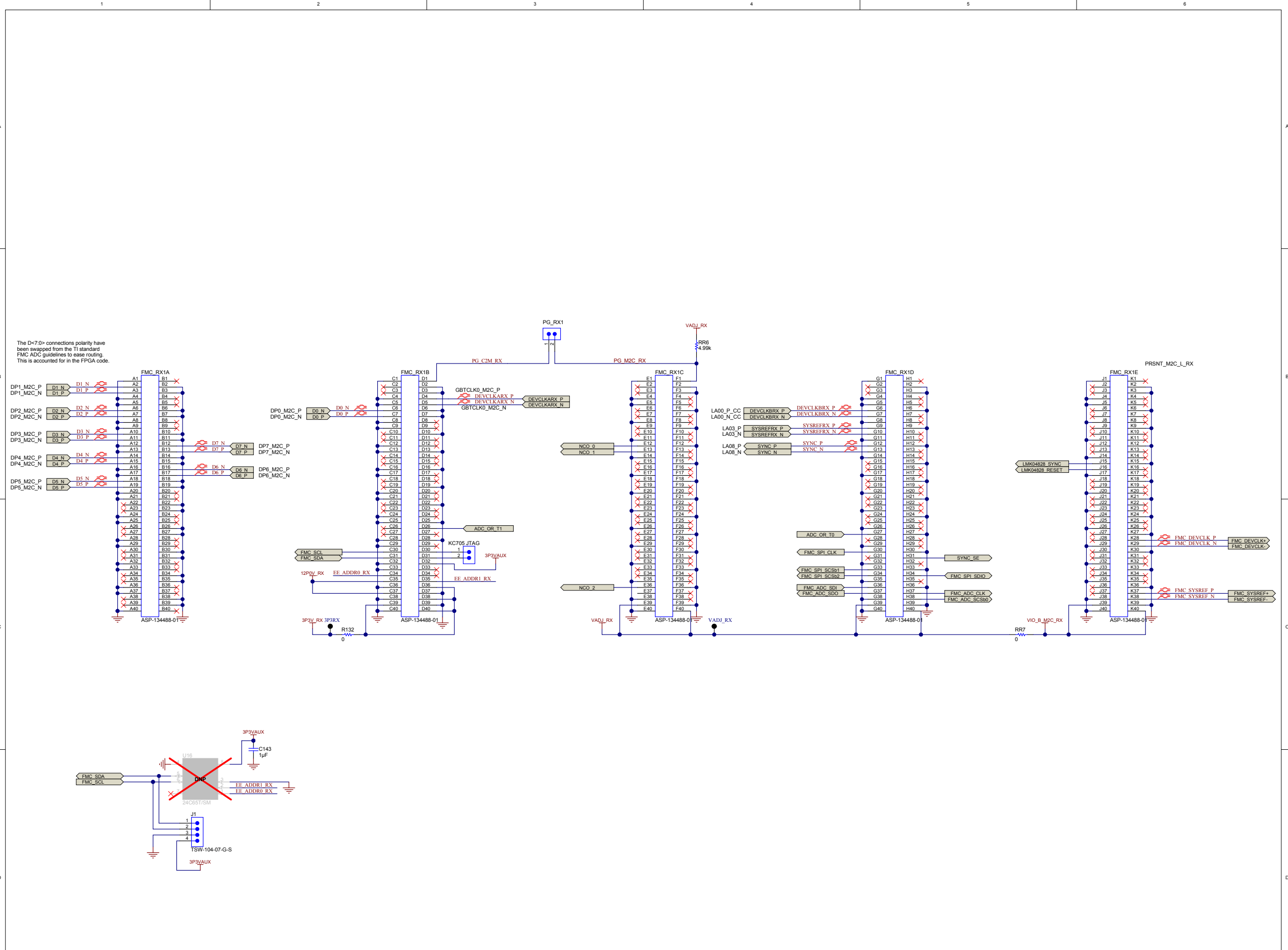
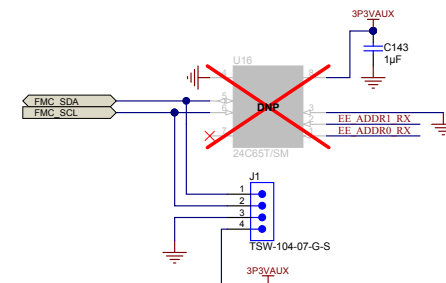
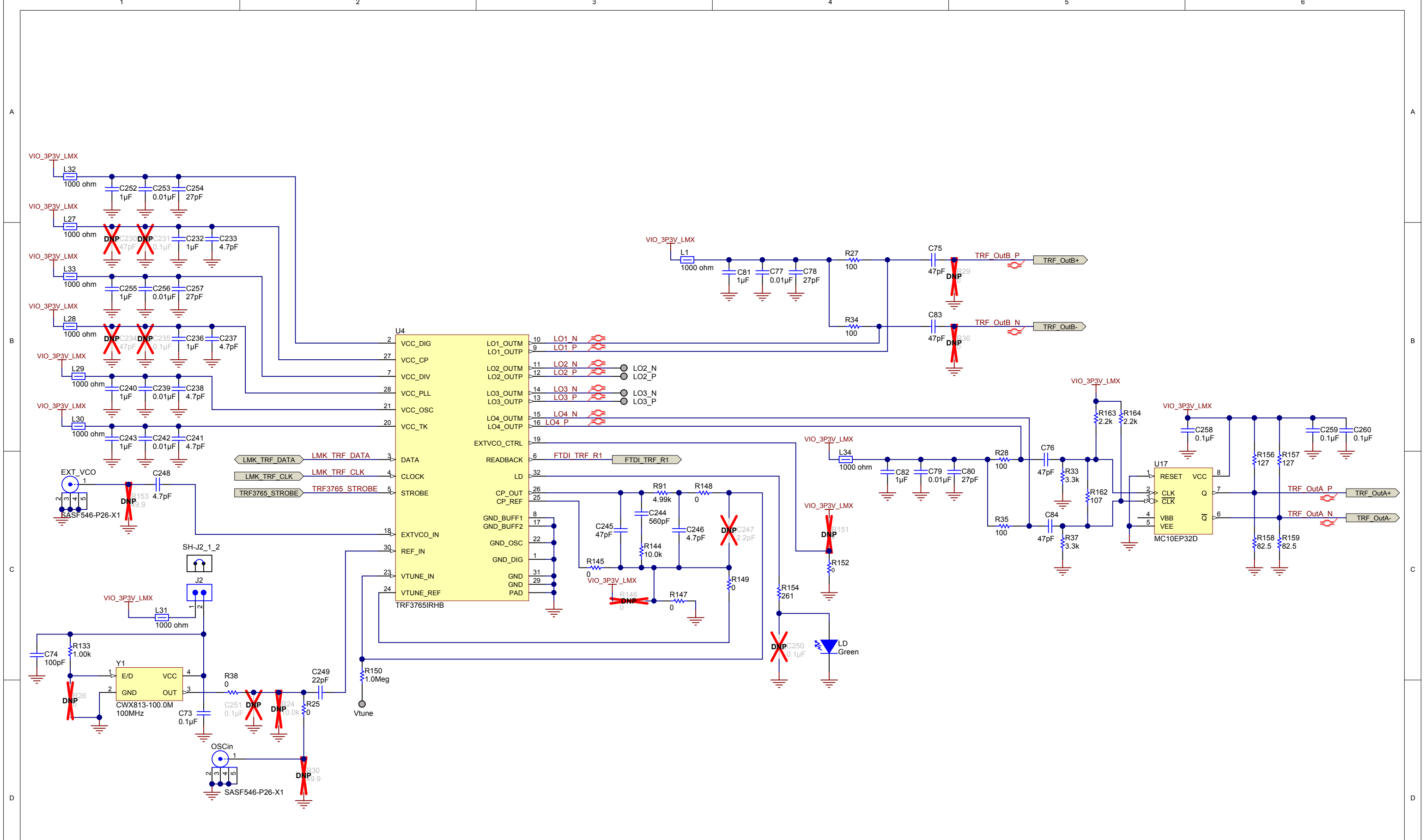


Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.



The D<7-0> connections polarity have been swapped from the TI standard FMC ADC guidelines to ease routing. This is accounted for in the FPGA code.

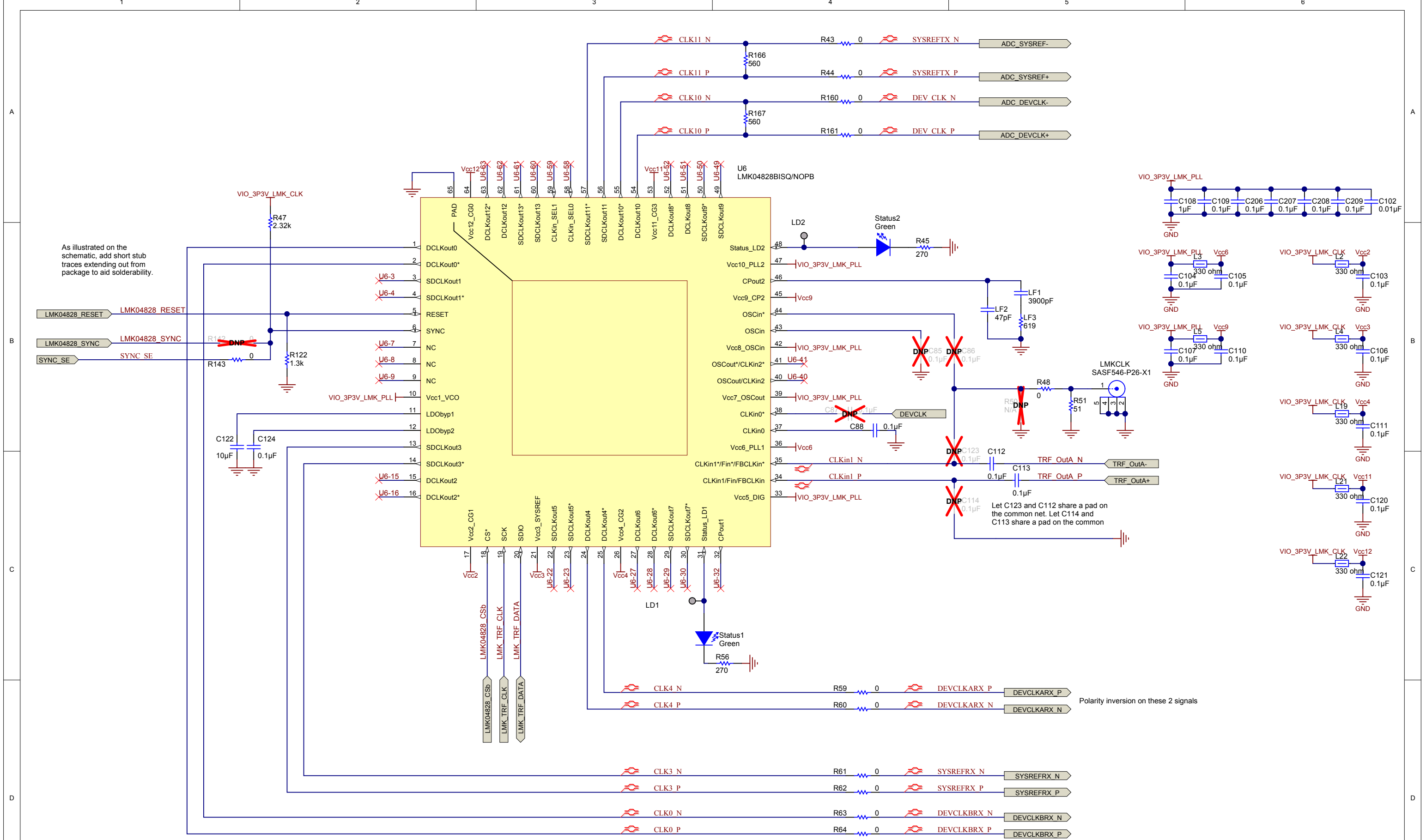




Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Designed for: Internal Use Only		Mod. Date: 12/8/2014	
Project Title: TSW12J54EVM			
Number: ??????	Rev: A	Sheet Title: TRF3765	
SVN Rev: Not in version control	Assembly Variant: 001_TSW12J54EVM	Sheet: 3 of 8	
Drawn By: Not shown in title block	File: TSW12J54EVM_A_3_TRF3765.SchDoc	Size: B	
Engineer: Jim Brinkhurst	Contact: http://www.ti.com/support		





As illustrated on the schematic, add short stub traces extending out from package to aid solderability.

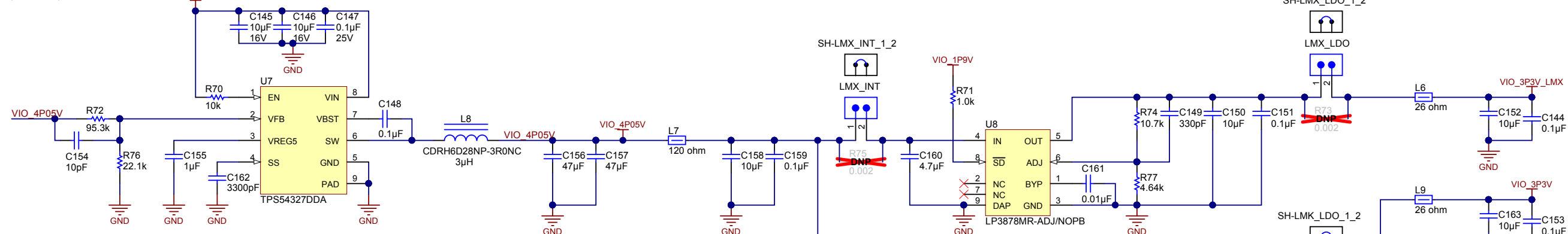
Number: ??????	Rev: A	Designed for: Internal Use Only	Mod. Date: 12/8/2014
SVN Rev: Not in version control	Project Title: TSW12J54EVM	Sheet Title:	
Drawn By:	Assembly Variant: 001_TSW12J54EVM	Sheet: 4 of 8	
Engineer: Jim Brinkhurst	File: TSW12J54EVM_A_4_LMK04828.SchDoc	Size: B	
	Contact: http://www.ti.com/support		

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.



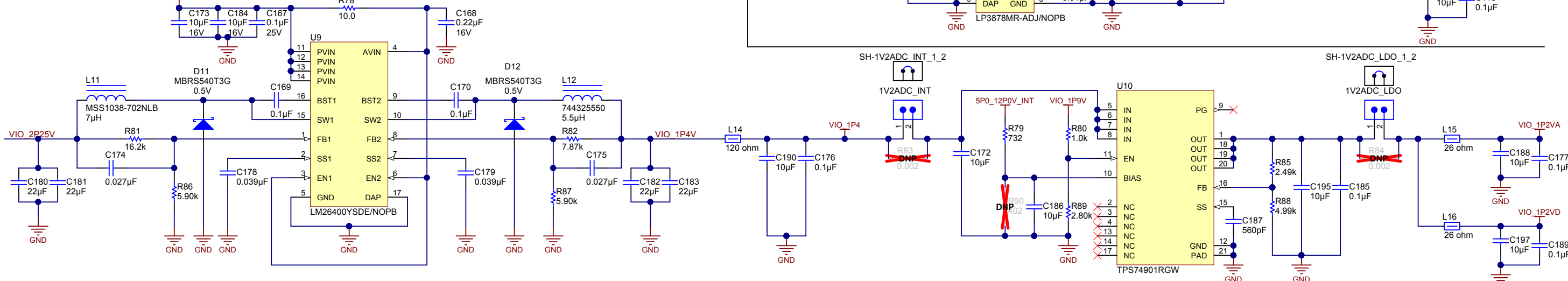
3.3V supply to clocks (LMK, LMX) and LEDs

5P0_12P0V_INT



1.9V and 1.2V supply to LM15851 / ADC12JXX00

5P0_12P0V_INT



12V main supply, from jack or via FMC connector to regulators

Add text label: "5V VIA JACK" and "12V VIA FMC, Install R90 for 12V operation"

SH-5V_12V_1_2

EXT5V_FMC12V

12P0V_RX

5P0_12P0V_INT

12V Green

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

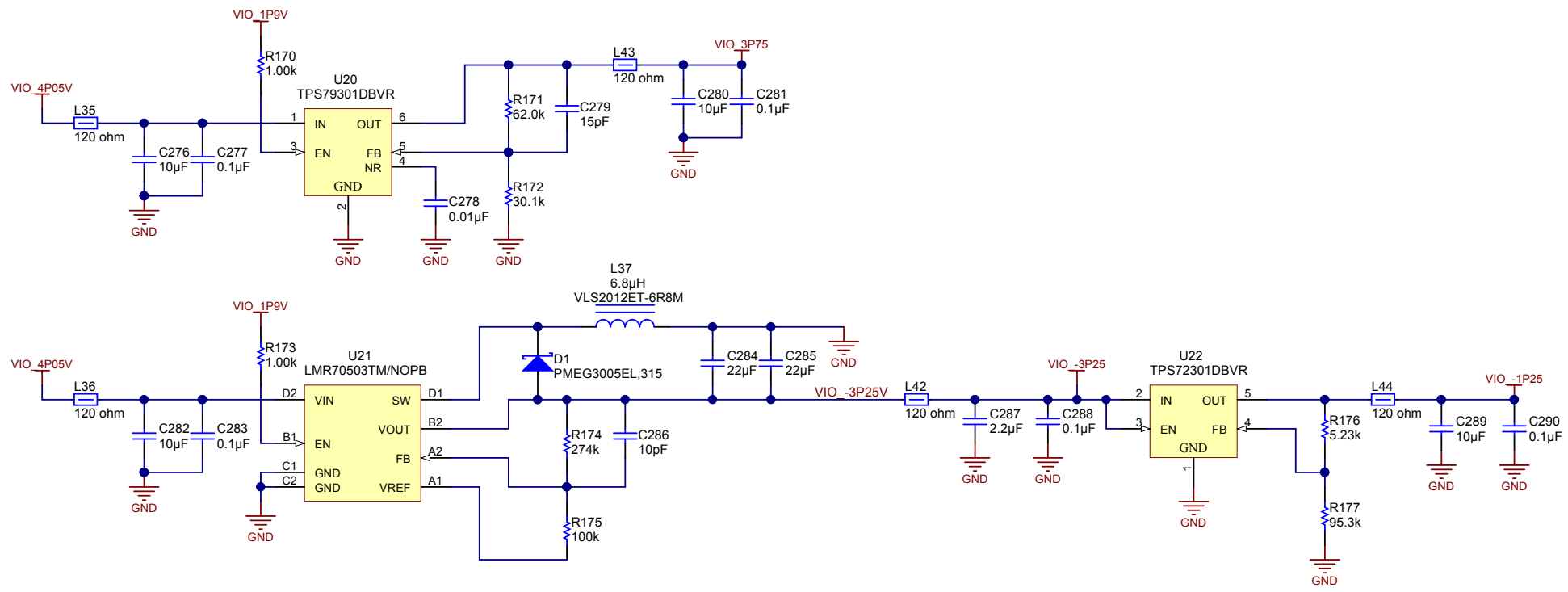
GND

Locate at edge opposite FMC connector

Number: ??????	Rev: A	Designed for: Internal Use Only	Mod. Date: 12/8/2014
SVN Rev: Not in version control	Project Title: TSW12J54EVM	Sheet Title:	
Drawn By:	Assembly Variant: 001_TSW12J54EVM	Sheet: 5 of 8	
Engineer: Jim Brinkhurst	File: TSW12J54EVM_A_5_Power.SchDoc	Size: B	
	Contact: http://www.ti.com/support		



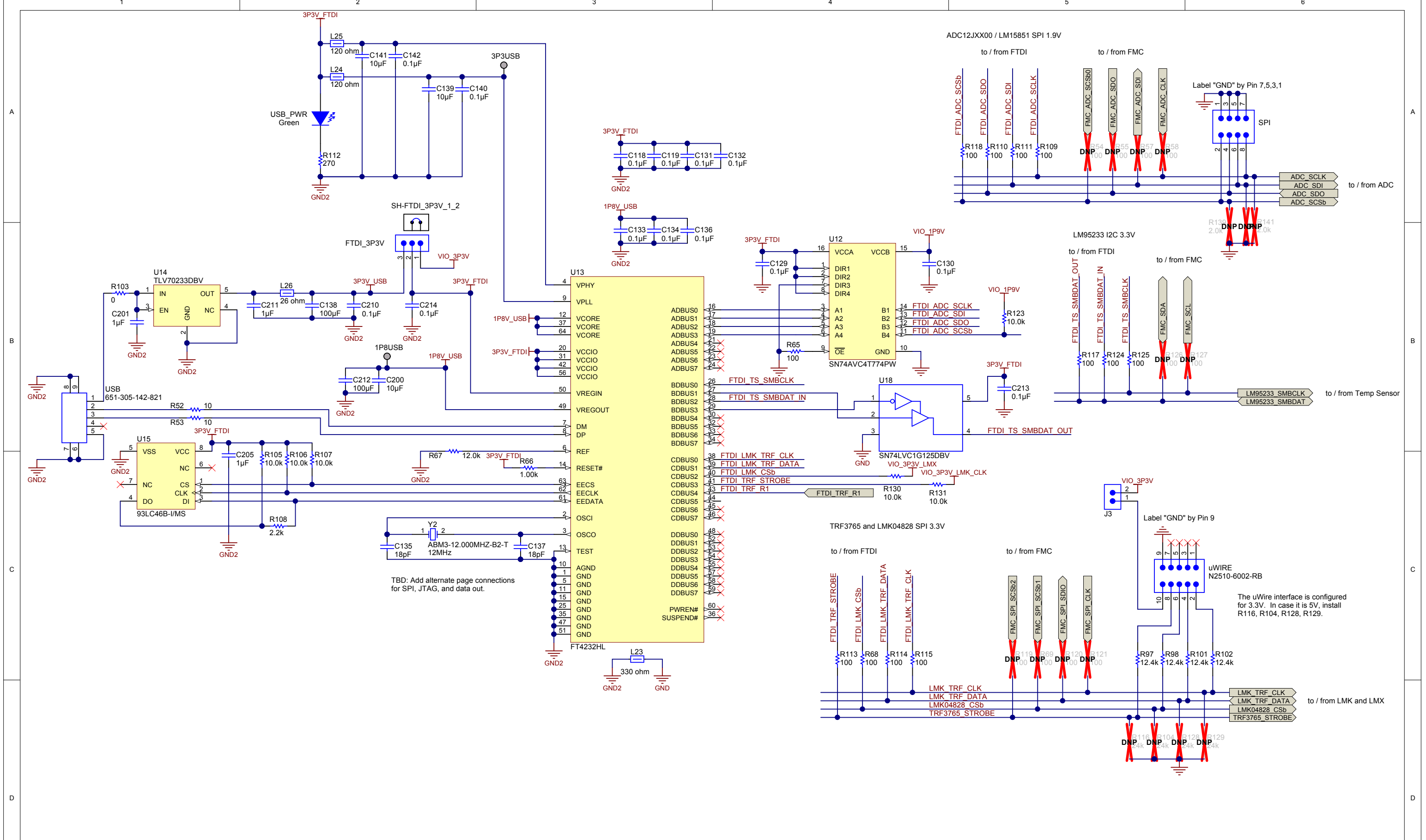
Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Designed for: Internal Use Only		Mod. Date: 12/8/2014	
Project Title: TSW12J54EVM			
Number: ??????	Rev: A	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001_TSW12J54EVM	Sheet: 6 of 8	
Drawn By:	File: TSW12J54EVM_A_6_Power.SchDoc	Size: B	
Engineer: Jim Brinkhurst	Contact: http://www.ti.com/support		





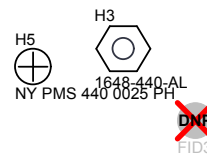
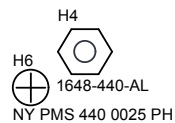
Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

~~DNP~~
FID1

PCB Number: ??????
PCB Rev: A

PCB
ESD LOGO
ESD Susceptible

PCB
LOGO
Texas Instruments



~~DNP~~
FID2

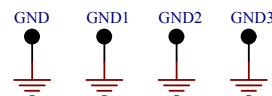
~~DNP~~
FID3

H9
MECH
FMC - FMC Screw
PMSSS 256 0075 PH

H10
MECH
FMC - FMC Nut

H11
MECH
FMC - FMC Screw
PMSSS 256 0075 PH

H12
MECH
FMC - FMC Nut



Place at least two of the GND test points in the power section.

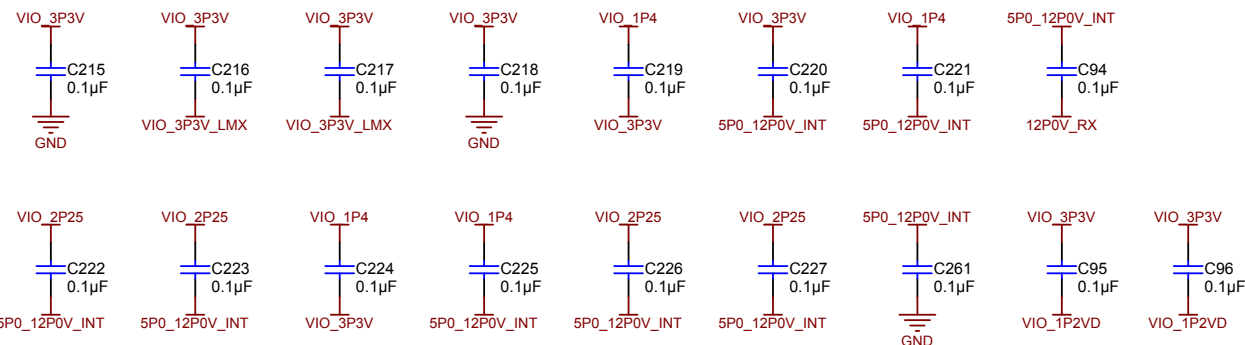
LBL1
PCB Label
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2., unless otherwise specified.



Designed for: Internal Use Only	Mod. Date: 12/8/2014
Project Title: TSW12J54EVM	
Number: ??????	Rev: A
SVN Rev: Not in version control	Assembly Variant: 001_TSW12J54EVM
Drawn By:	File: TSW12J54EVM_A_8_Hardware.SchDoc
Engineer: Jim Brinkhurst	Contact: http://www.ti.com/support



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Number: ??????	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001_TSW12J54EVM	Sheet: 8 of 8
Drawn By:	File: TSW12J54EVM_A_8_Hardware.SchDoc	Size: B
Engineer: Jim Brinkhurst	Contact: http://www.ti.com/support	