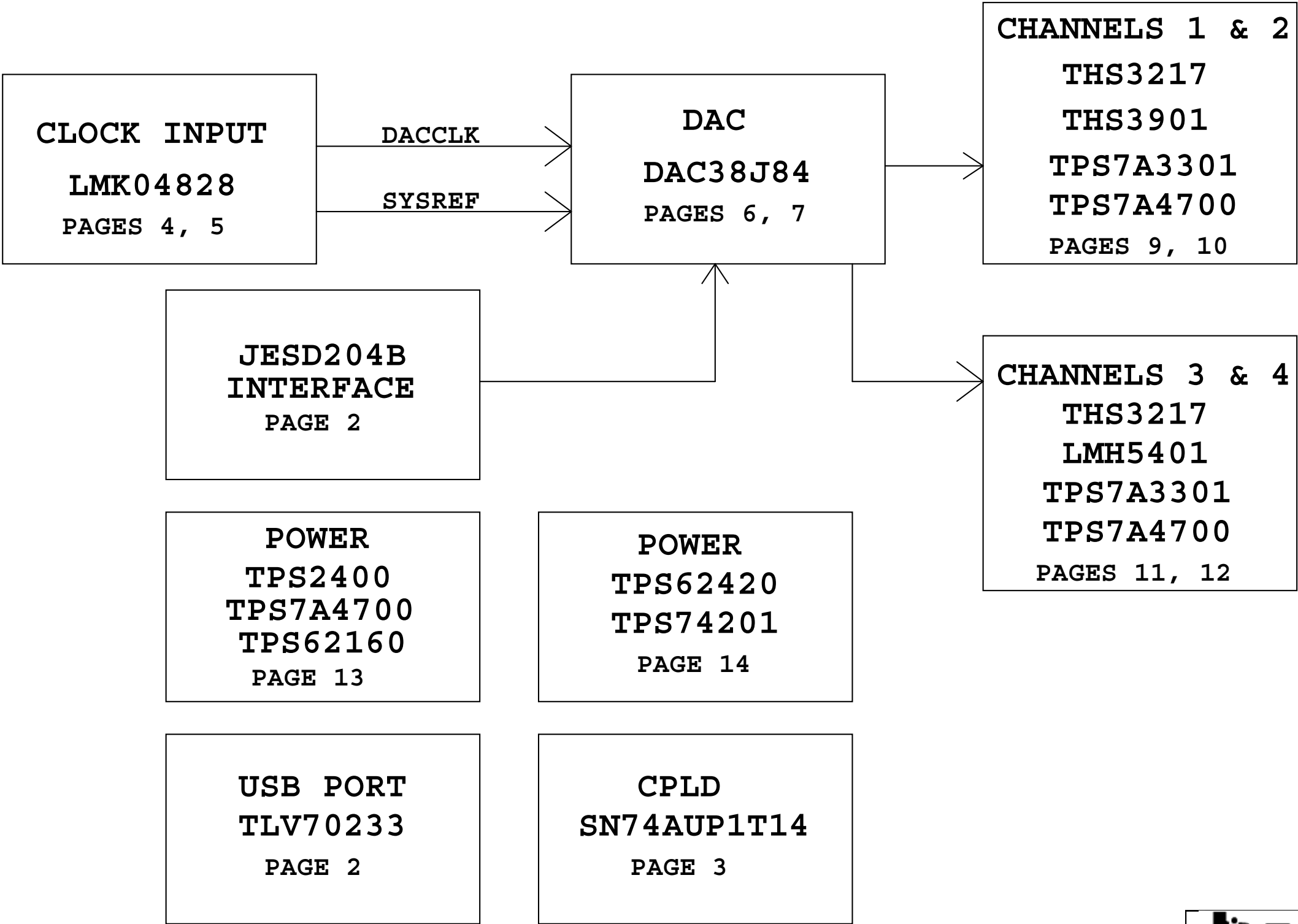
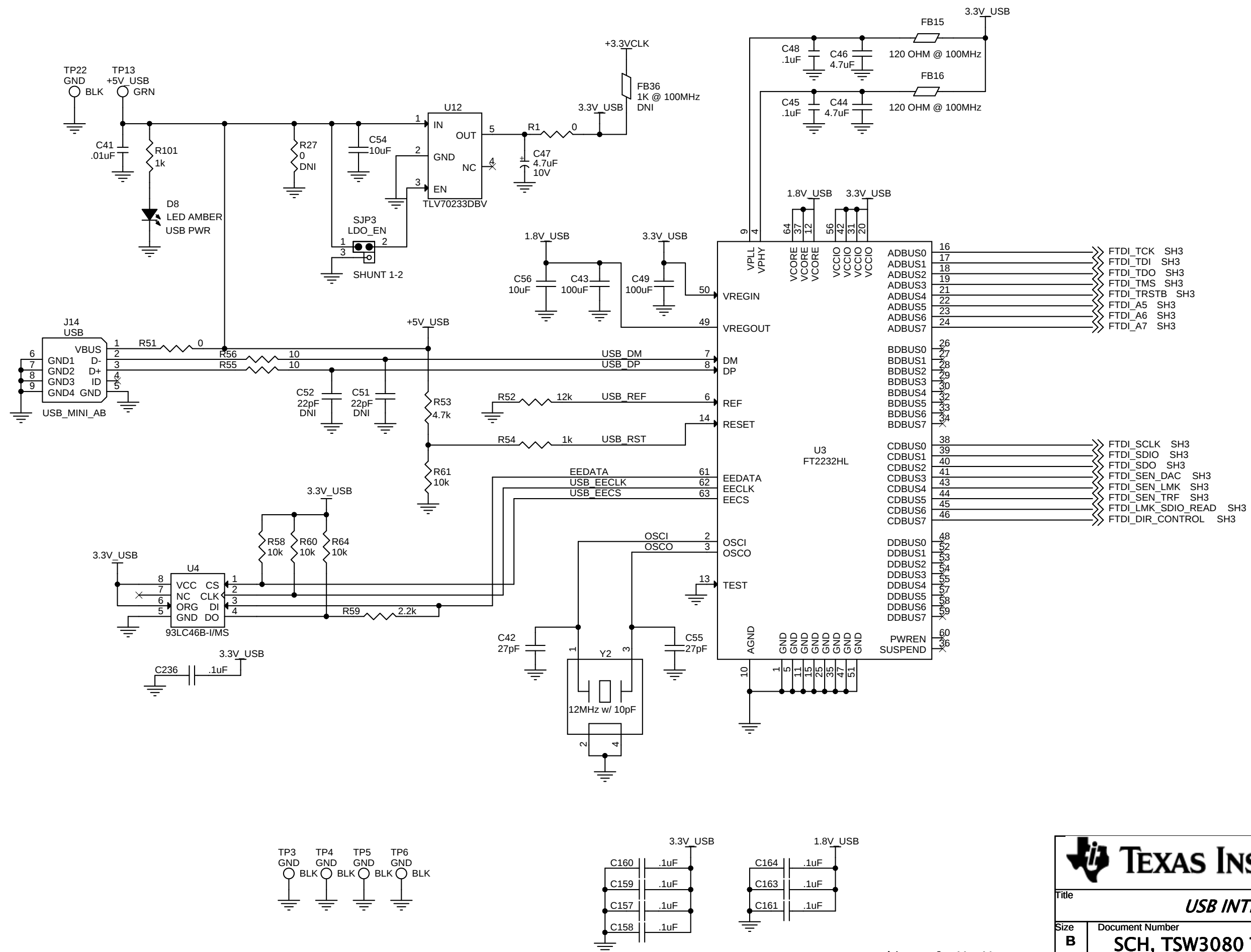


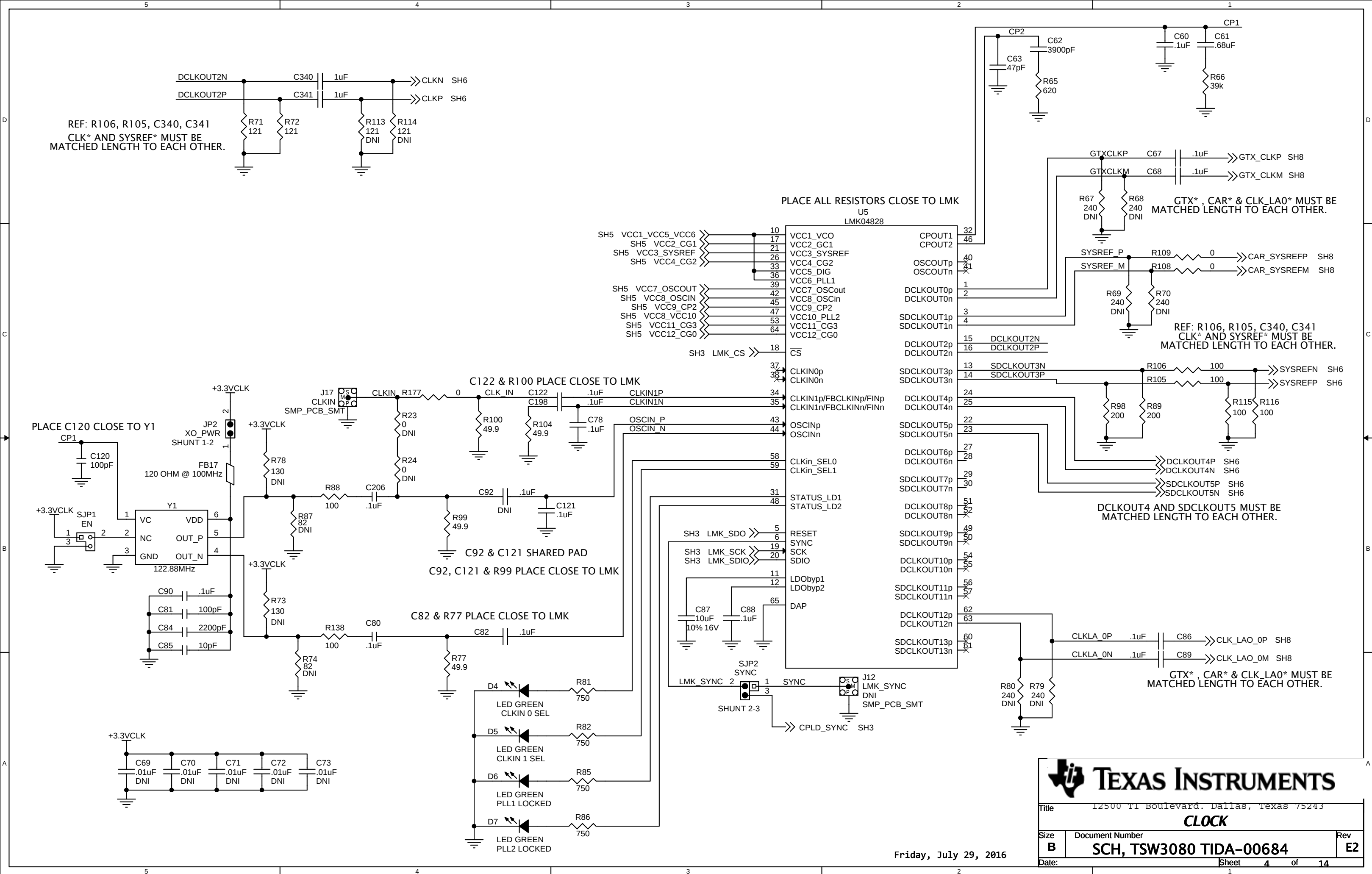


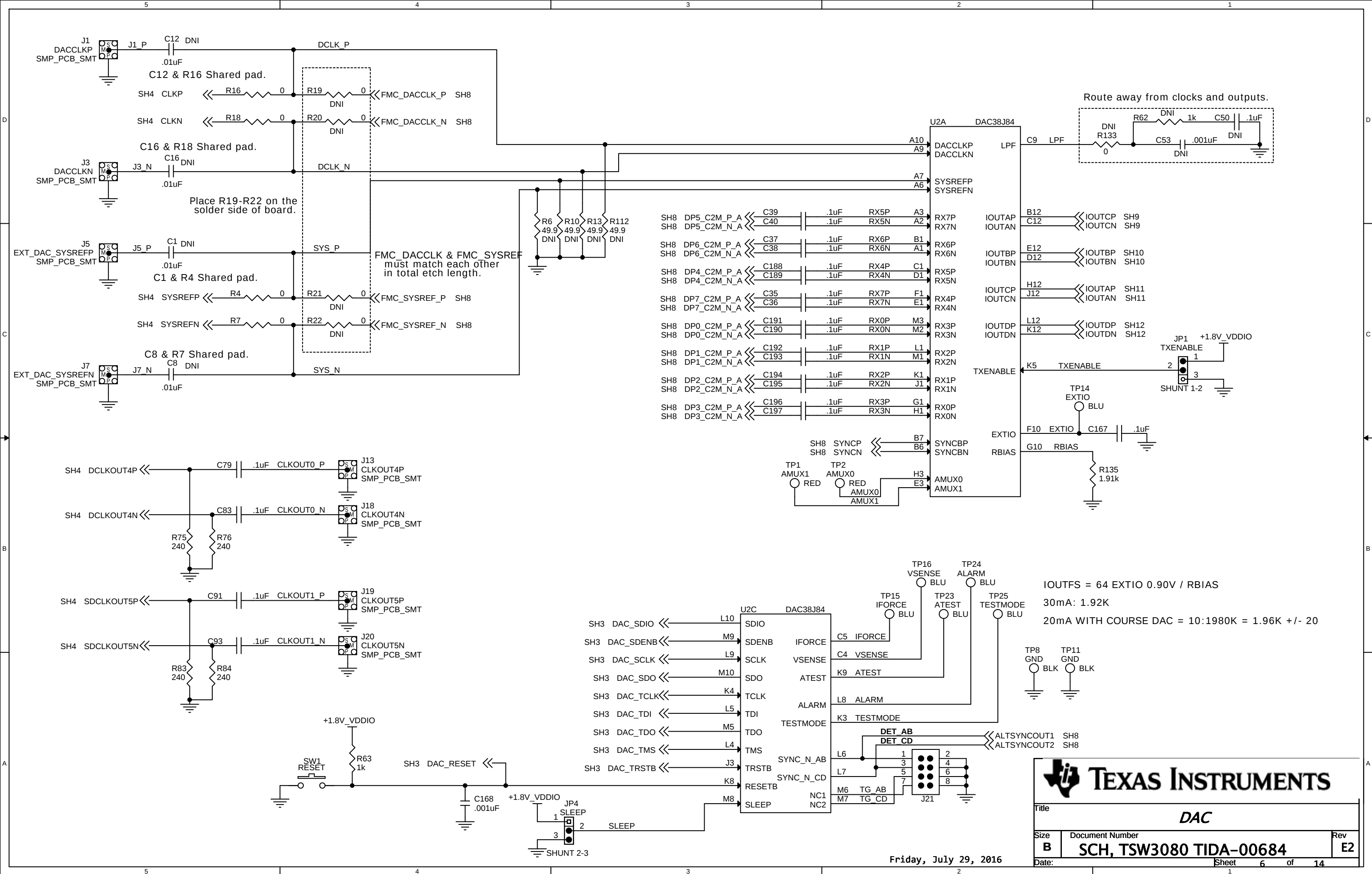
TEXAS INSTRUMENTS

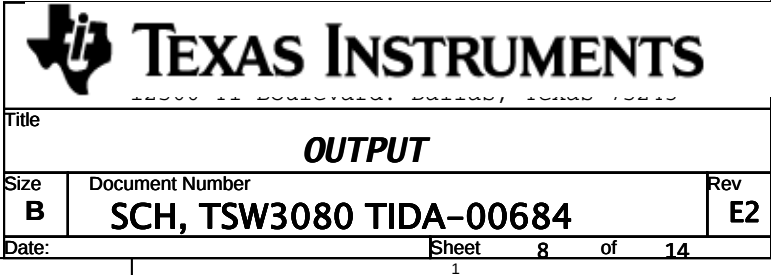
Title BLOCK DIAGRAM			
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Drawn:	JV SMITH	11/15
Engineer:	KEN CHAN	11/15

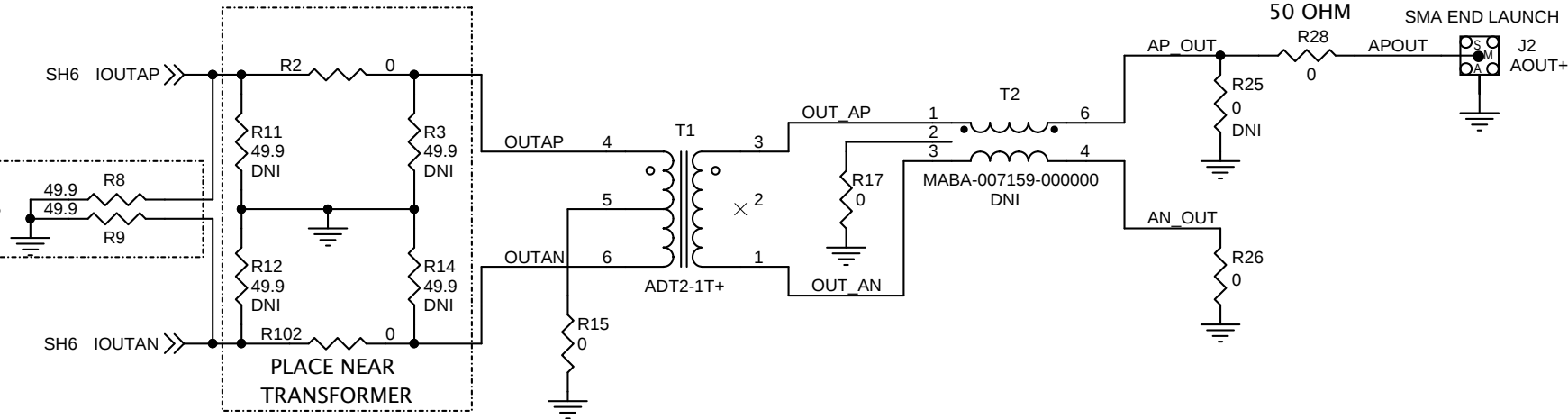




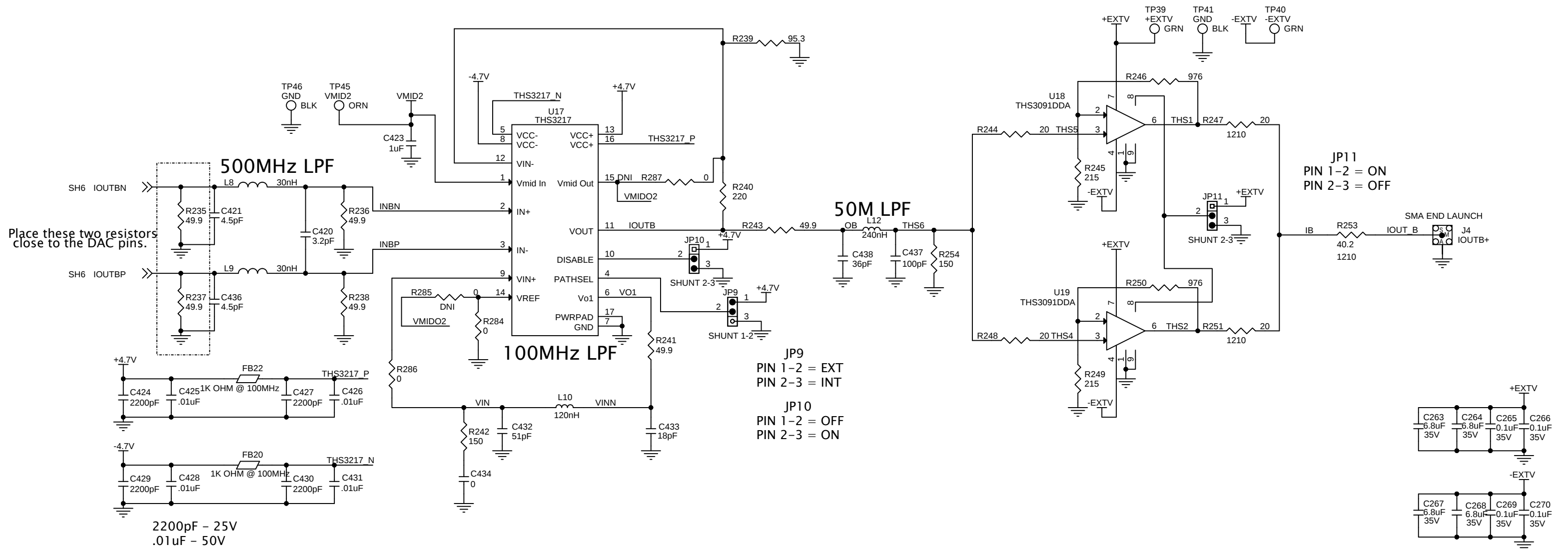




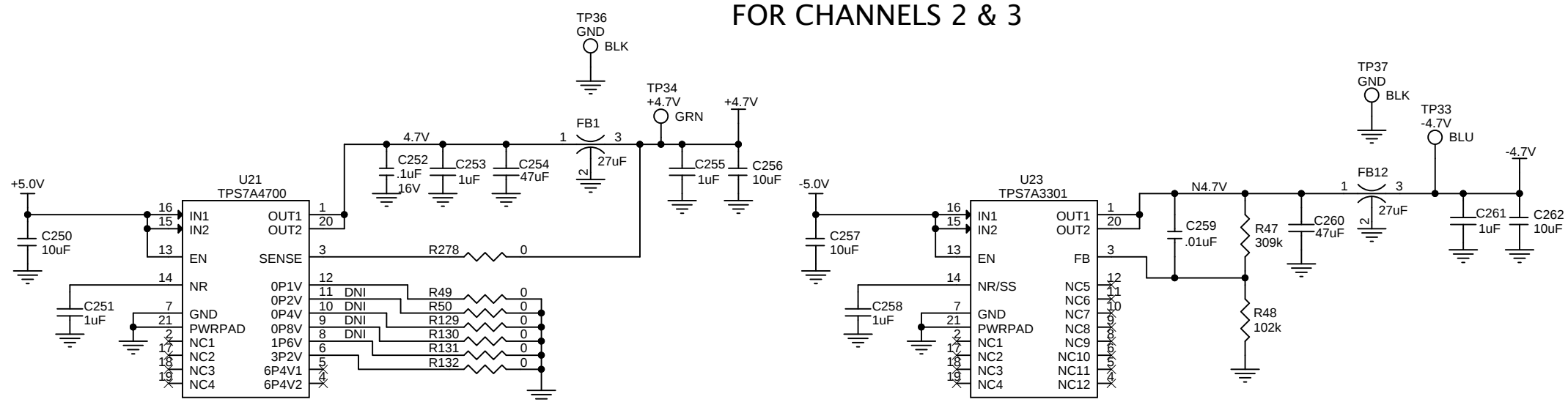
Place these two resistors
close to the DAC pins.



CHANNEL 2

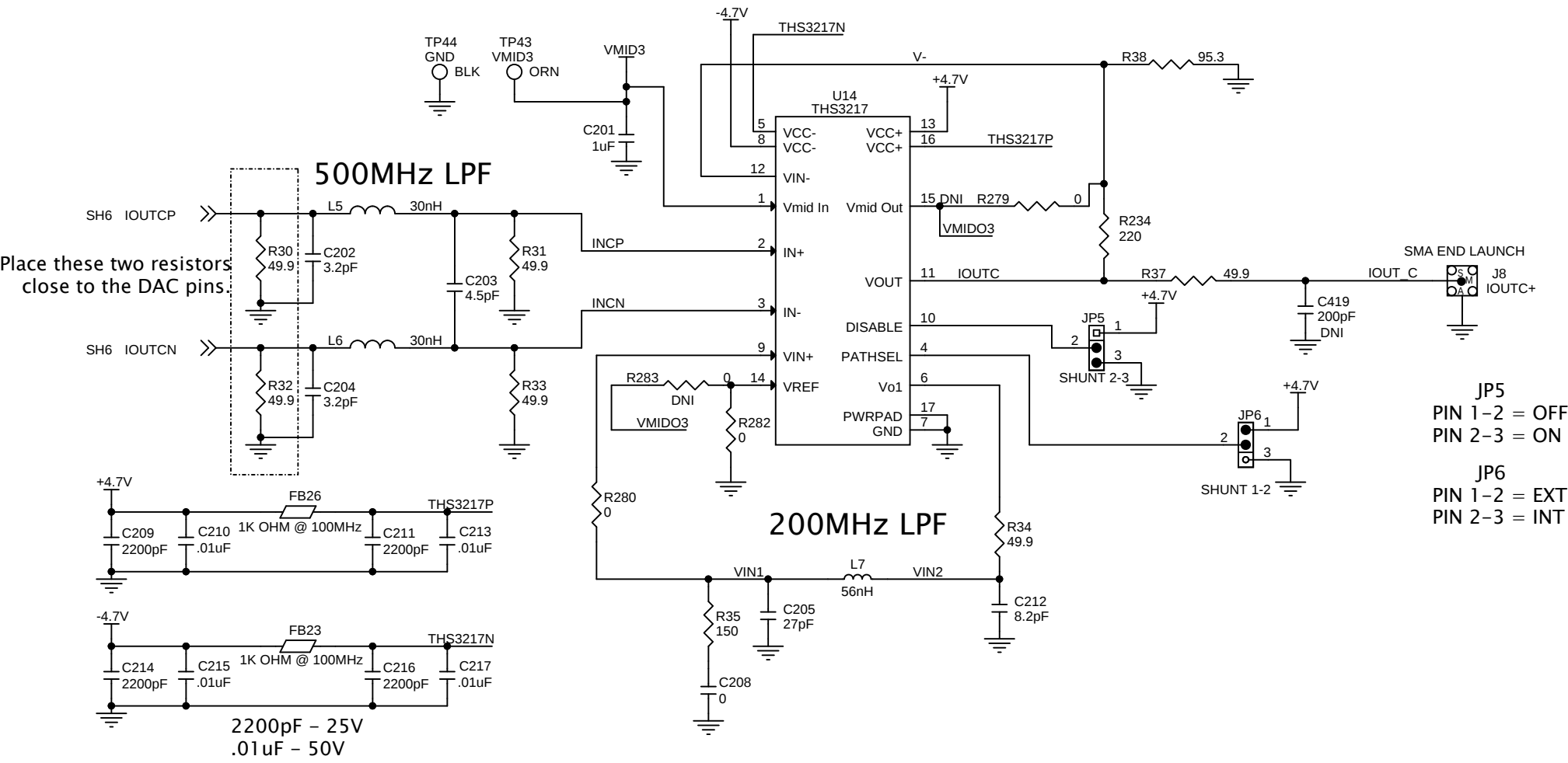


FOR CHANNELS 2 & 3



Title			
CHANNEL TWO			
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CHANNEL 3

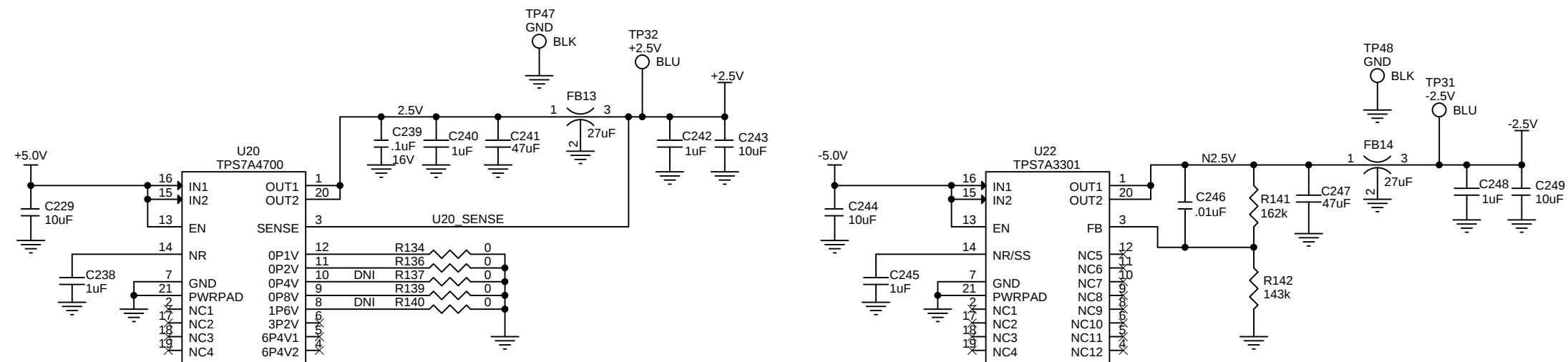
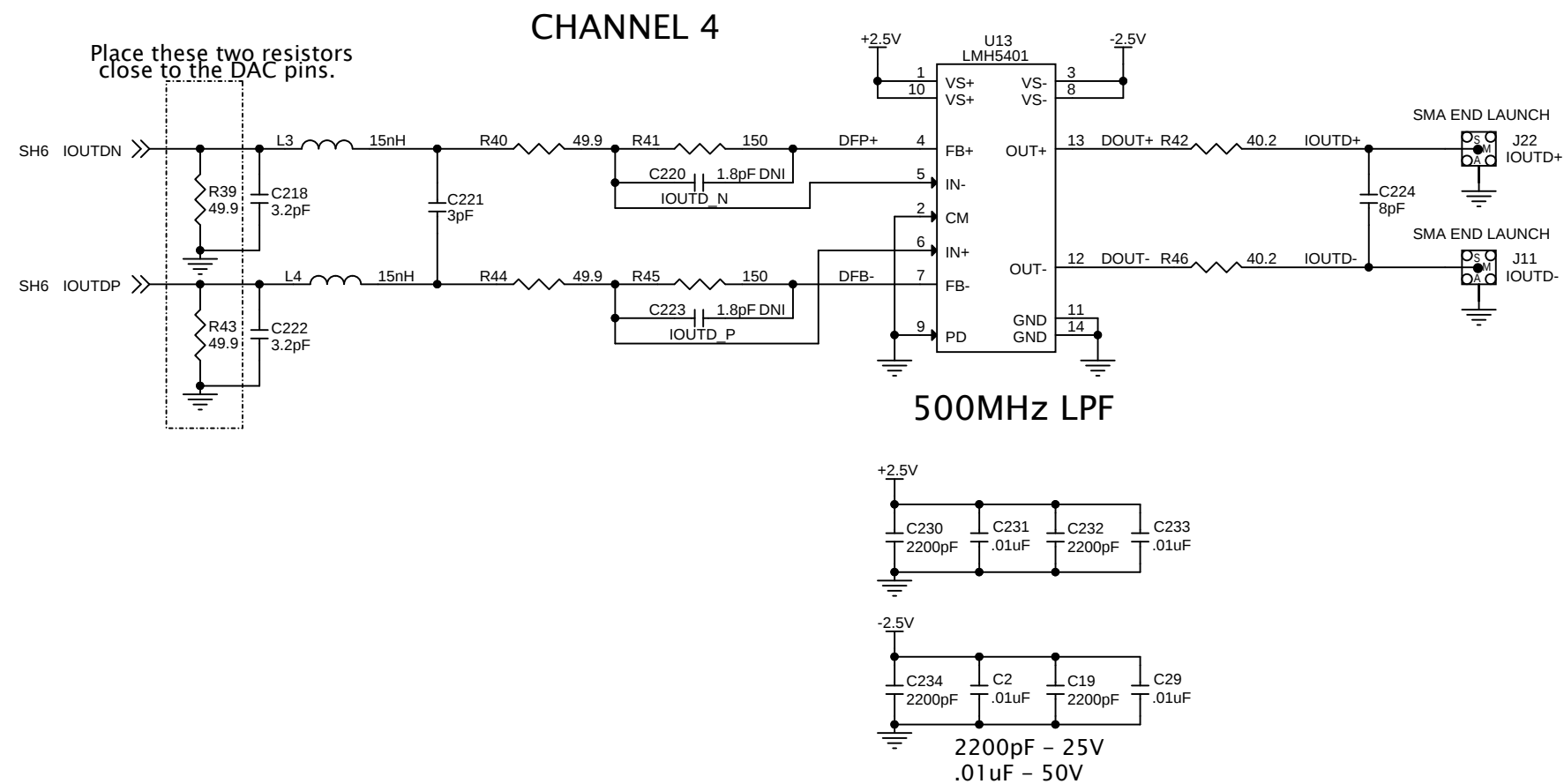


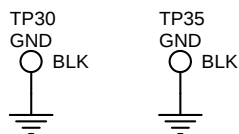
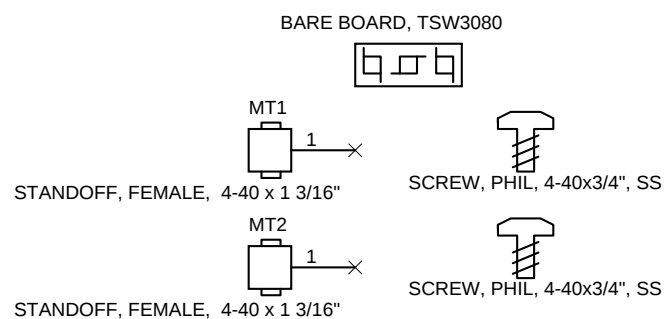
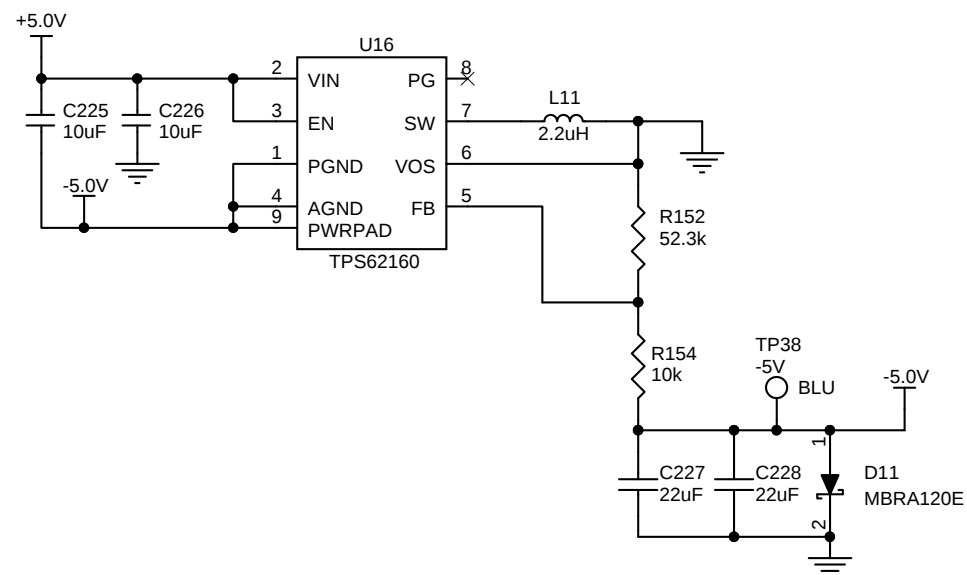
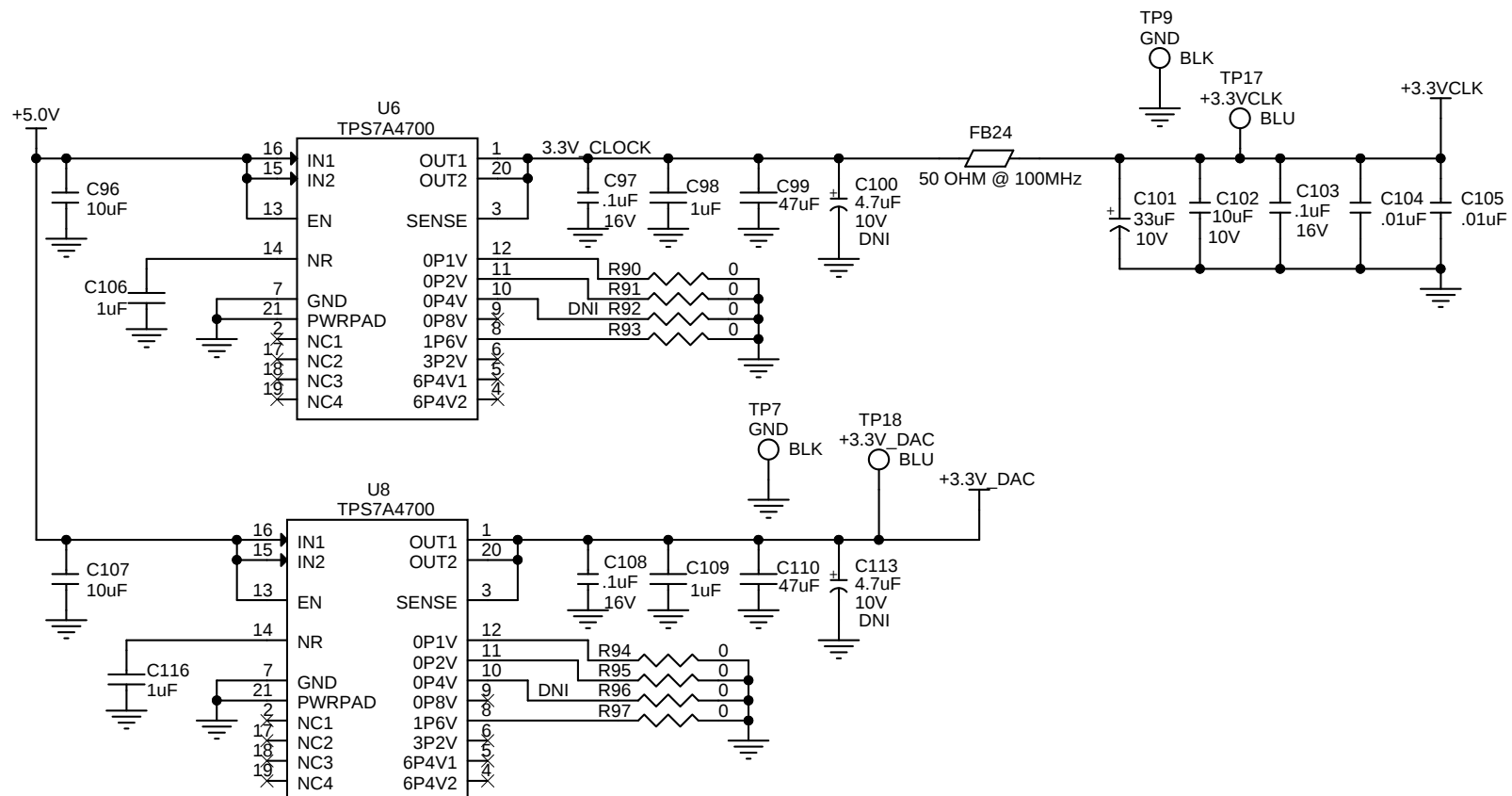
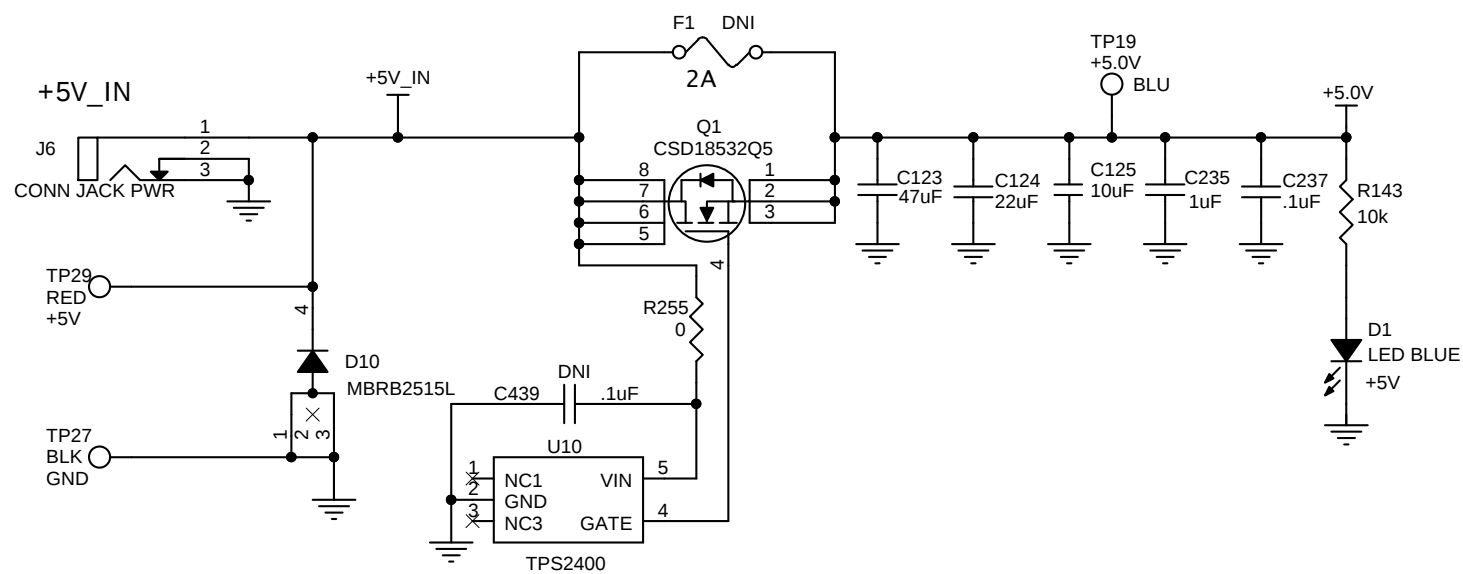
JP5
PIN 1-2 = OFF
PIN 2-3 = ON

JP6
PIN 1-2 = EXT
PIN 2-3 = INT



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CHANNEL THREE		
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Title		
MAIN POWER		
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