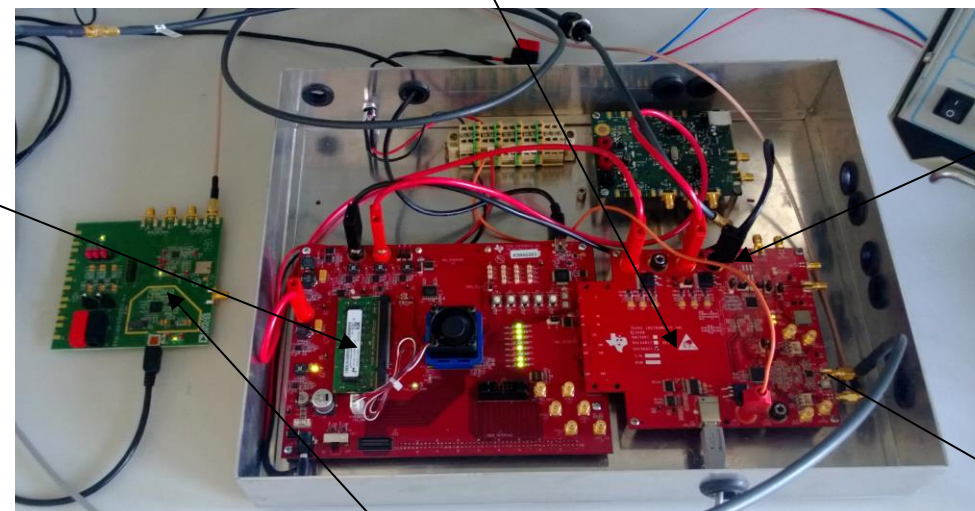




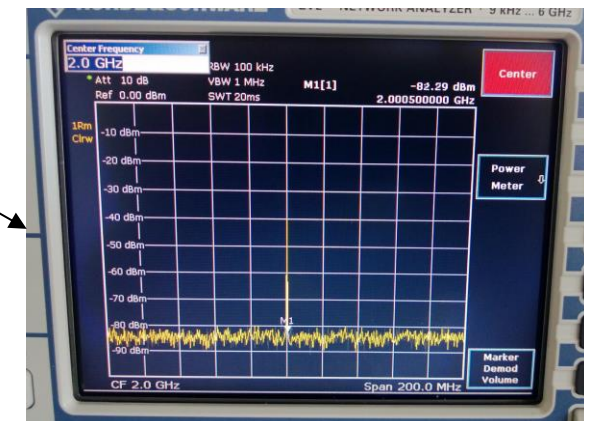
Stabilock 4031
 491,52 MHz
 8,0 dBm
 connected to DAC-Board
 J6 EXT_VCXO_P



DAC5682z.ini

[DAC]

Interface name="DAC_SAMPLE_WISE"
 Number of channels=2
 Number of Bits=16
 Max sample Rate=1000000000
 interp=4
 Bus=16
 dclkdly=0
 pa_clks1=106
 config1=16
 config2=1
 Format Pattern=-1,-2,1,2
 //- for lsb
 //+ for msb
 //eg -1,-2,1,2= lsb of 1st channel data,lsb of 2nd channel data,
 // msb of 1st channel data,msb of 2nd channel data.
 Read EVM Setup Procedure="EVM Setup Procedure not available"
 \\use <> as delimiter for newline



connected to DAC-Board
 J16 RFOUT

Connected to DAC-Board
 J23 RF_LO_IN

