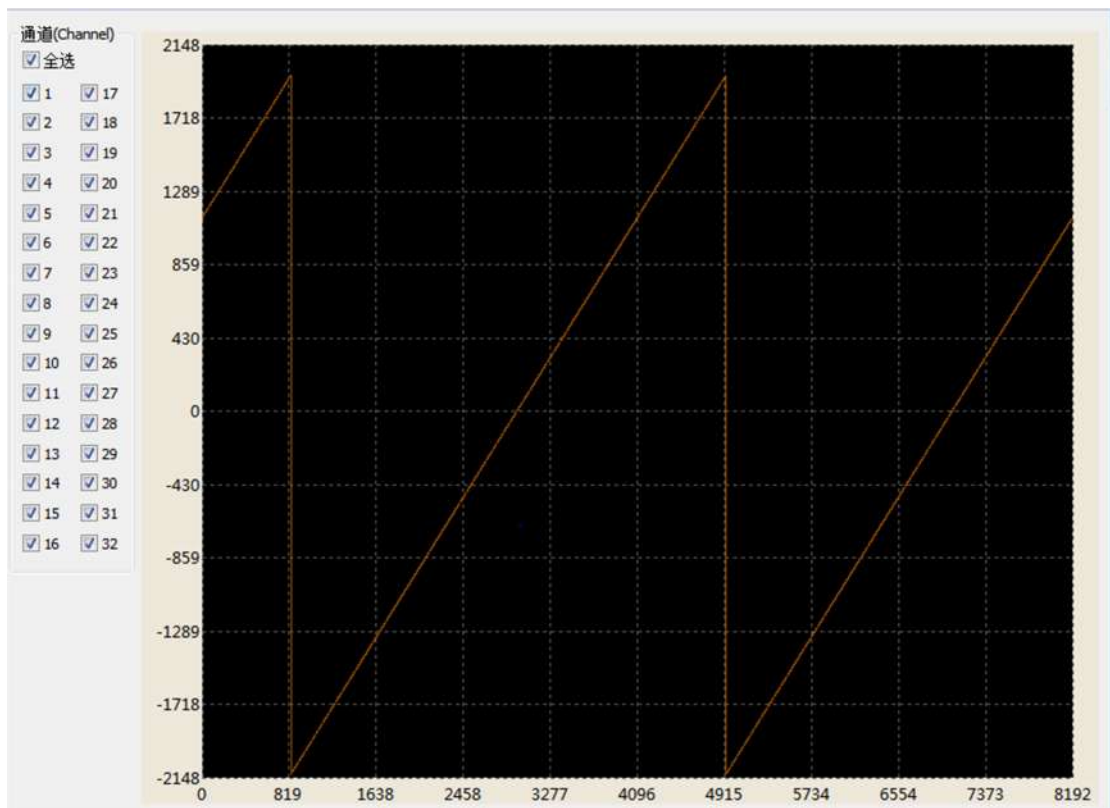
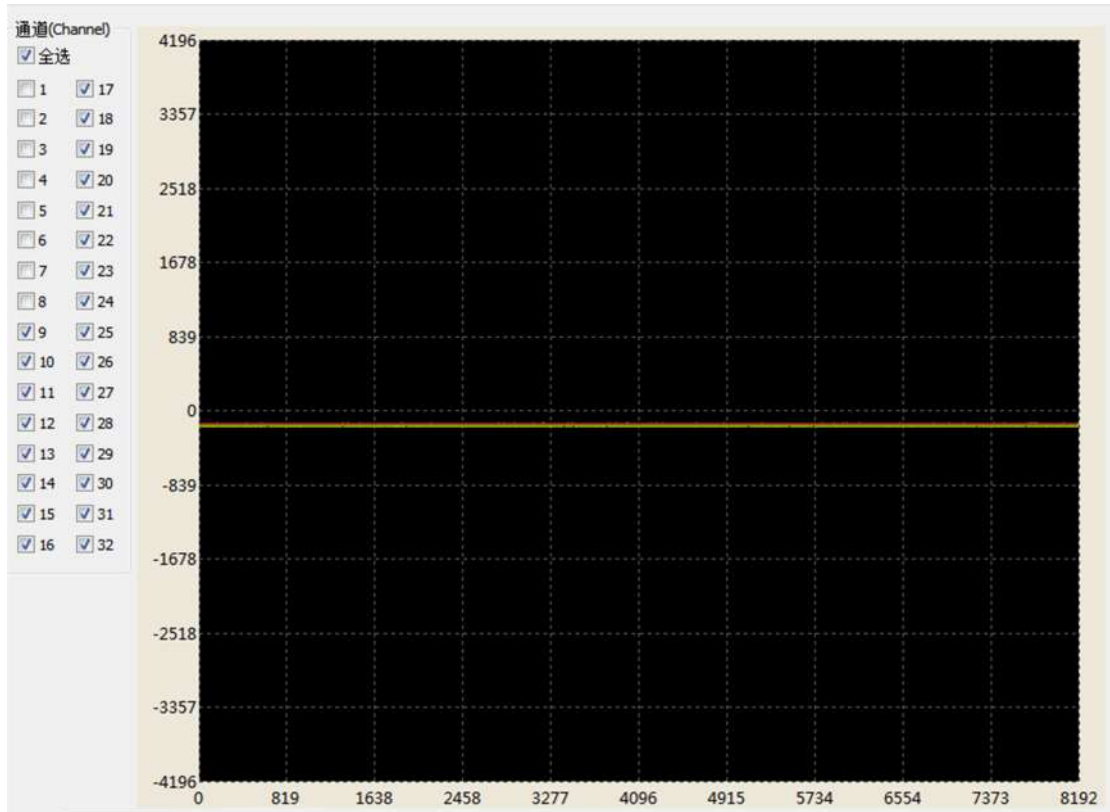


When test_pattern_modes is ramp , the data is correct, look at picture1



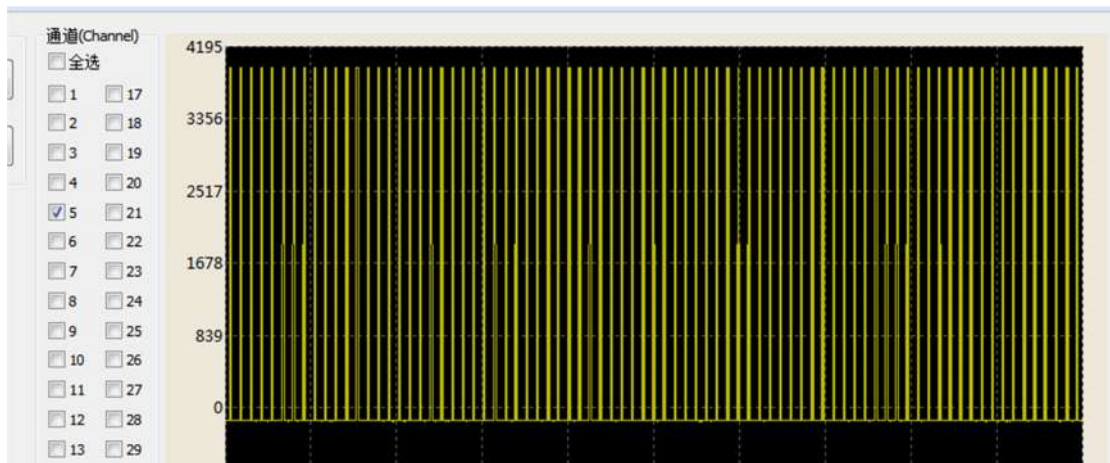
Picture 1

When test_pattern_modes is normal adc chip2 、 adc chip3 、 adc chip4 data are correct

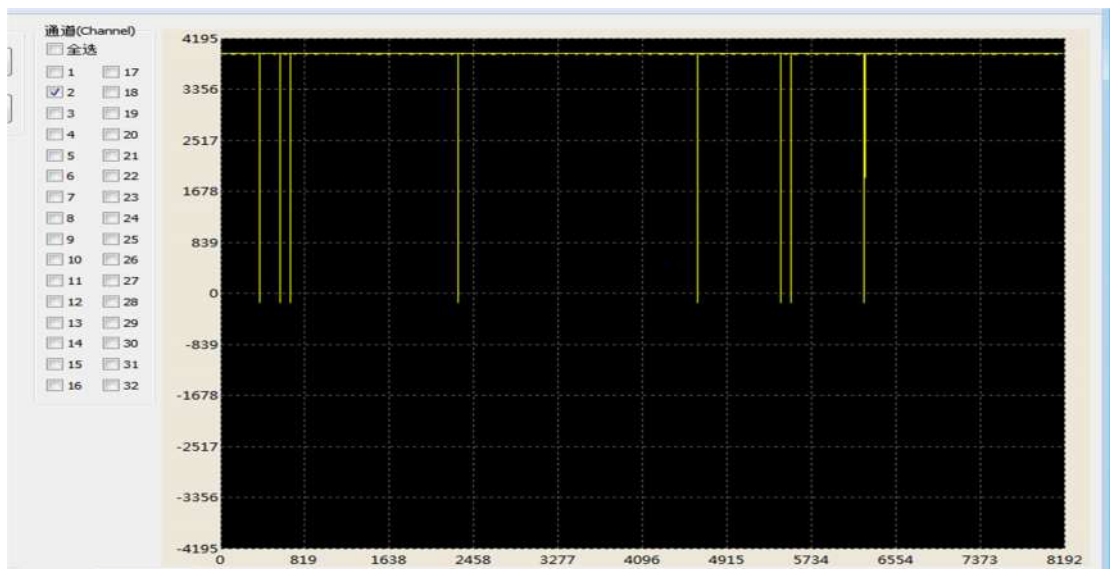


But test_pattern_modes is normal, some lane of adc chip1 is not correct

Lane 5



Lane2



Lane 1

