

Buffer ON / OFF comparison data and buffer OFF C9 removal comparison data  
 Setting : 2000SPS, PGA=2, Measured after executing SELFCAL

Input channel	VREF input[V]	AIN0 input[V]	AIN1 input[V]	Differential input[V]	ADC Out[V]			
					BUFFER-OFF	BUFFER-ON	BUFFER-OFF 300Ω addition C9 removal	BUFFER-ON 300Ω addition C9 removal
Ain0 - Ain1	1.5	2.5	1.5	1.0	0.99192482	0.99993193	1.00605452	1.01420164
		1.5	1.5	0.0	-0.00002736	0.00000358	-0.00003397	0.00001162
		0.5	1.5	-1.0	-0.99218571	-1.00019550	-1.00635672	-1.01447666
Swinging voltage order:					0.01mV	0.01mV	0.01mV	0.01mV

Input channel	VREF input[V]	AIN2 input[V]	AIN3 input[V]	Differential input[V]	ADC Out[V]			
					BUFFER-OFF	BUFFER-ON	BUFFER-OFF C9 removal	BUFFER-ON C9 removal
AIN2 - AIN3	1.5	1.0	0	1	1.00081635	1.00008231	1.01511449	1.01436186
		0.5	0	0.5	0.50339043	0.50004035	0.51057875	0.50716394
Swinging voltage order:					0.01mV	0.01mV	0.01mV	0.01mV

※1 : ADC input voltage calculated from the effective impedance at buffer off and the filter resistance of 300Ω