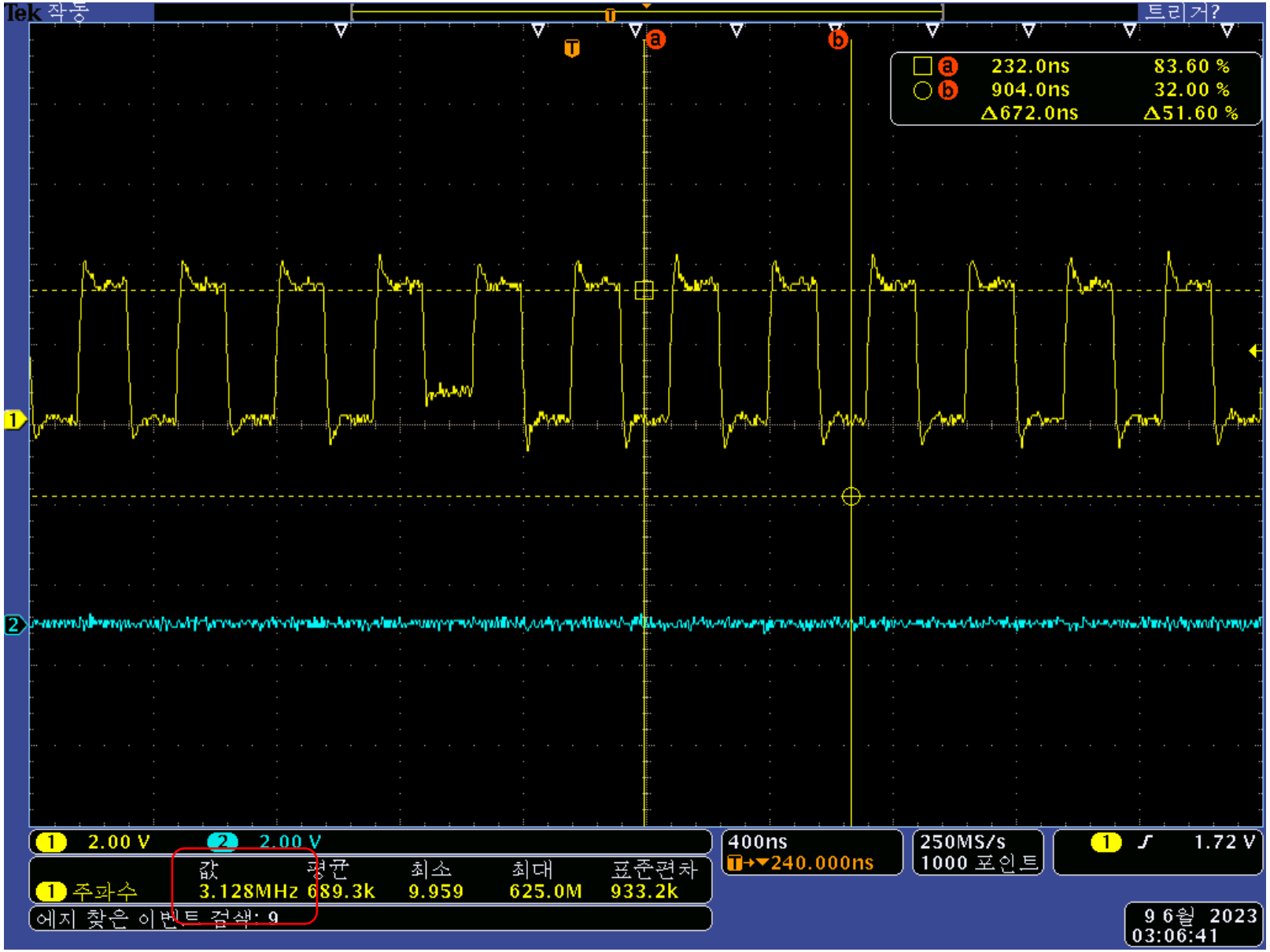
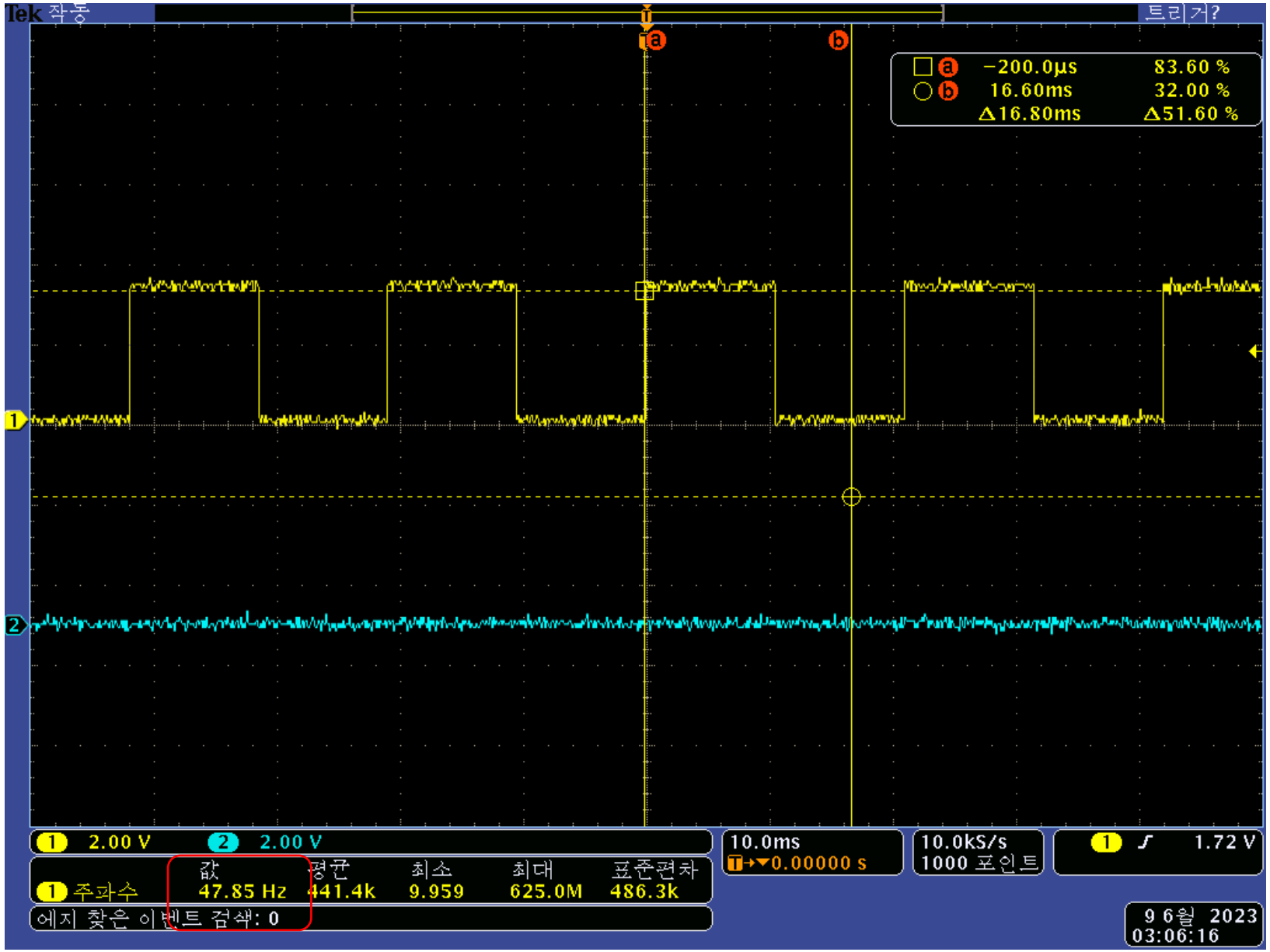
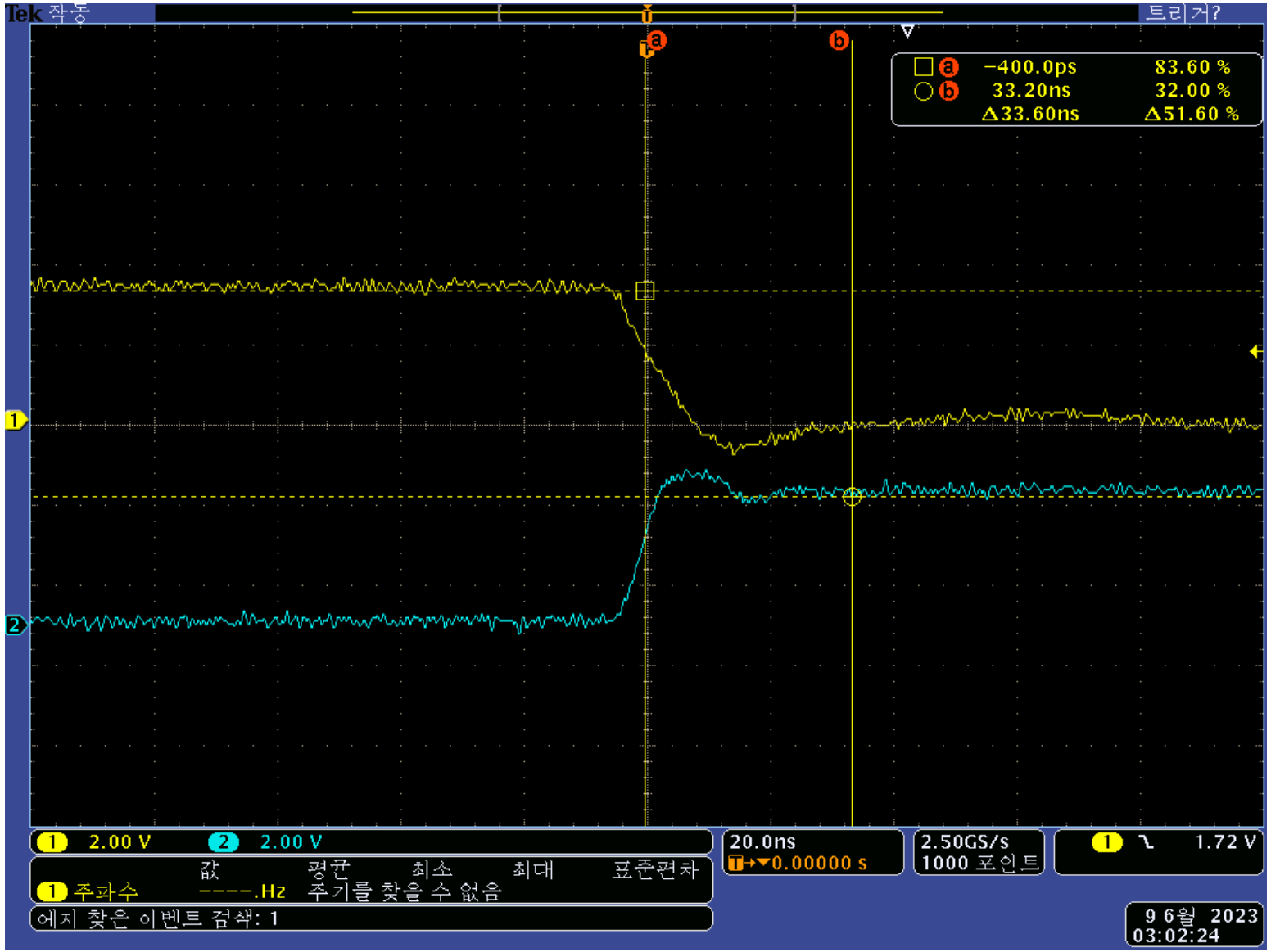


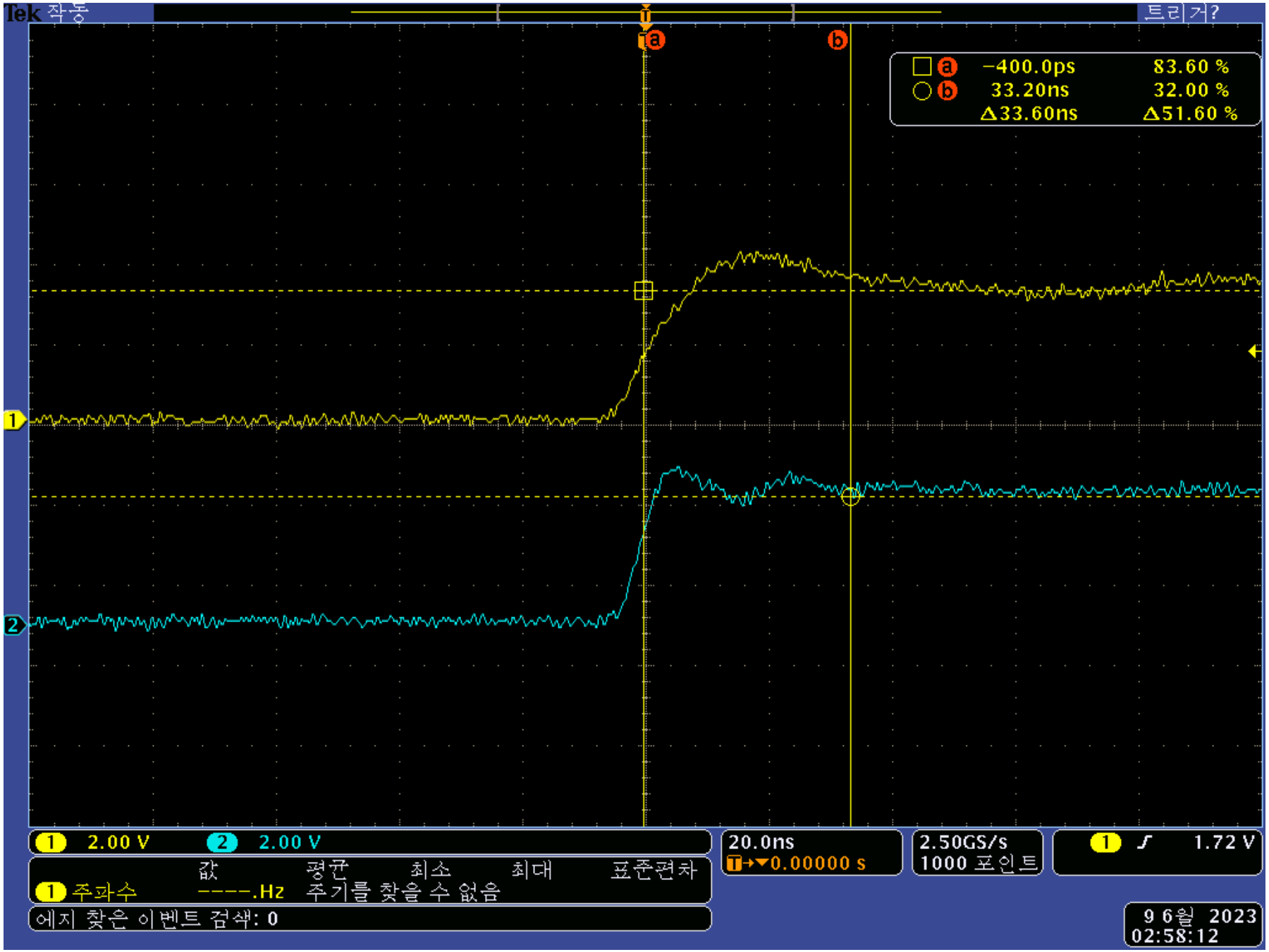
CLK(42Pin) image



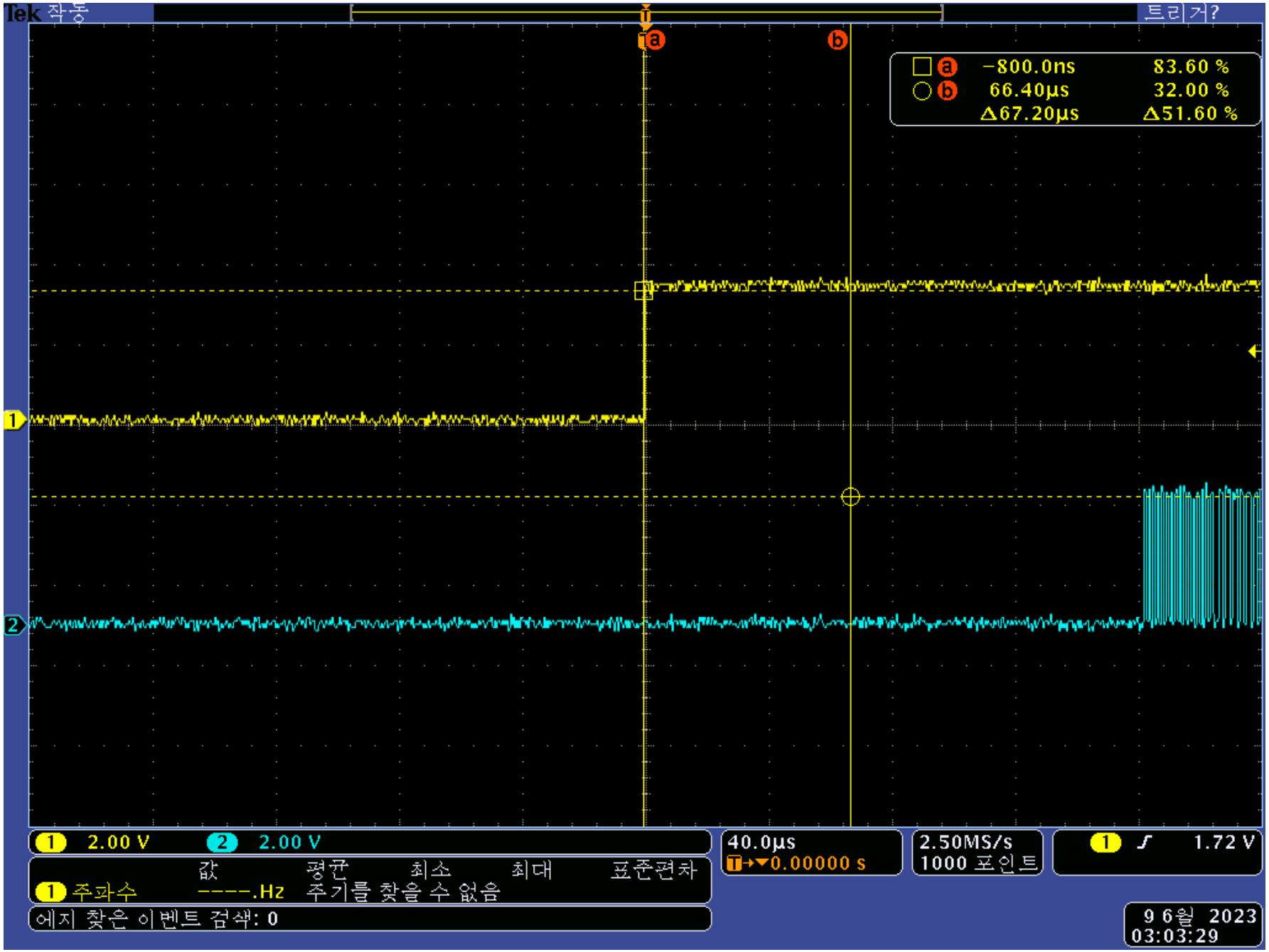
CONV(46Pin) image



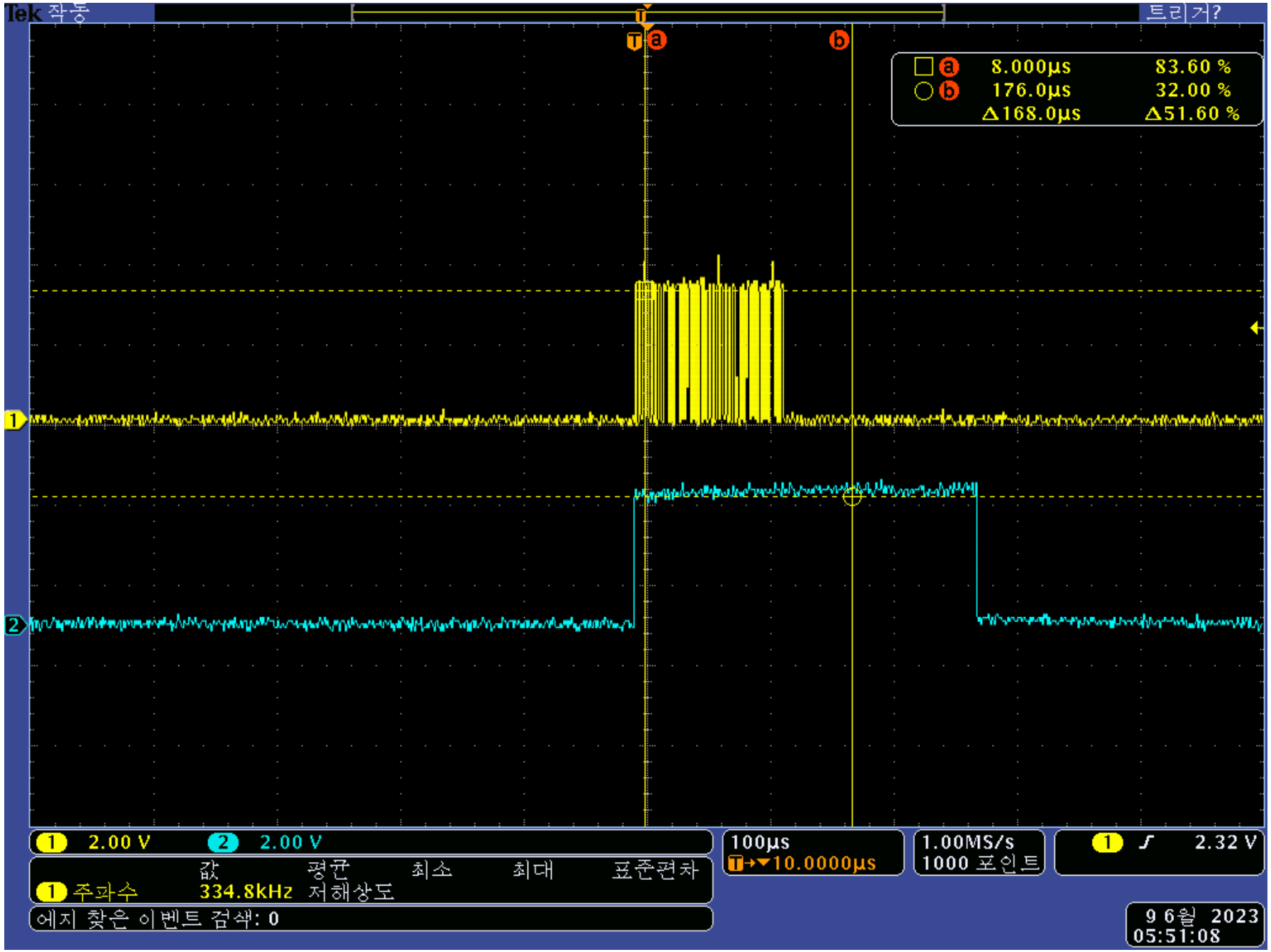
1 – CONV(46PIN) falling edge
2 – CLK(42PIN)



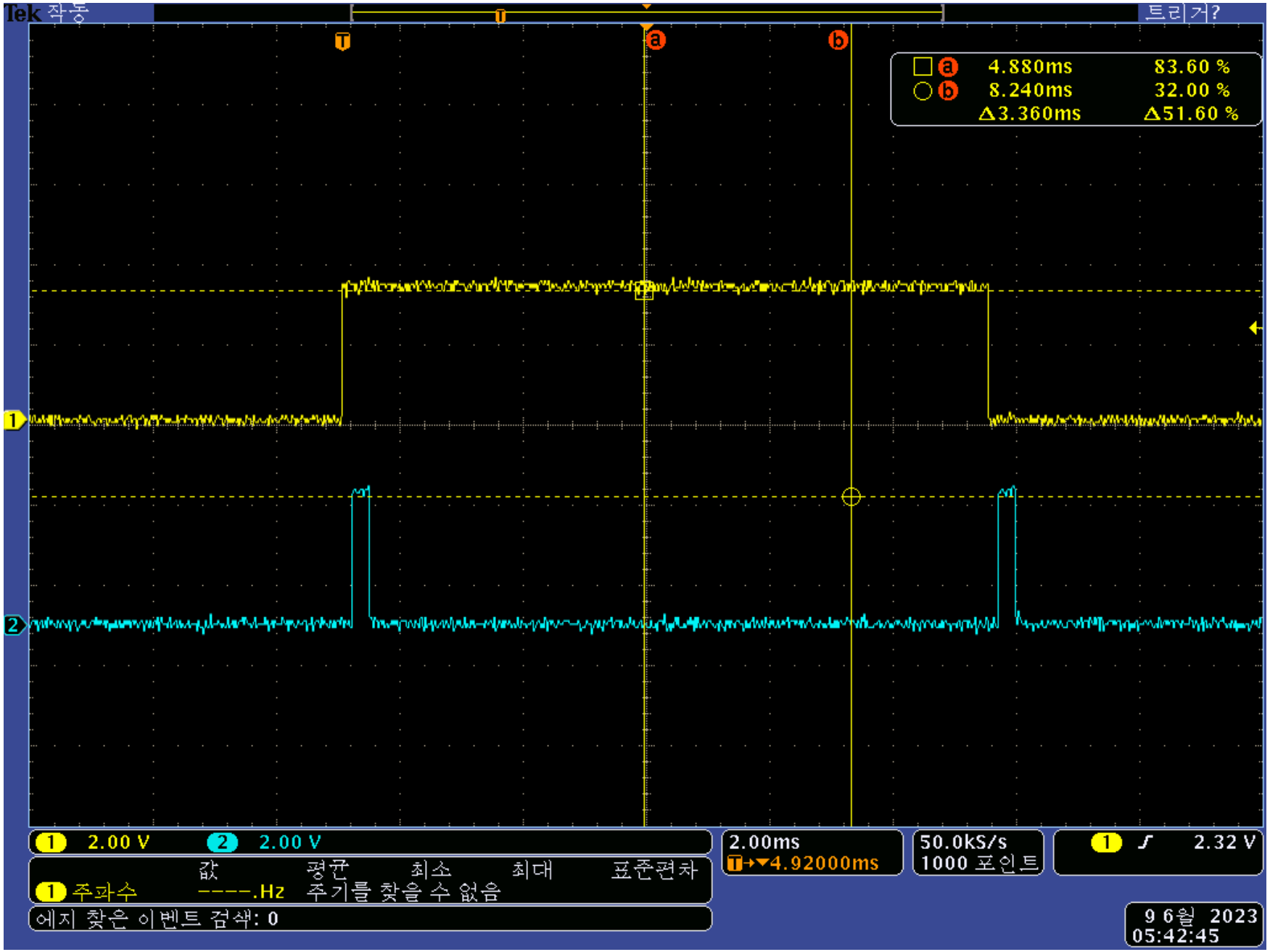
1 – CONV(46PIN) rising edge
2 – CLK(42PIN)



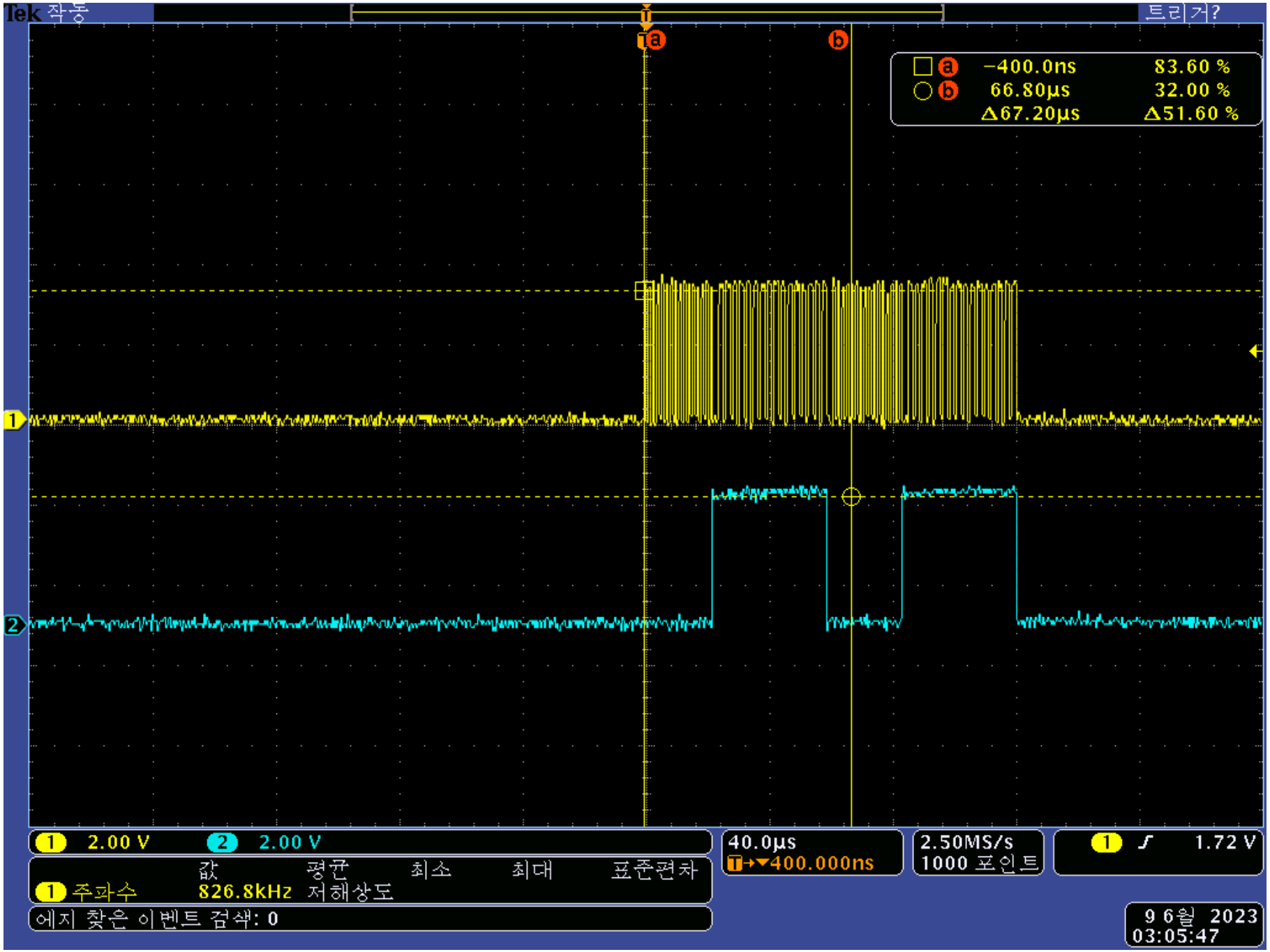
1 – CONV(46PIN)
2 – DCLK(40PIN)



1 – DCLK(40PIN)
2 – /DVALID(44PIN)



1 – CONV(46PIN)
2 – /DVALID(44PIN)



1 – DCLK(40PIN)
2 – DOUT(1PIN)