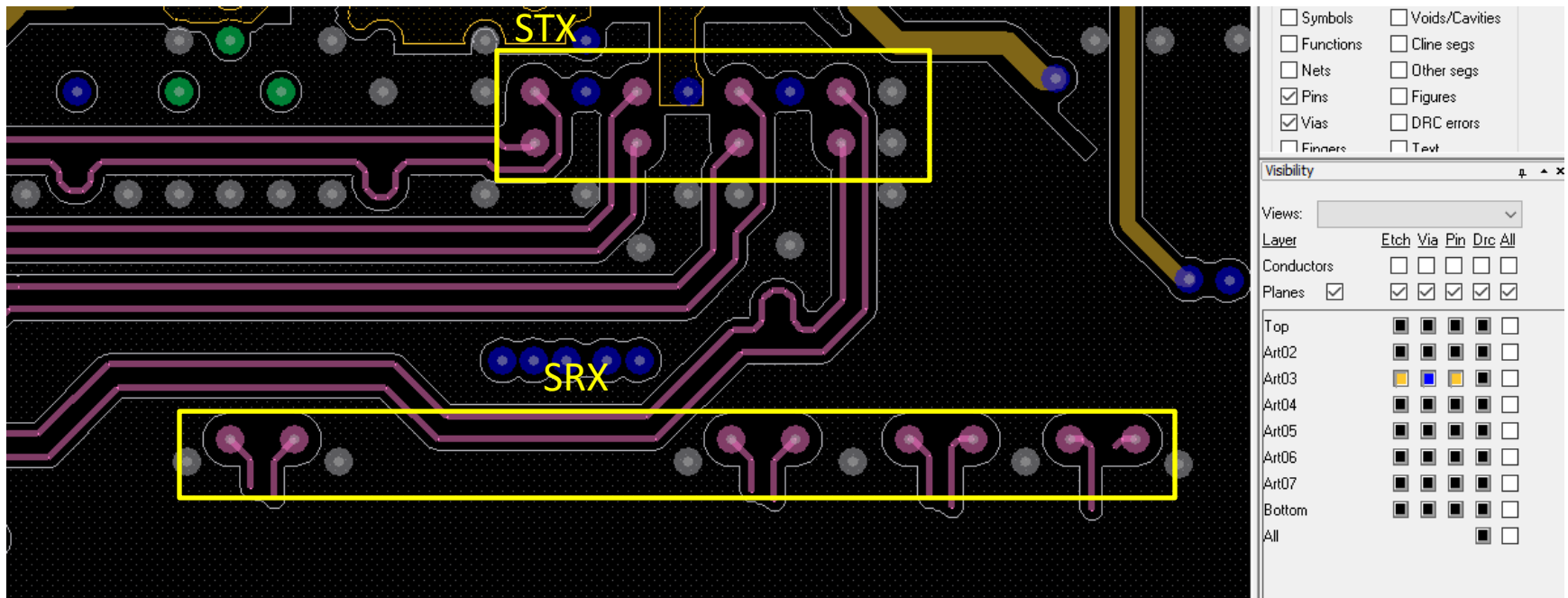


8T8R board layout Review

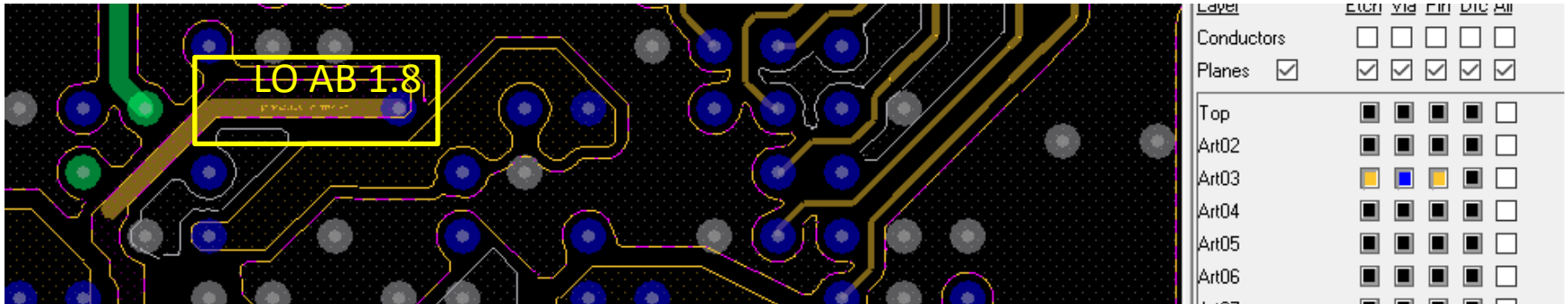
AFE77XX

Vias STX/SRX Lanes

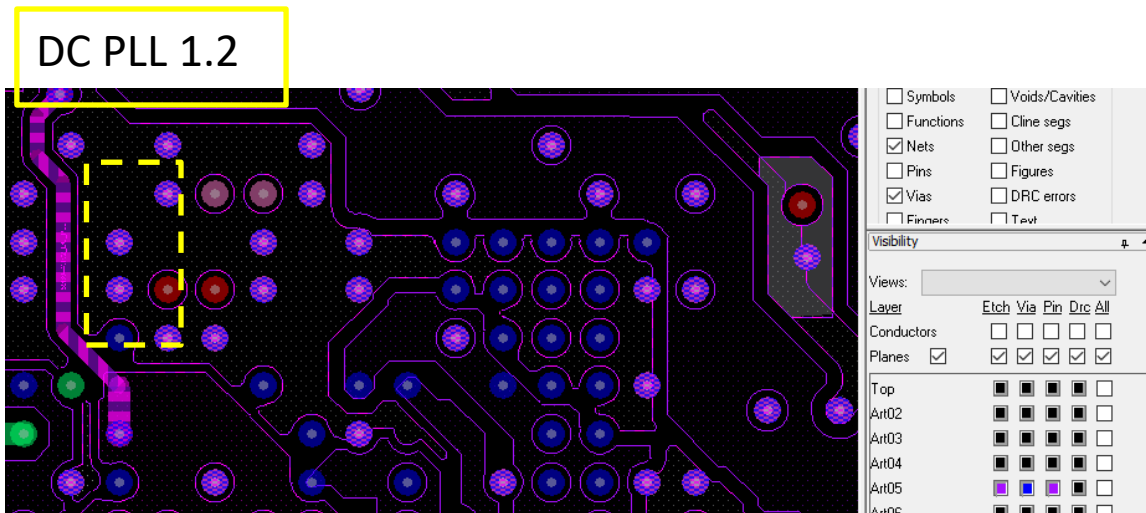


STX/SRX lanes are on Art03 and AFE is in bottom. Is there a backdrill on these Vias. Else it will create a stub and can cause reflection at 24G operation

LO/DC PLL Supplies

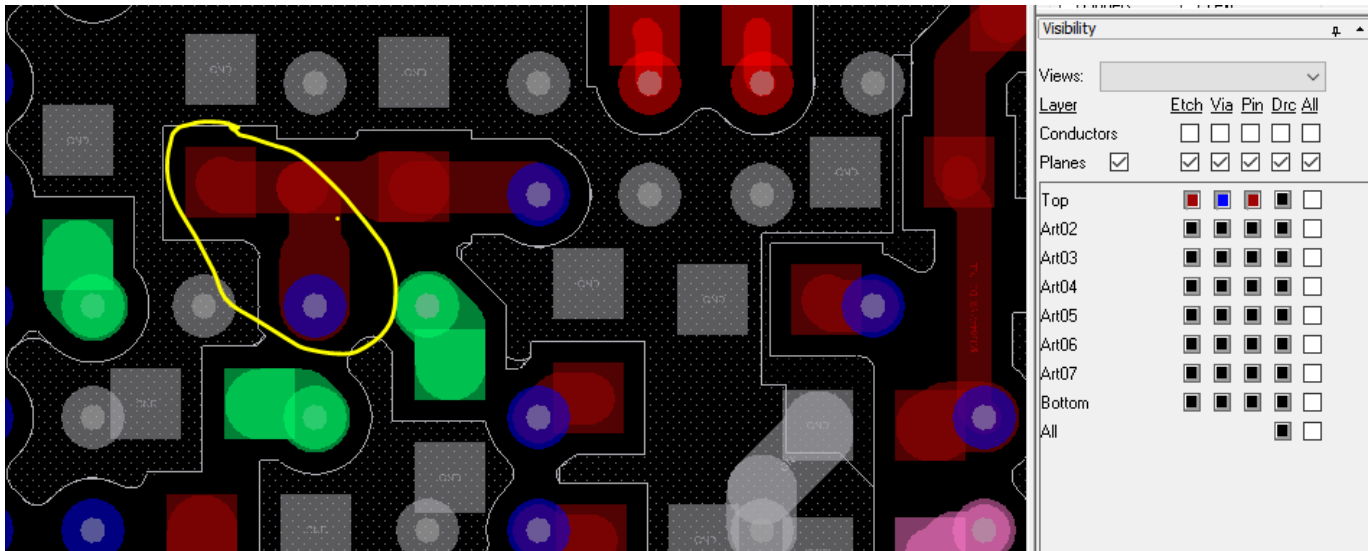


1.8V LO AB Supply shouldn't be routed as trace. A plane is preferred as it is sensitive to coupling. May cause spurs/Phase noise degradation. We cannot predict the impact



DC PLL 1.2 LO is a switching supply. Can be given larger plane in the highlighted area

TX LO mixer supply decoupling



Dcap on pin P15/R15(TX LO supply) can be moved closer to the Via
Other important pins are F13/G13(for TX) and T9/E9 (for RX). They look okay.