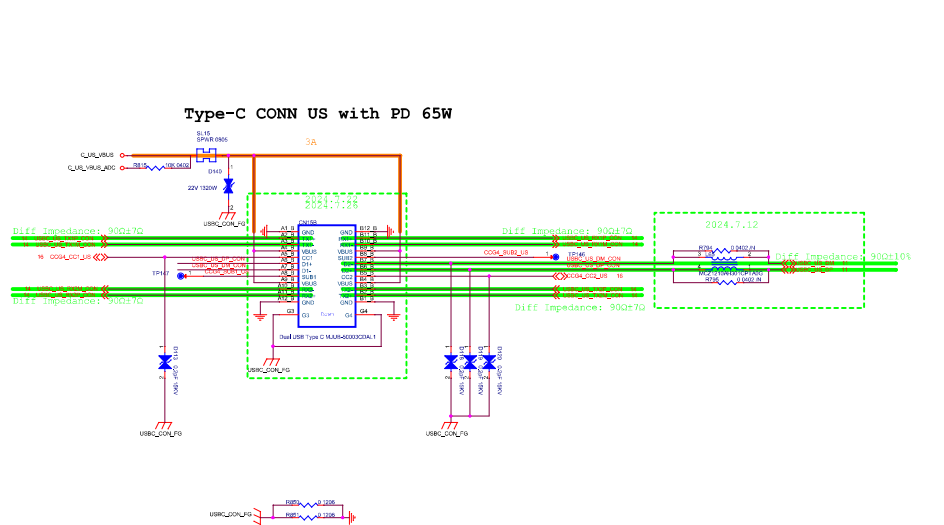
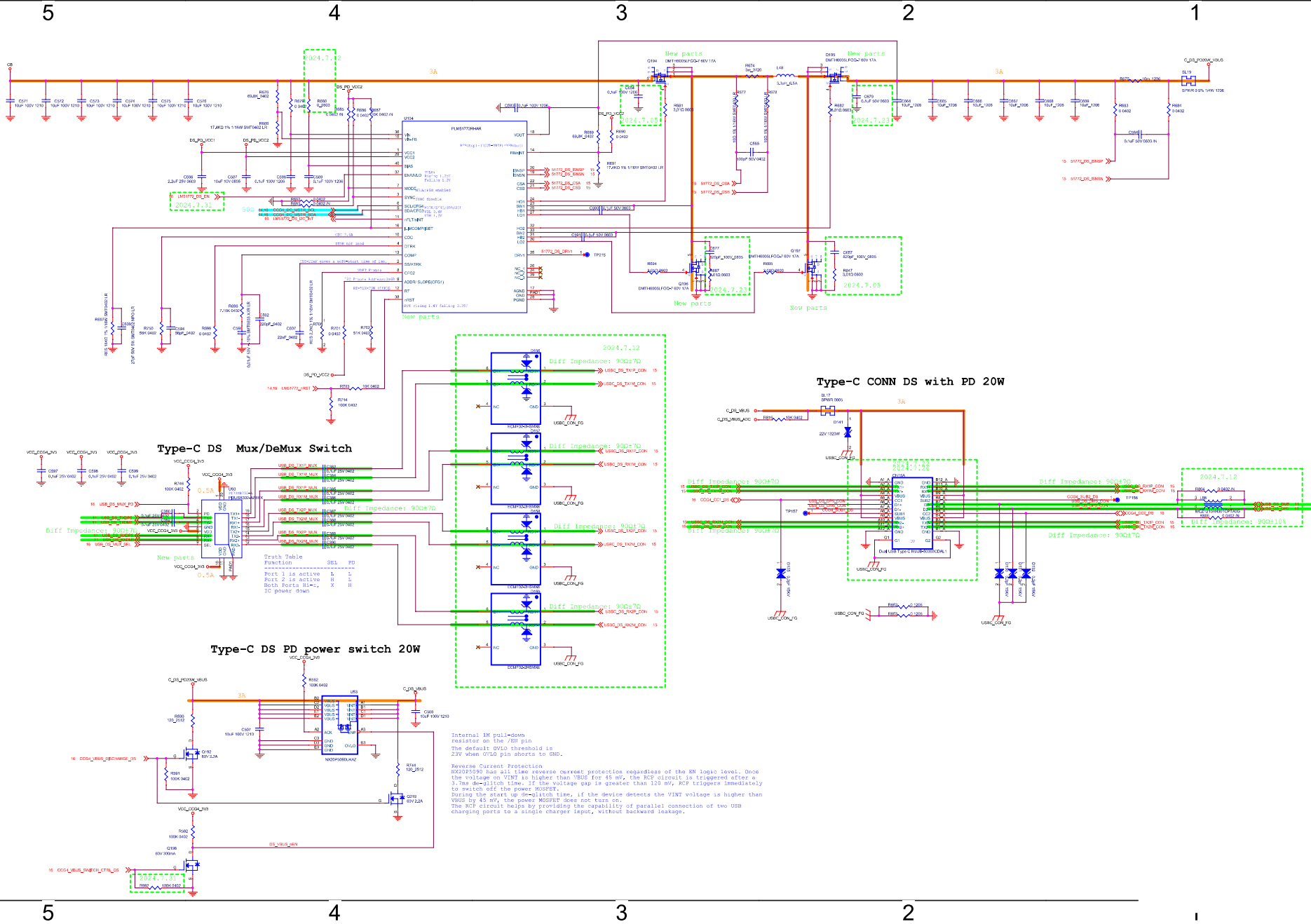


The default OVI0 threshold is 23V when OVI0 pin shorts to GND.
 Internal 1M pull-down resistor on the USB pin.

Reverse Current Protection
 RP2095090 has all time reverse current protection regardless of the RN logic level. Once the voltage on VBUS is higher than VBUS for 45 mV, the RCP circuit is triggered after a 3.7ms de-witch time. If the voltage gap is greater than 120 mV, RCP triggers immediately to switch off the power MOSFET.
 During the start up de-witch time, if the device detects the VBUS voltage is higher than VBUS by 45 mV, the power MOSFET does not turn on.
 The RCP circuit helps by providing the capability of parallel connection of two USB charging ports to a single charger input, without backward leakage.





D
C
B
A

E

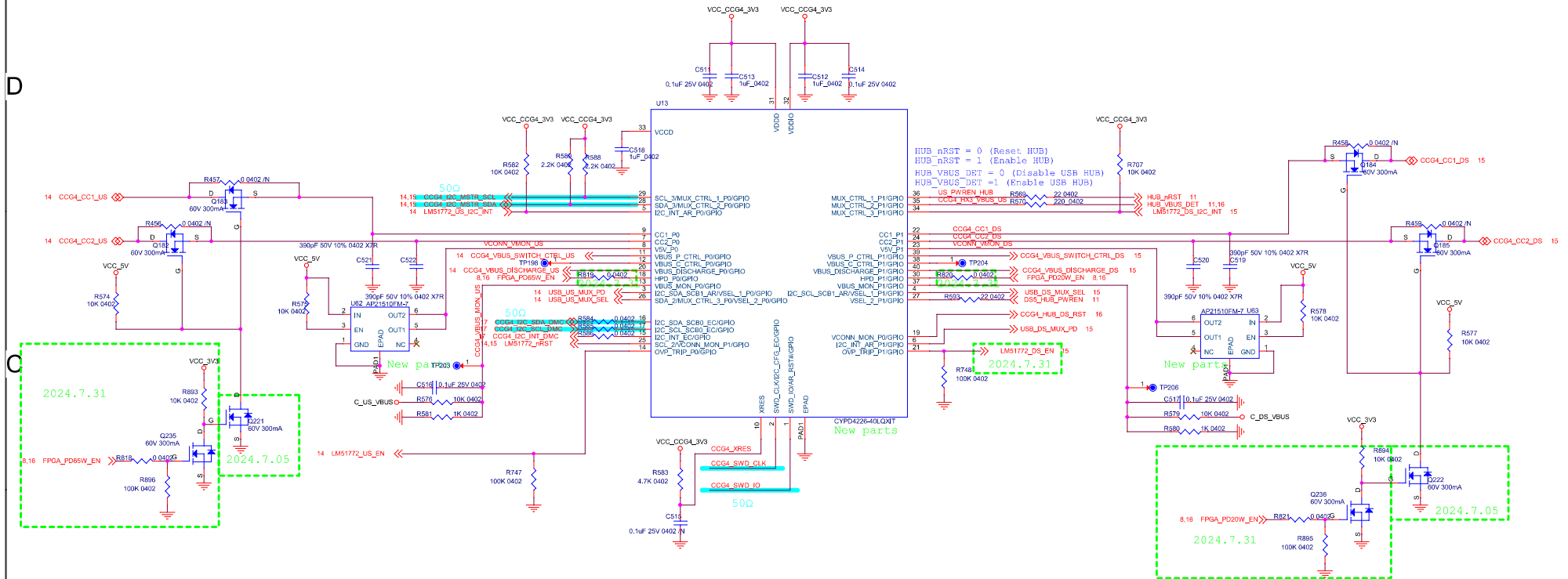
Internal IN pull-down resistor on the /EN pin. The default OVD threshold is 22V when OVD pin shorts to GND.

Reverse Current Protection
HX20P5000 has all time reverse current protection regardless of the EN logic level. Once the voltage on VINT is higher than 48mV for 48mV, the RCP circuit is triggered after a 3.7ms de-glitch time. If the voltage gap is greater than 120mV, RCP triggers immediately to switch off the power MOSFET.

During the start up de-glitch time, if the device detects the VINT voltage is higher than 48mV by 48mV, the power MOSFET does not turn on.

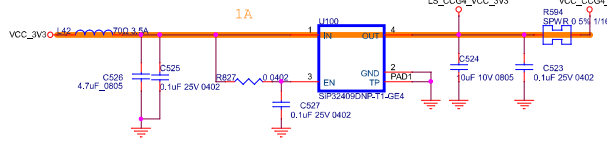
The RCP circuit helps by providing the capability of parallel connection of two USB charging ports to a single charger input, without backward leakage.

USB PD CCG4 controller



USB PD controller 3.3V Load switch

For USB PD controller
with Output discharge 280ohm



SUPERSPEED CURRENT & POWER Consumption
 1000BASE-T Full Duplex (USB SuperSpeed)
 3.3 V Supply Current Typical 256mA
 (VDDVARIO, VDD33A, VDD33_REG_IN, VDD_SW_IN = 3.3 V)
 Power Dissipation Typical 845mW

