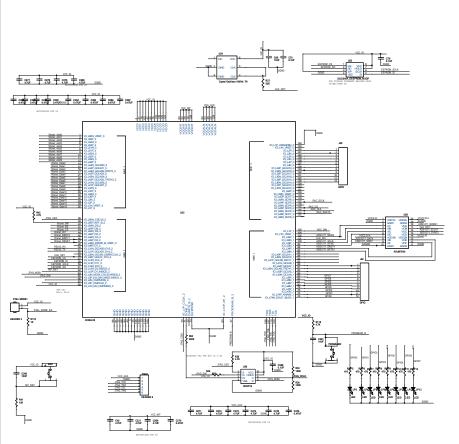
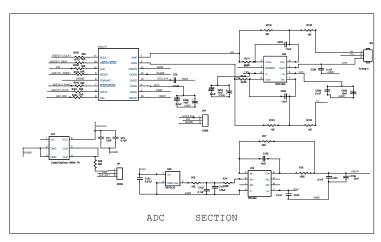
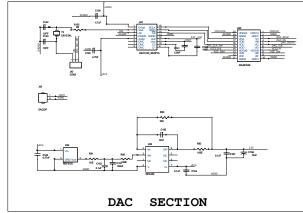


FPGA BASED DC OFFSET NULLIFICATION







FPGA SECTION

