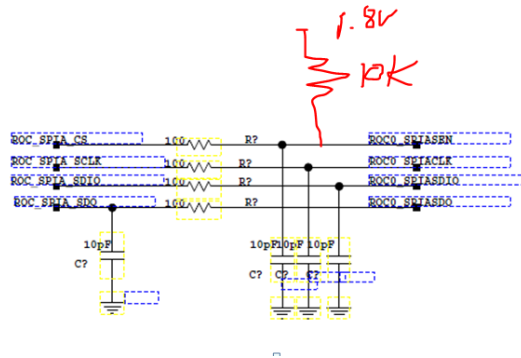
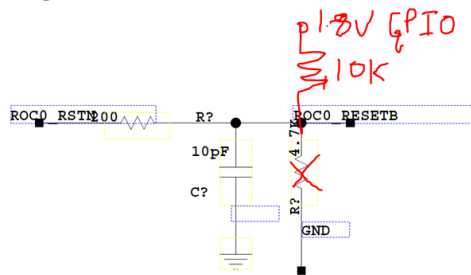


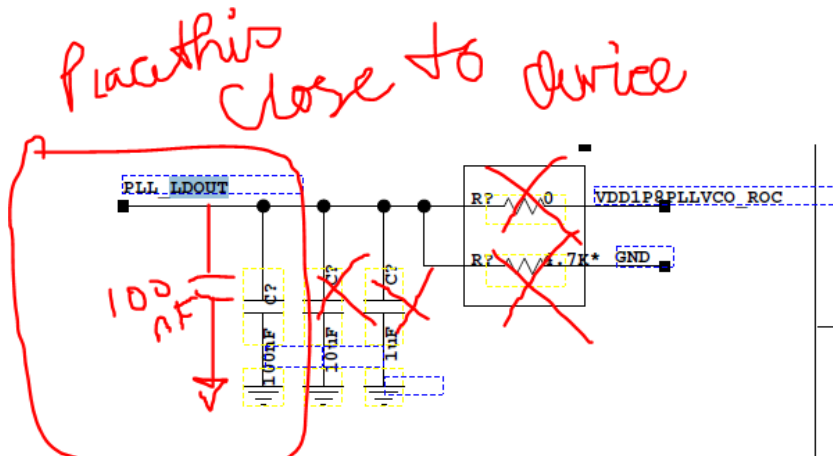
1. SPISEN does not have pull up resistors. Add pull up resistor to GPIO 1.8V supply for SPIASEN, SPIBSEN and SPI2SEN



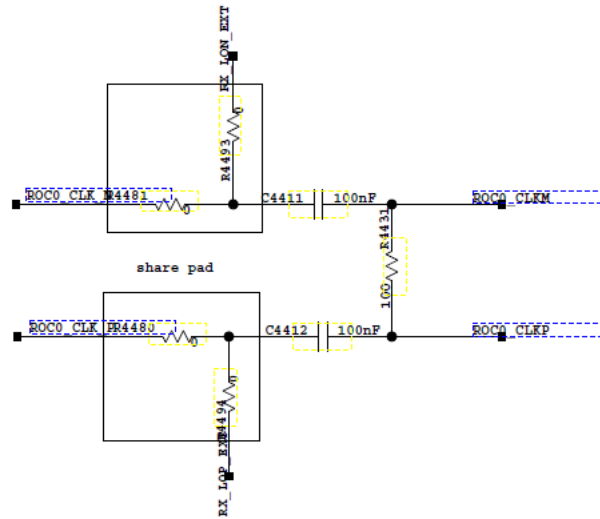
2. RESETN does not have pull up resistors. Add pull up resistor to GPIO 1.8V supply for RESETN and not ground



3. Replace with 2 numbers 100nF close to device. Remove all other components.



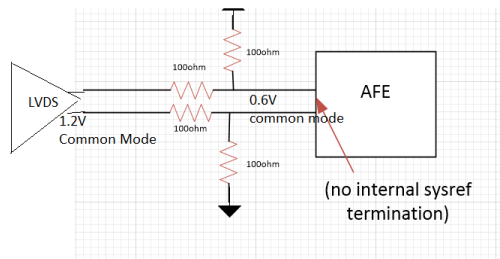
4. REFCLK has internal termination. 100 ohms below can be removed.



5. Can customer do pulse mode or they want sysref in continuous mode?

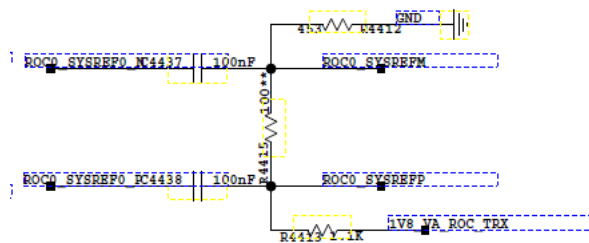
a)

For pulse mode, replace 100nF with 100 ohm , Replace R4412&R4412 with 100hm and DNI R4415. Like below configuration



b)

For sysref continuous mode, DNI resistors R4412, R4415 and R4413



6. PLL 1.8V and CLK 1.8V seem to be derived from same LDO. Recommended to follow the power tree to have separate LDOs for these nets.

