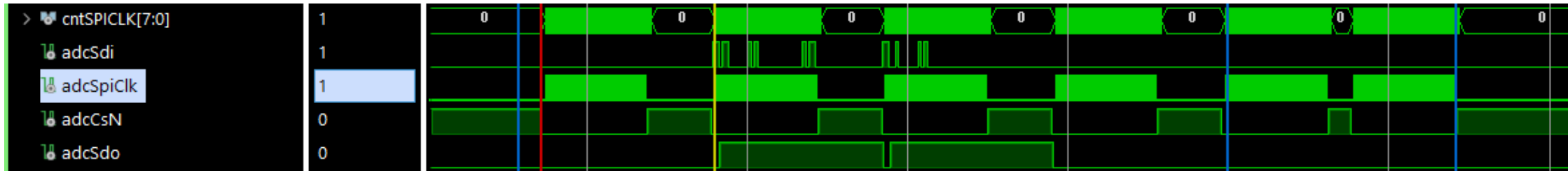
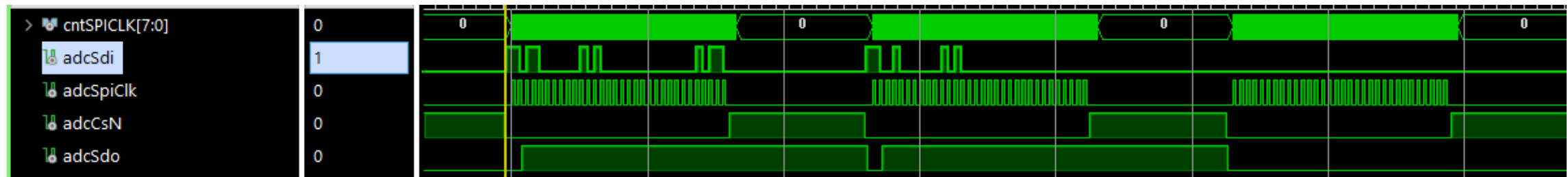


No op, Set x14, read x14, 3 consecutive no ops

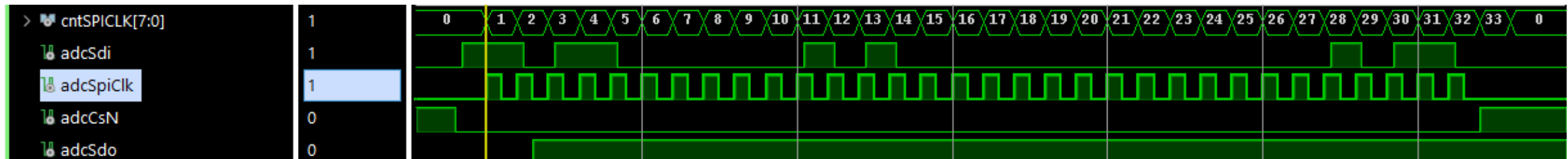


Zoomed in view of write, read, no op



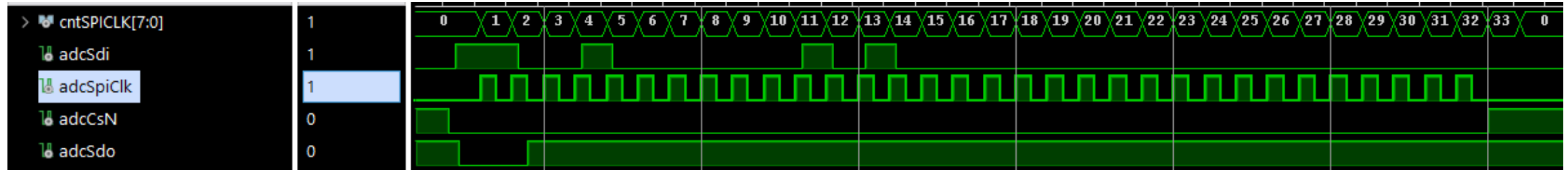
Write xD814000b, 32 spi clks using mode 0

1 us from falling edge of CONV/~CS to first SPI CLK



500ns from last spi clk to rising edge of ~CS

read xC8140000



No Op CMD

