

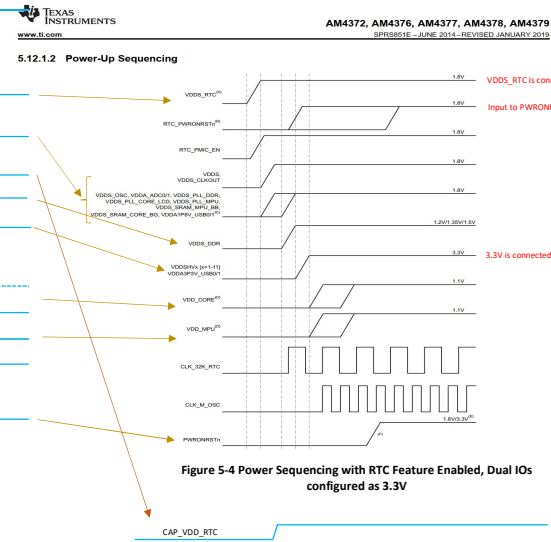
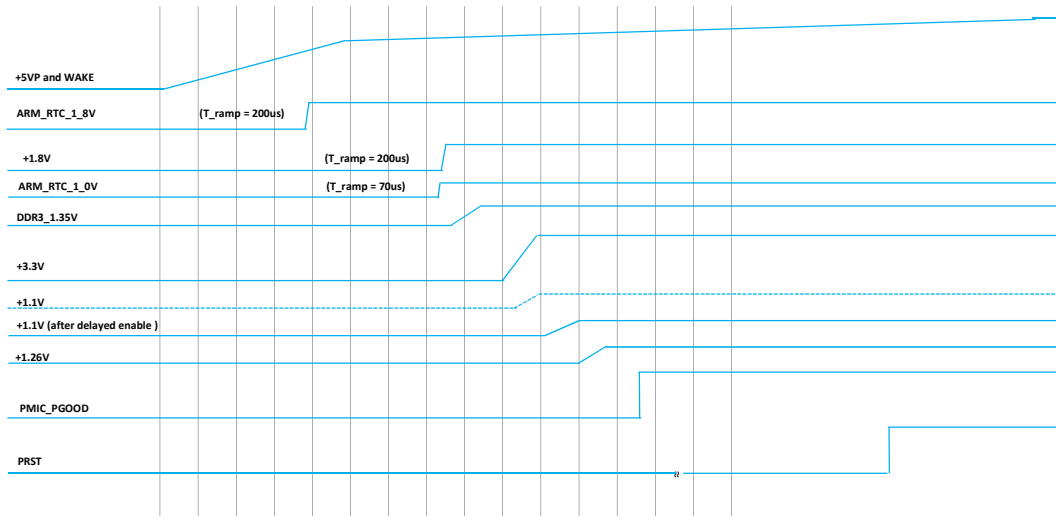
board schematic nets

waveforms observed in board
(1ms/div)

Connections to AM4372

AM4372 datasheet recommendation

Notes



Initially observed that 1.1V is rising along with 3.3V. (Is it a problem ?)
 Hence we added delay in enable of 1.1V so that the output starts rising only after 3.3V settles to final value.

VDD5_RTC is connected to RTC_KALDO_ENn in the board
 Input to PWRONRSTn is level shifted and connected to RTC_PWRONRSTn in the board

3.3V is connected to VDD53P3V_IOLDO in the board

CAP_VDD_RTC