

Layout Note:
Route all non-high speed signals on top and bottom layers. Use inner layers only for high-speed signals.

AM3352 Symbol Notes:
1. I/O Power for each pin is noted
2. Pin state at Reset (PWRONRSTn = L) and Reset Release (after PWRONRSTn L->H transition) is shown if not Z

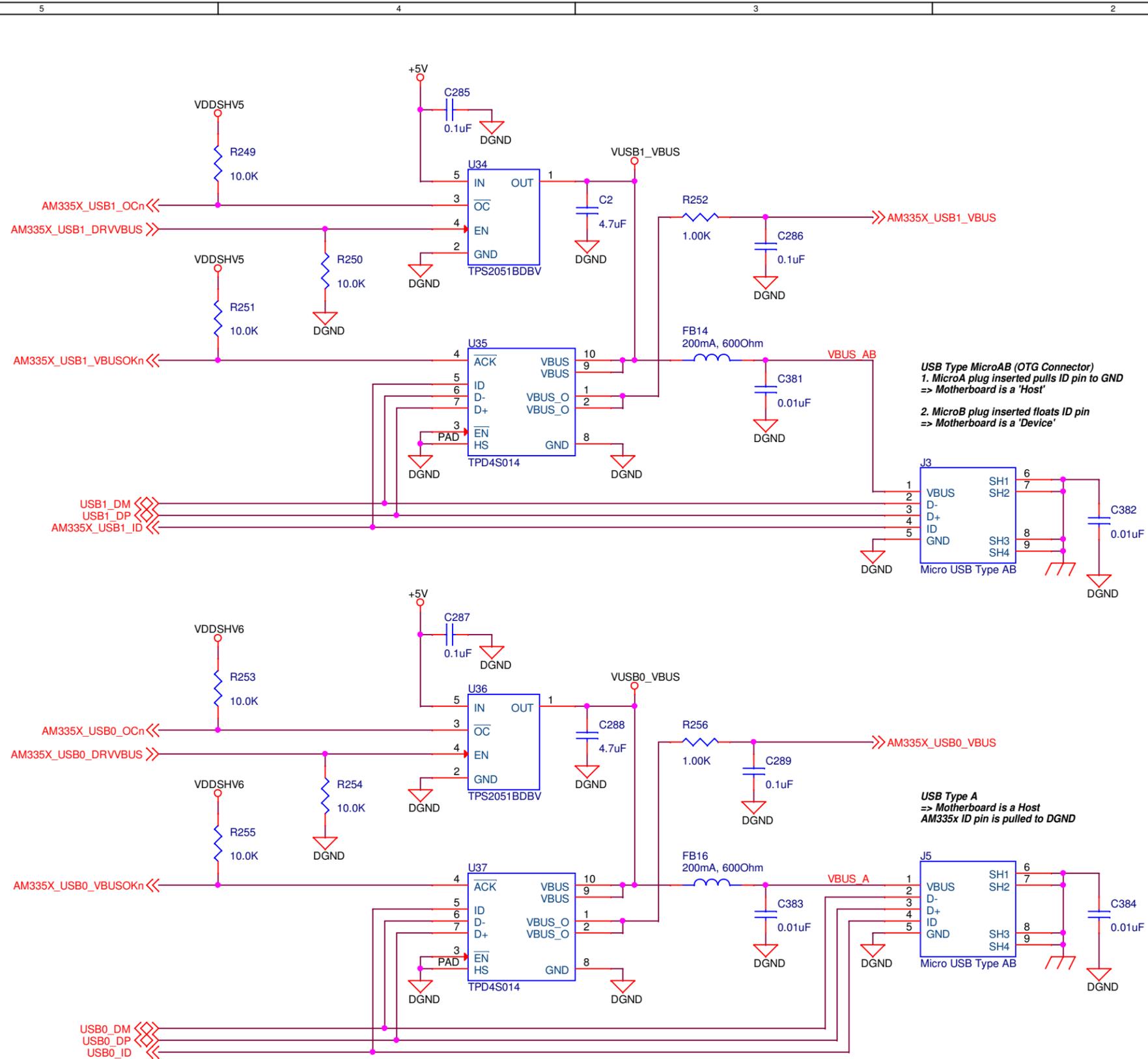
Finesse solutions for life

3501 Leonard Court
Santa Clara, CA 95054

Title: Schematic, Linux Motherboard: AM335x MPU 2 of 3

Size B Document Number F320-2017-001 Rev 2

Date: Thursday, April 02, 2015 Sheet 3 of 16



USB Type MicroAB (OTG Connector)
 1. MicroA plug inserted pulls ID pin to GND
 => Motherboard is a 'Host'
 2. MicroB plug inserted floats ID pin
 => Motherboard is a 'Device'

USB Type A
 => Motherboard is a Host
 AM335x ID pin is pulled to DGND



3501 Leonard Court
 Santa Clara, CA 95054

Title		
Schematic, Linux Motherboard: USB0 & USB1 Interface		
Size	Document Number	Rev
B	F320-2017-001	2
Date:	Thursday, April 02, 2015	Sheet 11 of 16