

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	BETA	23-Jan-2003	

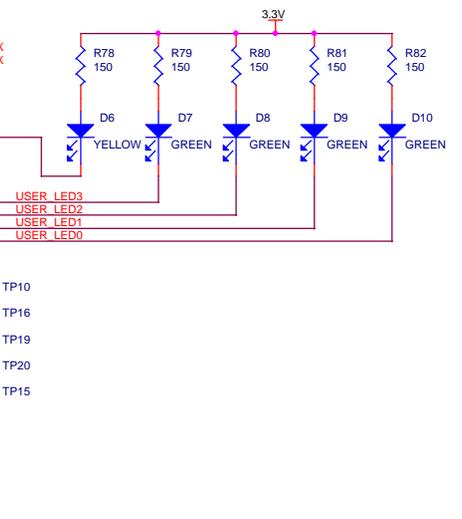
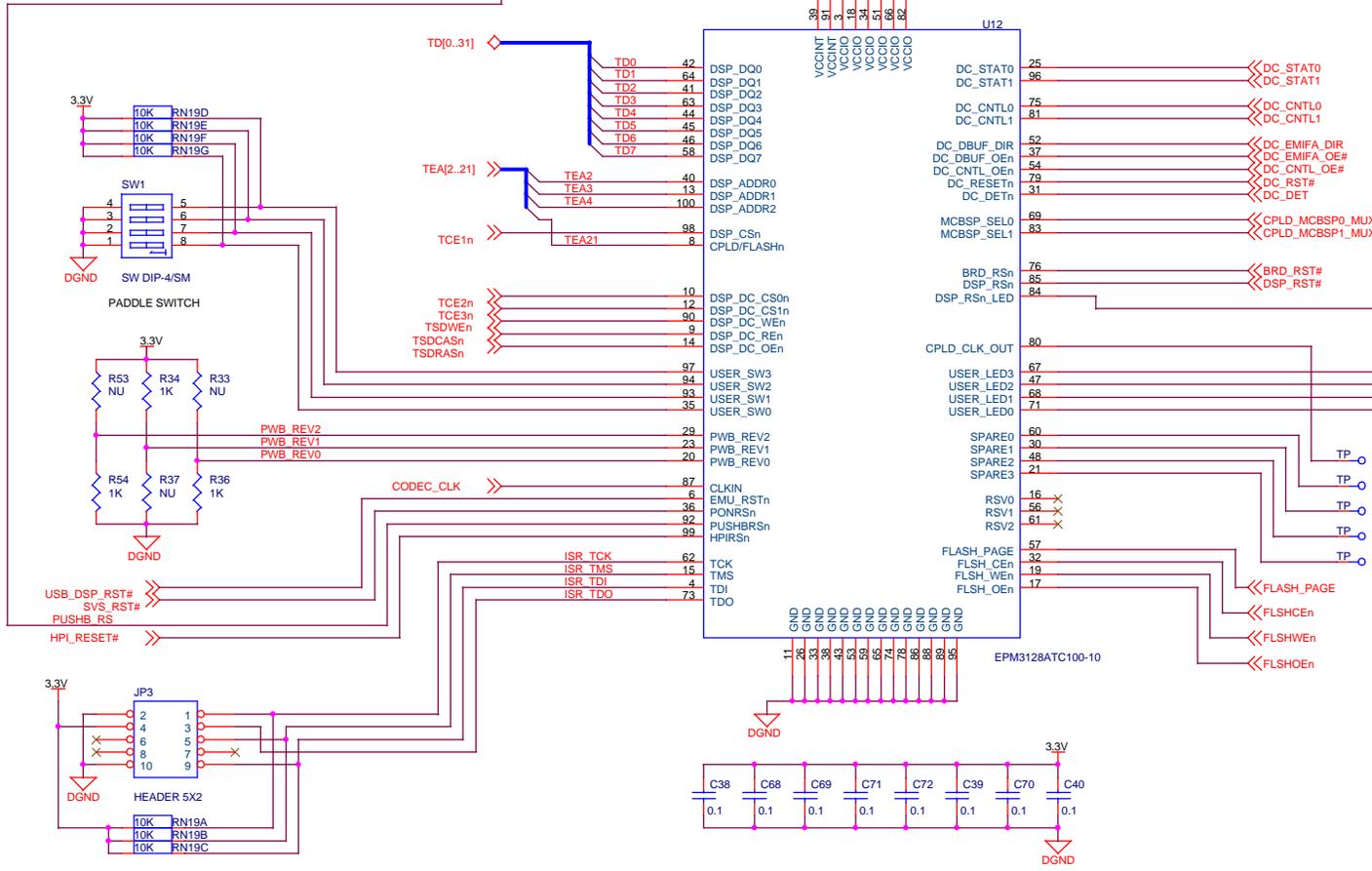
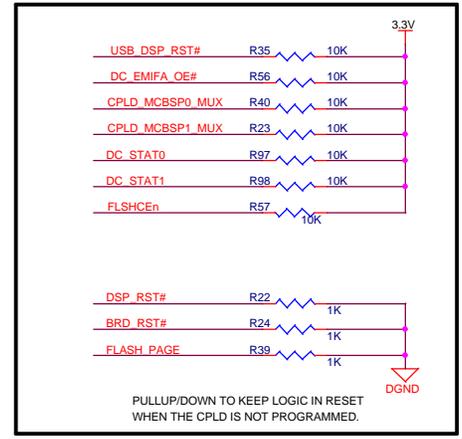
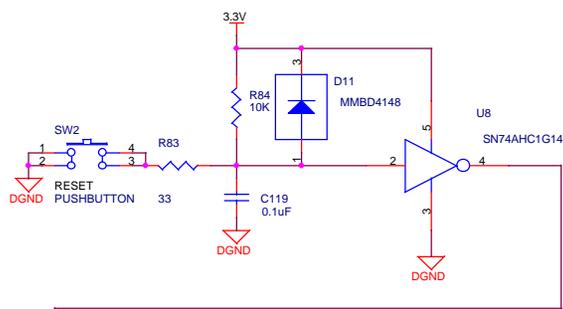
# 07-October-2003

The TMS320C6713DSK design is based on TMS320C6713 device data sheet SPRS186B and errata SPRZ173E. This schematic is subject to change without notification. Spectrum Digital Inc. assumes no liability for applications assistance, customer product design or infringement of patents described herein.

REVISION STATUS OF SHEETS								OWN	DATE
REV								CHK	DATE
SH								ENGR	DATE
REV	A	B	A	A	A	A	A	ENGR-MGR	DATE
SH	8	9	10	11	12	13	14	QA	DATE
REV		C	A	C	A	A	B	MFG	DATE
SH	1	2	3	4	5	6	7	ELSE	DATE
								APPLICATION	USED ON

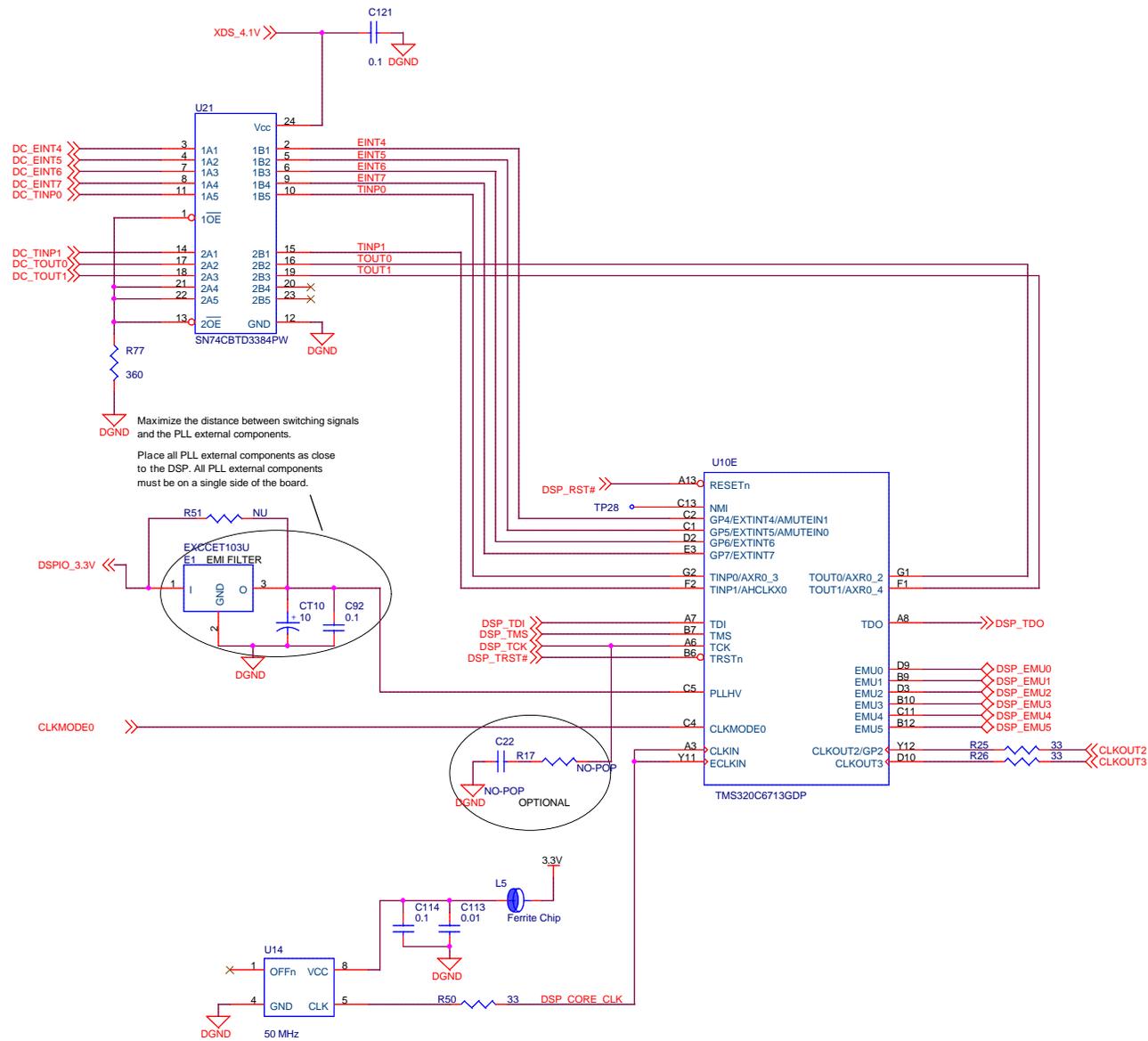
SPECTRUM DIGITAL INCORPORATED

Title			TMS320C6713 DSK
Size B	Document Number	Rev C	
	506732		
Date:	Monday, November 24, 2003	Sheet	1 of 13

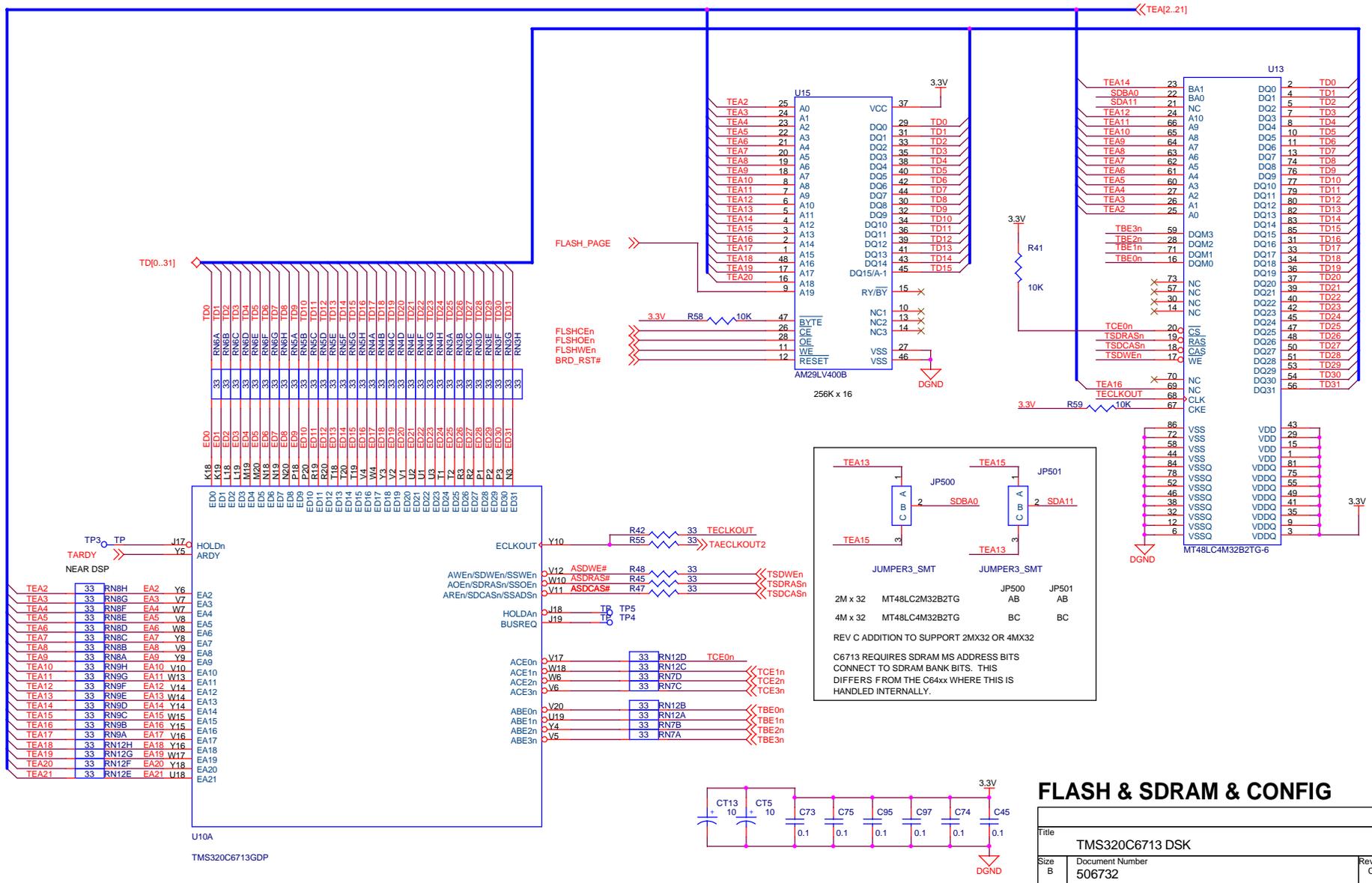


### CPLD

Title		
TMS320C6713 DSK		
Size B	Document Number	Rev C
	506732	
Date:	Monday, November 24, 2003	Sheet 2 of 13

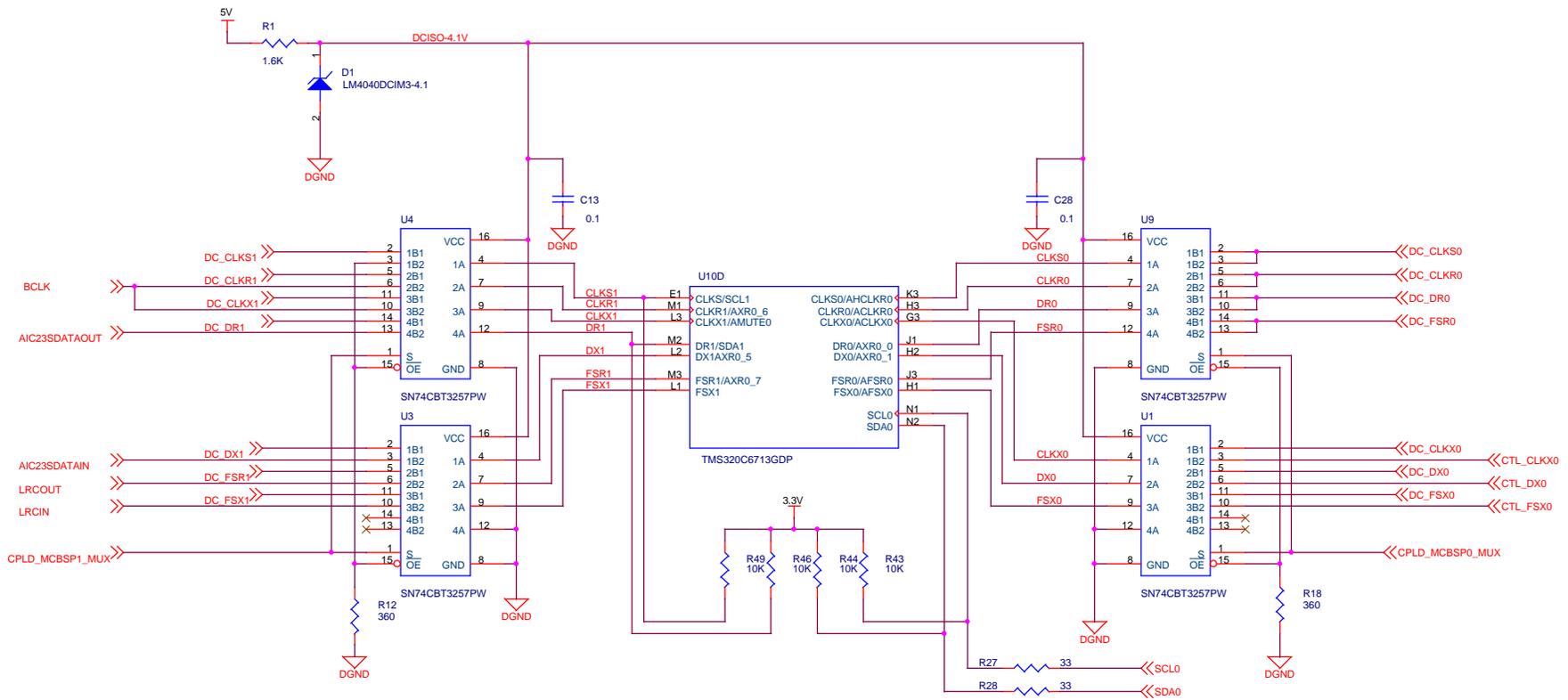


Title		
TMS320C6713 DSK		
Size	Document Number	Rev
B	506732	A
Date:	Monday, November 24, 2003	Sheet 3 of 13



## FLASH & SDRAM & CONFIG

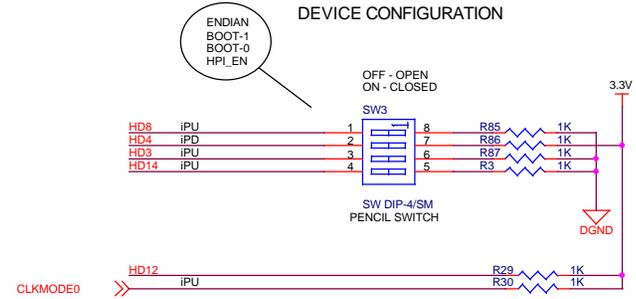
Title		
TMS320C6713 DSK		
Size	Document Number	Rev
B	506732	C
Date:	Monday, November 24, 2003	Sheet 4 of 13



## MCBSP

Title		
TMS320C6713 DSK		
Size	Document Number	Rev
B	506732	A
Date:	Monday, November 24, 2003	Sheet 5 of 13

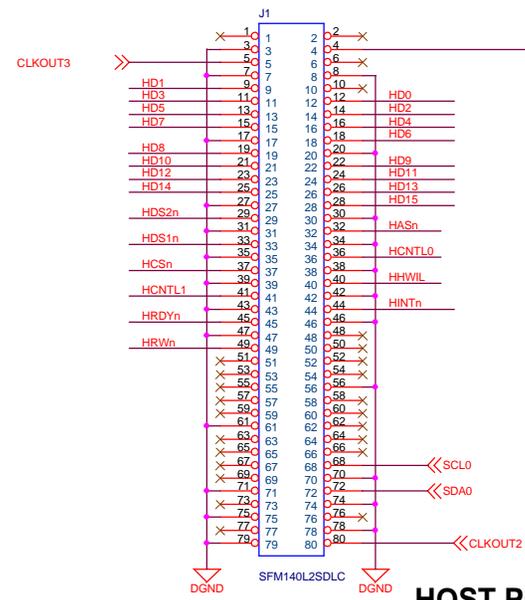
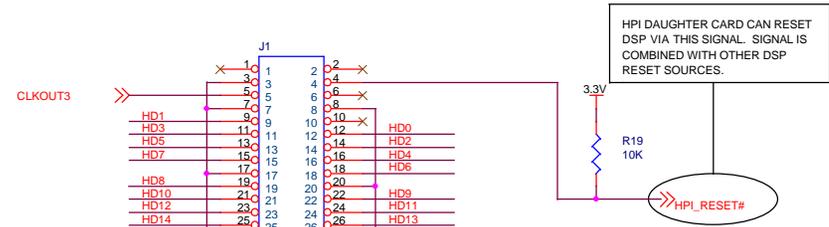
### DEVICE CONFIGURATION



### U10C

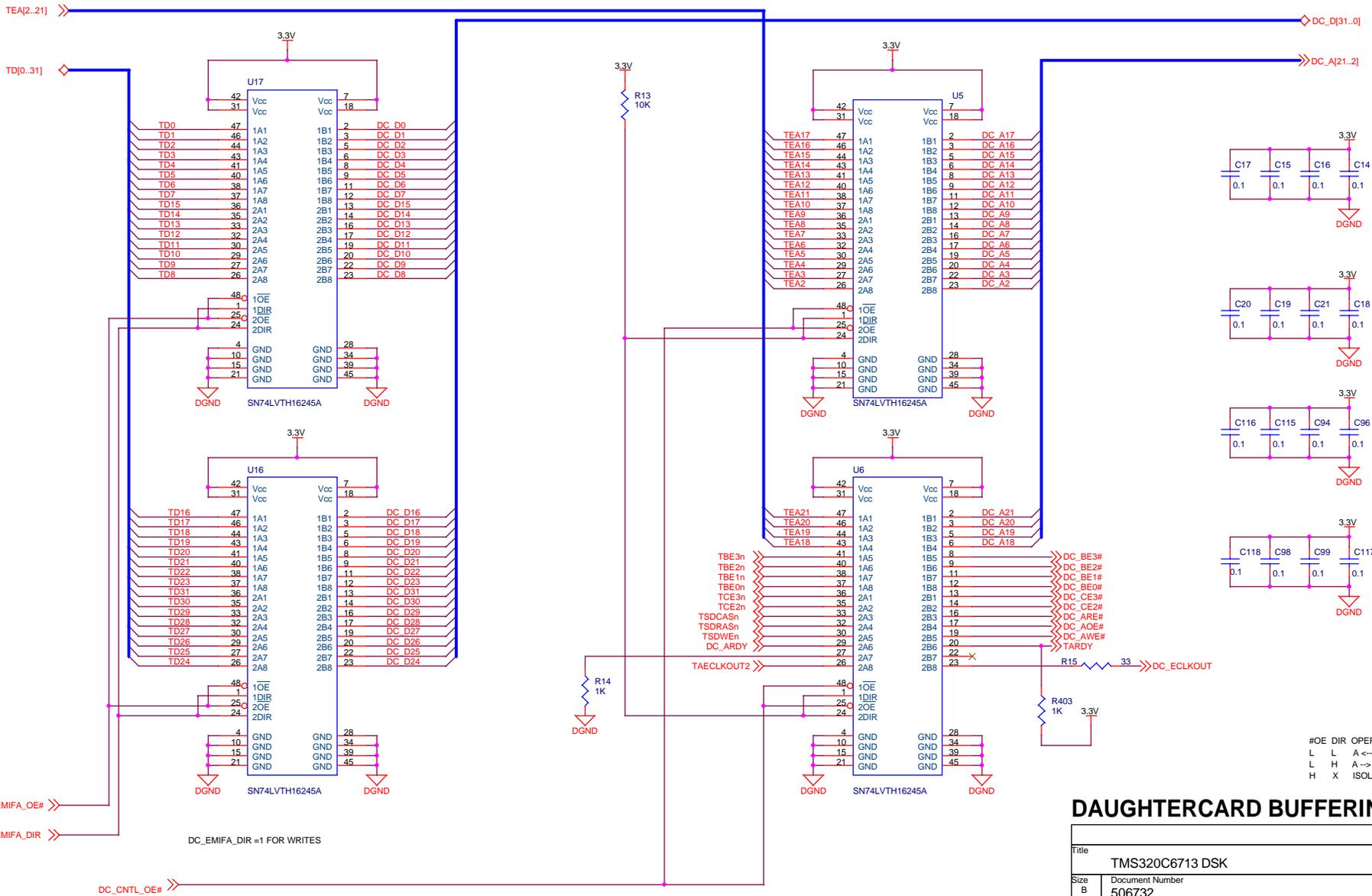
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C14	HD14
A15	HD13
C15	HD12
A16	HD11
B16	HD10
C16	HD9
B17	HD8
A18	HD7
C17	HD6
B18	HD5
C19	HD4
C20	HD3
D18	HD2
D20	HD1
E20	HD0
J20	HINTn
G19	HCNTL1
G18	HCNTL0
H20	HHWIL
G20	HRWn
E18	HASn
E20	HCSn
E19	HDS1n
F18	HDS2n
H19	HRDYn
HD15/GP15	
HD14/GP14	
HD13/GP13	
HD12/GP12	
HD11/GP11	
HD10/GP10	
HD9/GP9	
HD8/GP8	
HD7/GP3	
HD6/ACLKR1	
HD5/ACLKX1	
HD4/GP0	
HD3/AMUTE1	
HD2/AFSX1	
HD1/AXR1_7	
HD0/AXR1_4	
HINTn/GP1	
HCNTL1/AXR1_1	
HCNTL0/AXR1_3	
HRWn/AFSR1	
HRWn/AXR1_0	
HASn/ACLKX1	
HCSn/AXR1_2	
HDS1n/AXR1_6	
HDS2n/AXR1_5	
HRDYn/ACLKR1	

TMS320C6713GDP



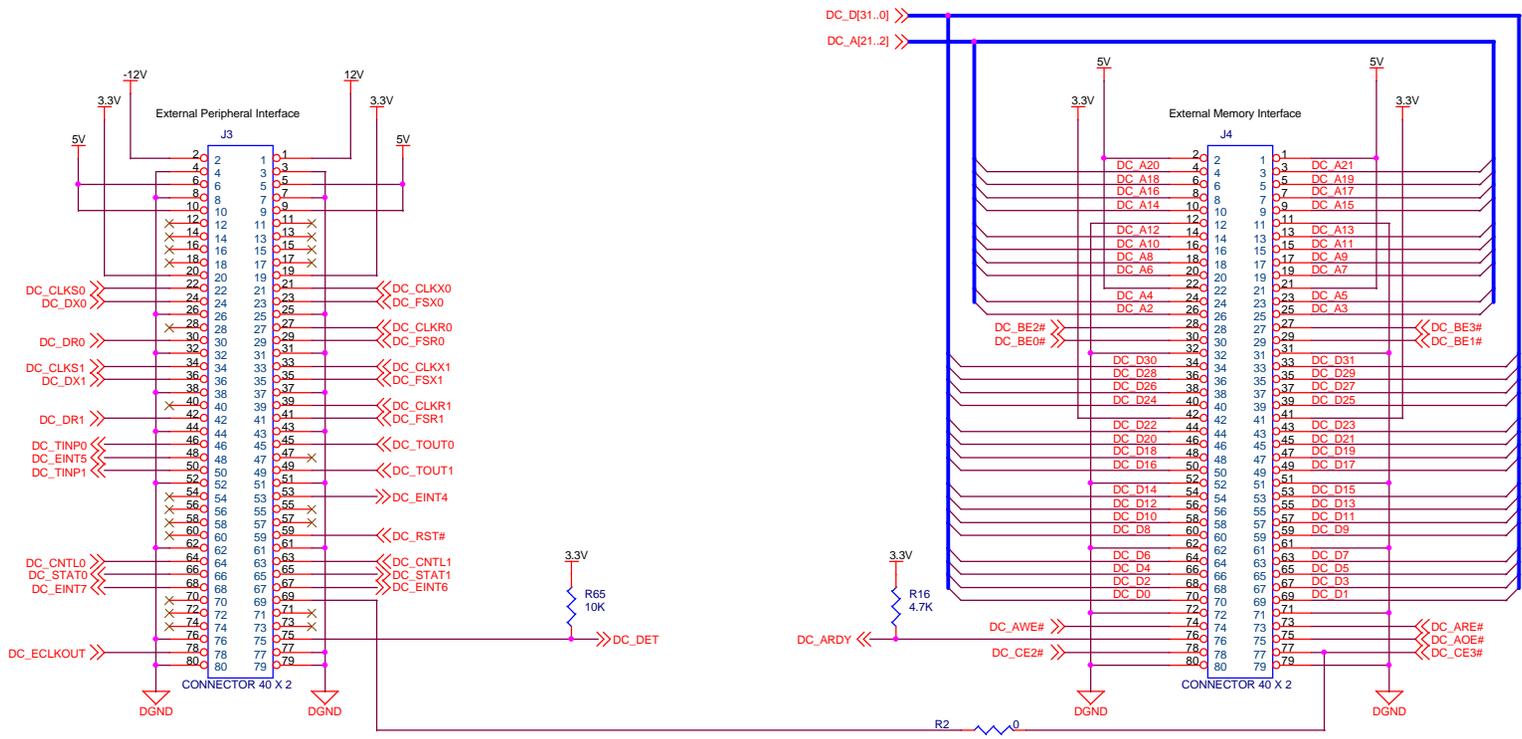
### HOST PORT/McASP

Title		
TMS320C6713 DSK		
Size B	Document Number	Rev A
	506732	
Date:	Monday, November 24, 2003	Sheet 6 of 13



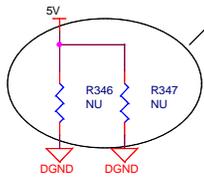
## DAUGHTERCARD BUFFERING

Title		
TMS320C6713 DSK		
Size	Document Number	Rev
B	506732	B
Date:	Monday, November 24, 2003	Sheet 7 of 13



## DAUGHTERCARD I/F

Title		
TMS320C6713 DSK		
Size	Document Number	Rev
B	506732	A
Date:	Monday, November 24, 2003	Sheet 8 of 13

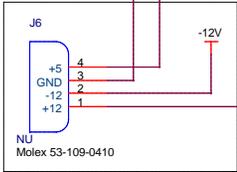
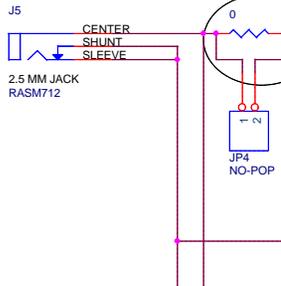


OPTIONAL, POWER SUPPLY  
LOAD RESISTORS, 2512  
BODY

0.025 OHMS FOR POWER  
MEASUREMENT

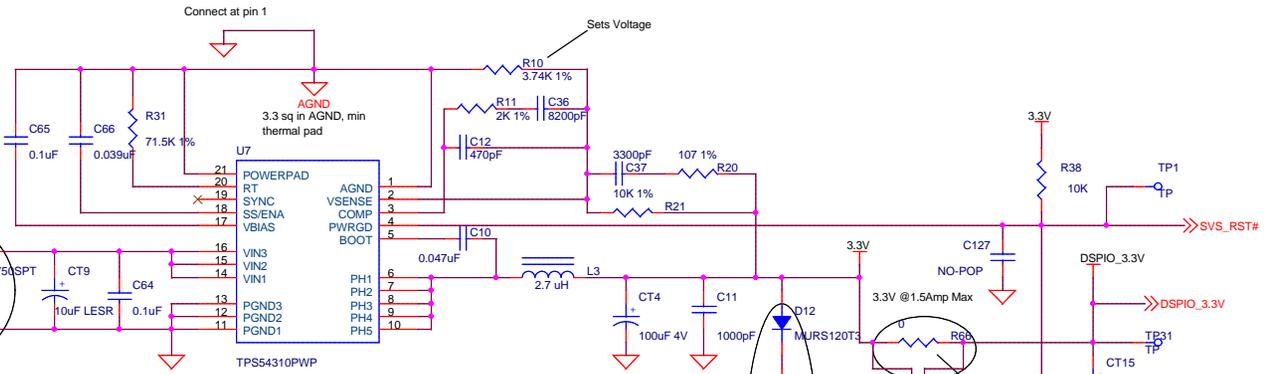
SYSTEM POWER MEASUREMENT  
POINT S. R IS 2512 BODY, 6 VIAS  
FROM PAD TO PLANE

**POWER INPUT**

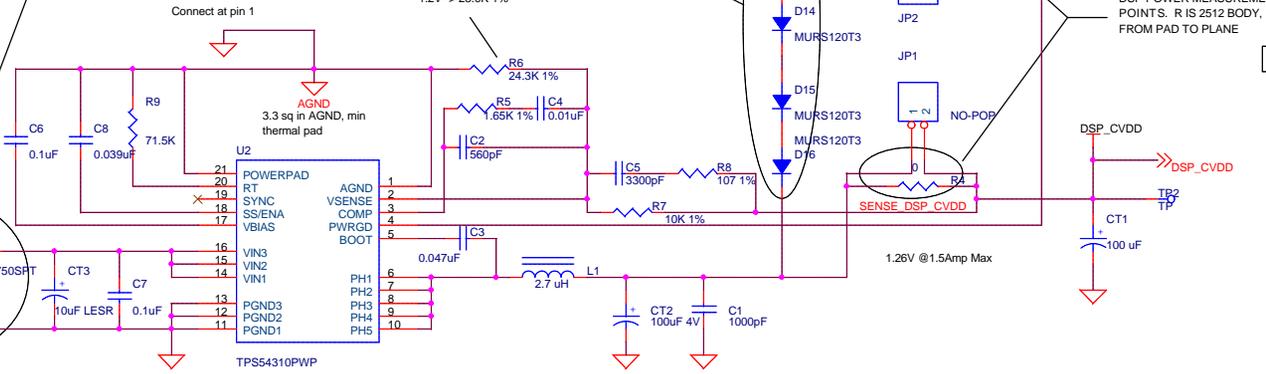


TO BE POPULATED BY  
THE USER IF  
NEEDED.

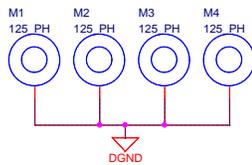
**WARNING:**  
DO NOT SUPPLY POWER TO BOTH  
POWER CONNECTORS AT THE  
SAME TIME!



EMI SUPPRESSION. LOCATE NEAR EACH REGULATOR.  
6 VIAS FROM PAD TO PLANE OR DIRECT TIE.



**DAUGHTERCARD STANDOFF GROUNDING**



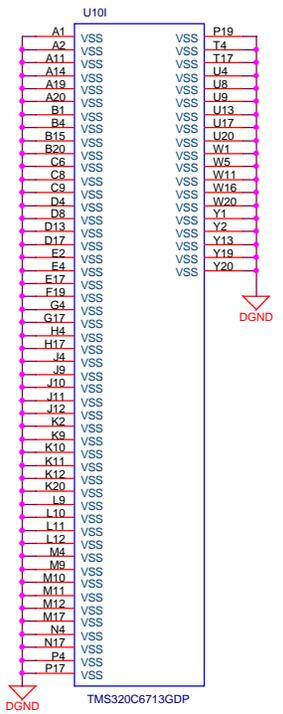
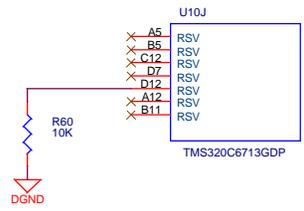
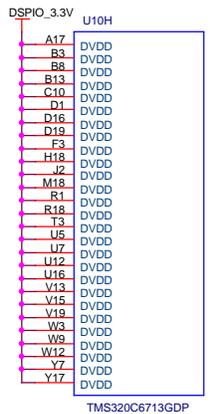
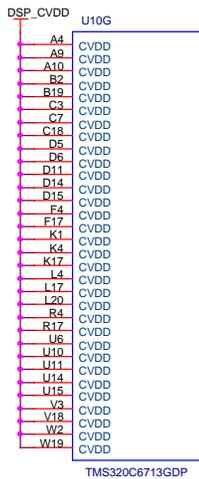
KEEP TRACES A MINIMUM  
OF 0.070 INCHES FROM  
THESE HOLES.

EACH REGULATOR CAN SUPPLY UP TO 3A OF  
CURRENT. HOWEVER COMPONENT VALUES  
HAVE BEEN SELECTED FOR 1.5A OPERATION.

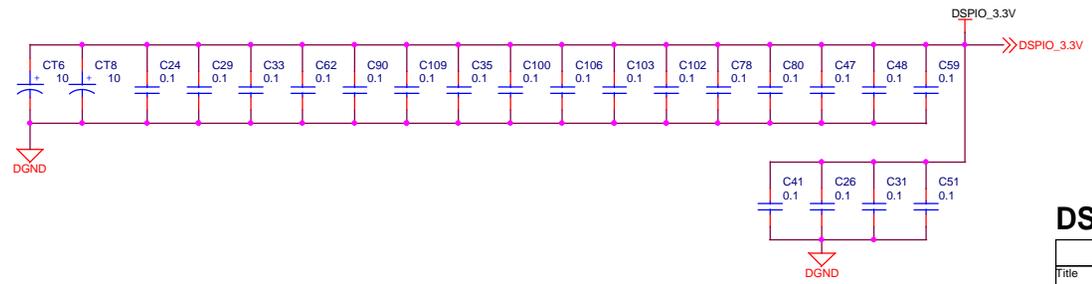
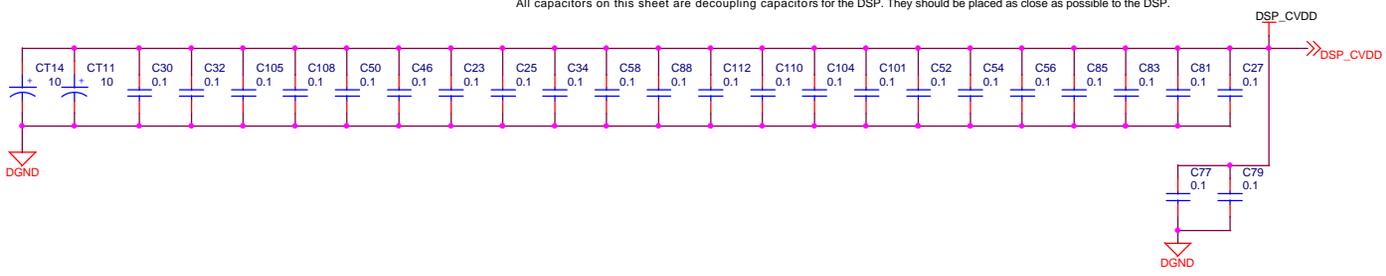
VALUES CALCULATED WITH SWIFT DESIGN TOOL 2.0.  
FOLLOW TPS54310 EVM LAYOUT

**POWER**

Title		
TMS320C6713 DSK		
Size B	Document Number	Rev B
	506732	
Date:	Monday, November 24, 2003	Sheet 9 of 13



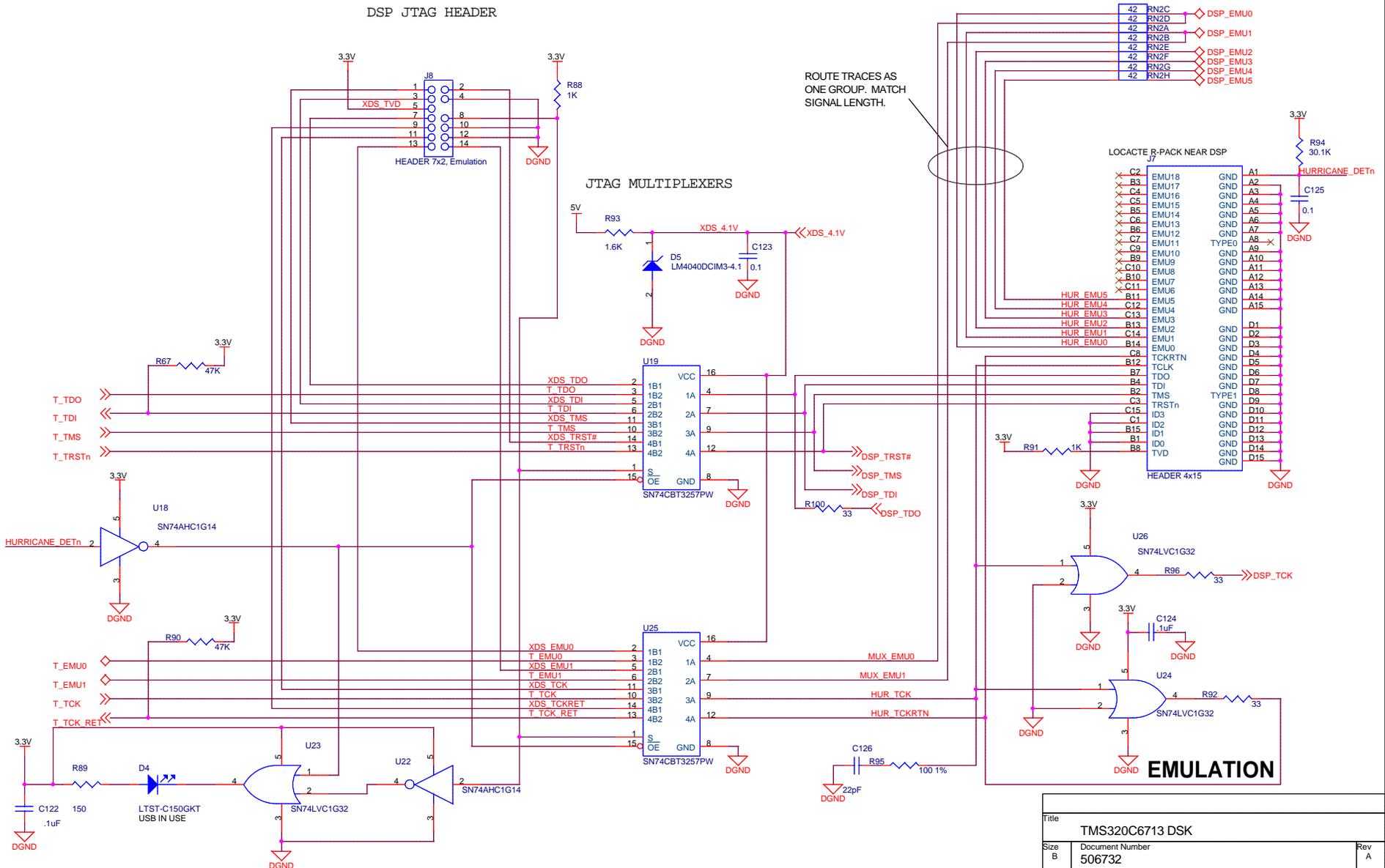
All capacitors on this sheet are decoupling capacitors for the DSP. They should be placed as close as possible to the DSP.



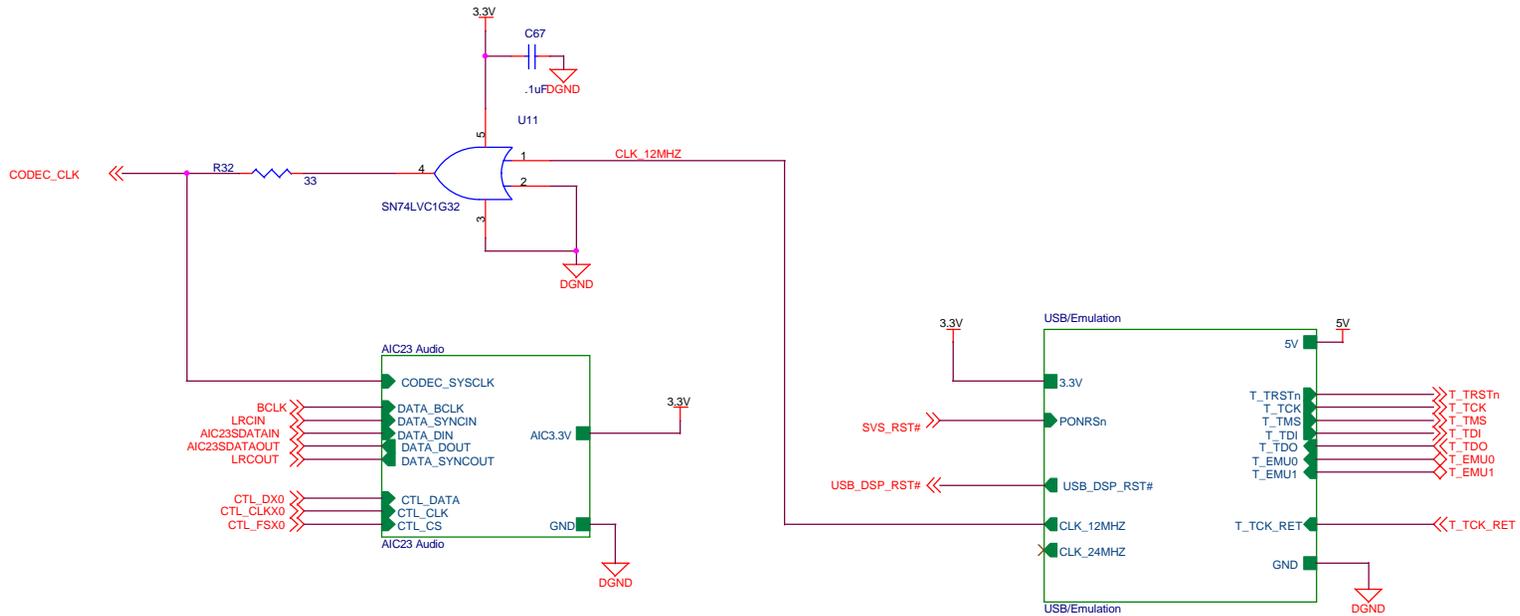
## DSP POWER & DECOUPLING

Title		
TMS320C6713 DSK		
Size	Document Number	Rev
B	506732	A
Date:	Monday, November 24, 2003	Sheet 10 of 13

DSP JTAG HEADER



Title		
TMS320C6713 DSK		
Size B	Document Number	Rev A
	506732	
Date:	Monday, November 24, 2003	Sheet 11 of 13



## Hierarchical Blocks

Title		
TMS320C6713 DSK		
Size	Document Number	Rev
B	506732	A
Date:	Monday, November 24, 2003	Sheet 12 of 13