

AM62Q-LP STARTER KIT EVM

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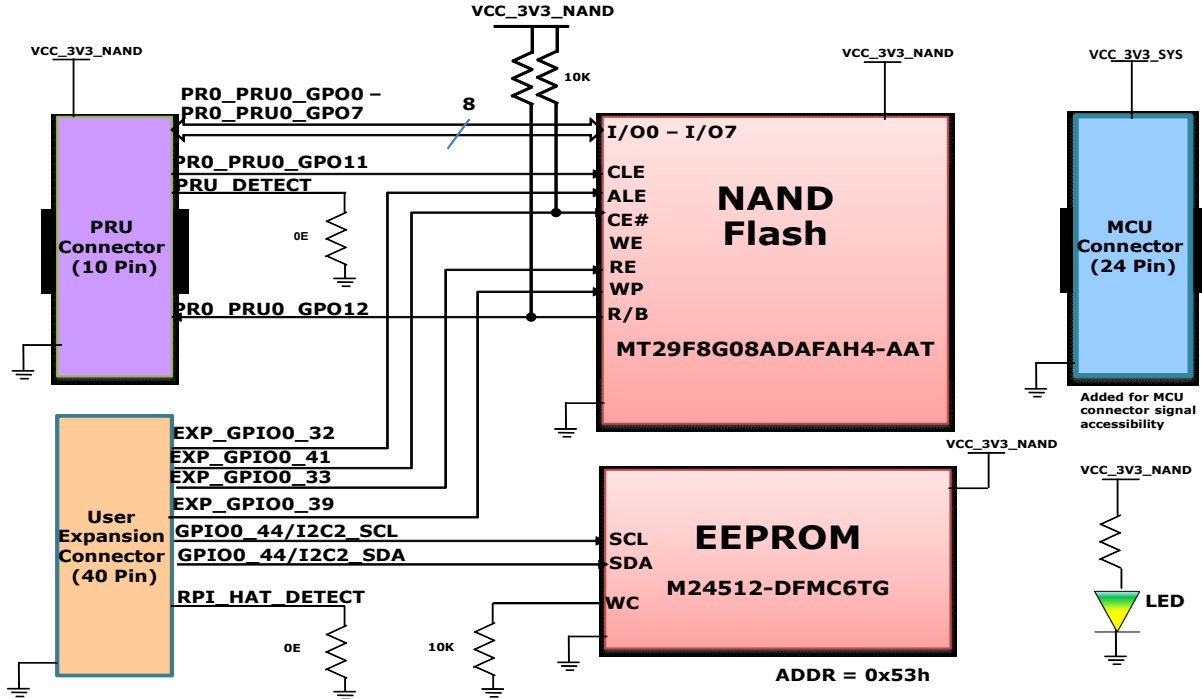
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REVISION HISTORY

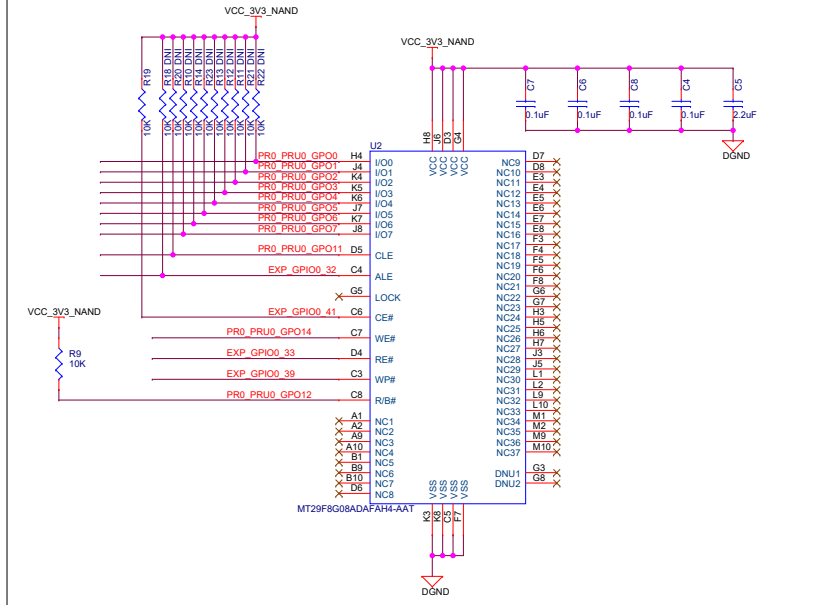
VER #	DATE	DESCRIPTION OF CHANGES	AUTHOR	REVIEWED BY	APPROVED BY
0.01	21 SEP 2022	Drafted from E1 Version EXP_GPIO0_32 this signal changed to J1.30 PR0_PRU0_GPO11, PR0_PRU0_GPO14 swapped on J3	Mistral Design Team	Deepak NS	Krishna Prasad A
1	21 SEP 2022	Released	Mistral Design Team	Deepak NS	Krishna Prasad A

BLOCK DIAGRAM AM62Q-LP-SKEVM X8 NAND EXPANSION BOARD

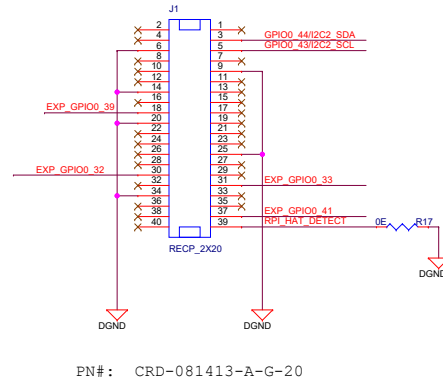


X8 NAND INTERFACE

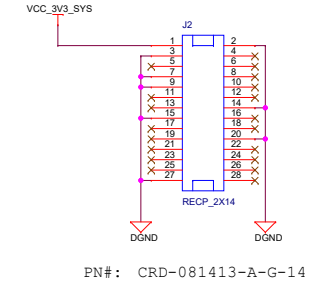
NAND DEVICE



USER EXPANSION CONNECTOR

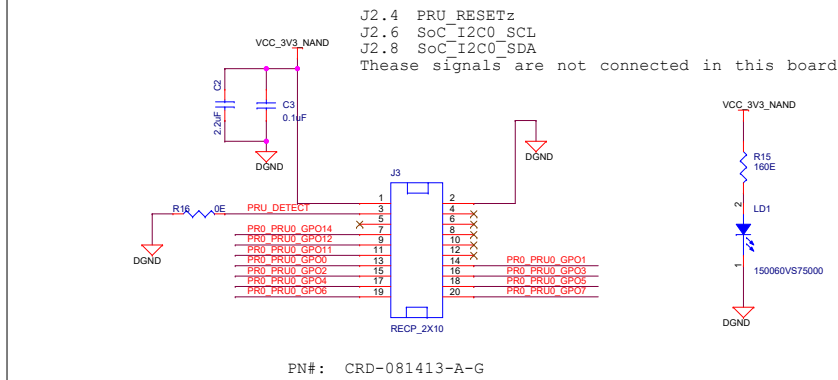


MCU HDR

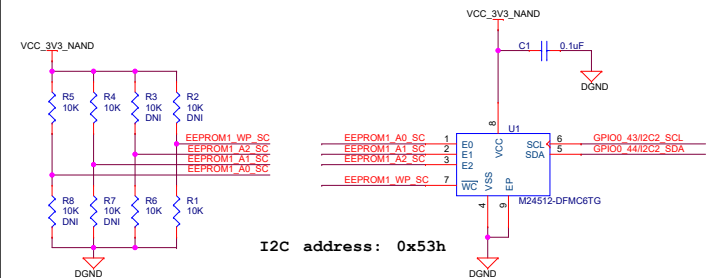


Note: This is used for bringout the MCU HDR Signals from Main board while mounting X8 NAND Expansion card

PRU HEADER



EEPROM



Designed for TI by Mistral Solutions Pvt Ltd



Title: x8 NAND

Size	PROC143E1 x8 NAND EXP CARD	Rev	
C		E2	
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HARDWARE SCHEMATICS

ASSEMBLY NOTES

1. All MSL components should be baked as per JEDEC standard.
2. PCB should be baked at 120 degree for 8 hours.
3. Board assembly must comply with workmanship standards. IPC-A-610 Class 2, unless otherwise specified.
4. These assemblies are ESD sensitive, ESD precautions shall be observed.
5. These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
6. Provide serial numbers to the assembled boards for identification.
7. The assembled board are wrapped in ESD Covers(individual) and packed securely before shipment.

BARE PCB



LOGOs

PCB
LOGO
Texas Instruments

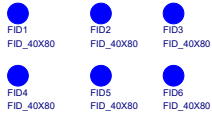
PCB
LOGO

For Evaluation only; not FCC approved for resale
For Evaluation only; not FCC approved for resale

PCB
LOGO
WEEE Mark

PCB
LOGO
CE Mark

FIDUCIALS



LABELS

Board Serial No.



Assembly Revision



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Title HARDWARE SCHEMATICS

Size PROC143E1 x8 NAND EXP CARD

Date: Wednesday, September 21, 2022

Rev E2

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