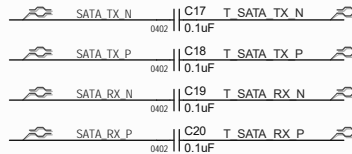
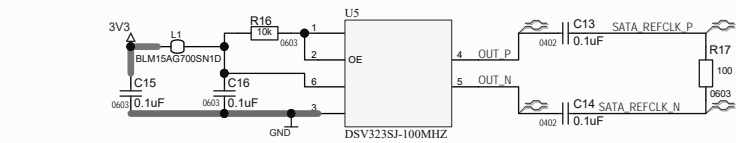








VP DIN 15 VSYNCUHP1 HD/UUP D 7	V18	VPF DIN15_VSYNCUHP1_HD7/UUP_CH1_D7	V18	VPF_DOUT15VLC0_D_15UUP_XD7/GPI07/7B007	V18
VP DIN 14 VSYNCUHP1 HD/UUP D 6	V19	VPF DIN14_VSYNCUHP1_HD6/UUP_CH1_D6	V19	VPF_DOUT14VLC0_D_14UUP_XD6/GPI07/8B006	V19
VP DIN 13 FIELDUHP1 HD/UUP D 5	T16	VPF DIN13_FIELDUHP1_HD5/UUP_CH1_D5	T16	VPF_DOUT13VLC0_D_13UUP_XD5/GPI07/9B005	T16
VP DIN 12UHP1 HD/UUP D 4	R18	VPF DIN11UHP1_HD3/UUP_CH1_D3	R18	VPF_DOUT11VLC0_D_12UUP_XD4/GPI07/8B004	R18
VP DIN 10UHP1 HD/UUP D 2	R19	VPF DIN10_UHP1_HD2/UUP_CH1_D2	R19	VPF_DOUT11VLC0_D_10UUP_XD3/GPI07/8B003	R19
VP DIN 9UHP1 HD/UUP D 0	R15	VPF DIN9UHP1_HD1/UUP_CH1_D1	R15	VPF_DOUT10VLC0_D_9UUP_XD2/GPI07/7B002	R15
VP DIN 8UHP1 HD/UUP D 0	P17	VPF DIN8UHP1_HD0/UUP_CH1_D0/GPI08	P17	VPF_DOUT10VLC0_D_8UUP_XD1/GPI07/1B001	P17
VP DIN 7UHP1 HD 15UUP D 15/RMI TXD 1	U18	VPF DIN7UHP1_HD15UUP_CH1_D15RMI_TXD[1]	U18	VPF_DOUT7VLC0_D_7UUP_XD15/GPI08[0]	U18
VP DIN 6UHP1 HD 14UUP D 14/RMI TXD 0	V16	VPF DIN6UHP1_HD14UUP_CH1_D14RMI_TXD[0]	V16	VPF_DOUT6VLC0_D_6UUP_XD14/GPI08[0]	V16
VP DIN 5UHP1 HD 13UUP D 13/RMI TXEN	R14	VPF DIN5UHP1_HD13UUP_CH1_D13RMI_TXEN	R14	VPF_DOUT5VLC0_D_5UUP_XD13/GPI07[1]	R14
VP DIN 4UHP1 HD 12UUP D 12/RMI RXD 1	W16	VPF DIN4UHP1_HD12UUP_CH1_D12RMI_RXD[1]	W16	VPF_DOUT4VLC0_D_4UUP_XD12/GPI07[12]	W16
VP DIN 3UHP1 HD 11UUP D 11/RMI RXD 0	V17	VPF DIN3UHP1_HD11UUP_CH1_D11RMI_RXD[0]	V17	VPF_DOUT3VLC0_D_3UUP_XD11/GPI07[11]	V17
VP DIN 2UHP1 HD 10UUP D 10/RMI RXERR	W17	VPF DIN2UHP1_HD10UUP_CH1_D10RMI_RXERR	W17	VPF_DOUT2VLC0_D_2UUP_XD10/GPI07[10]	W17
VP DIN 1UHP1 HD 9UUP D 9/RMI RXD 5 CLK1	W18	VPF DIN1UHP1_HD9UUP_CH1_D9RMI_RXD_5_CLK	W18	VPF_DOUT1VLC0_D_1UUP_XD9/GPI07[9]	W18
VP DIN 0UHP1 HD 8UUP D 8/RMI CRS DV	W19	VPF DIN0UHP1_HD8UUP_CH1_D8RMI_CRS_DV	W19	VPF_DOUT0VLC0_D_0UUP_XD8/GPI07[8]	W19
VP CLKIN3MMSD1 DAT 1	H3	VPF_CLKIN3MMSD1_D1/GPI08[2]	H3	MMSCD1_D6SLCD_HSYNG/GPI08[8]	H3
VP CLKIN2MMSD1 DAT 3	J3	VPF_CLKIN2MMSD1_D3/GPI08[4]	J3	MMSCD1_D4SLCD_VSYNG/GPI08[8]	J3
VP CLKIN1UHP1 HDS1	V15	VPF_CLKIN1UHP1_HDS1EN/GPI08[6]	V15	MMSCD1_D1VLC0_MCLK/GPI08[4]	V15
VP CLKIN0UHP1 HCS/UUP 2XTXCLK	W14	VPF_CLKIN0UHP1_HCS/UUP2_XTXCLK	W14	MMSCD1_D7VLC0_CLK/GPI08[11]	W14
				LCD_AC_ENB_CSVG/GPI0[7]	
SP10 CLKPEPMM0/GP1 8MIL RXCLK	D19	SP10_CLKPEPMM0_A/GP1[8]MIL_RXCLK	D19	VPF_CLK03/GPI08[1]	D19
SP10 SOMIPEPMM0_VSYNC/GP1 8MIL RXRR	C18	SP10_SOMIPEPMM0_VSYNC/GP1[8]MIL_RXRR	C18	VPF_CLK02MMSCD1/GPI08[3]	C18
SP10 SOMIPEPMM0_VSYNC/GP1 8MIL RXD	C18	SP10_SOMIPEPMM0_VSYNC/GP1[8]MIL_RXD	C18		
SP10 ENAPEPMM0B/RPU0 830 6MIL RXD3	C17	SP10_ENAPEPMM0B_RMI_RXD3	C17	MMSCD1_D0UUP_CH0_CLK/GPI08[15]	C17
SP10 SCS_5U4ART0 RXD/G6 4MIL RXD3	C19	SP10_SCS_5U4ART0_RXD/G6[4]MIL_RXD3	C19	MMSCD1_C1UCLKUP_CH0_START/GPI08[16]	C19
SP10 SCS_4U4ART0 TXD/G6 3MIL RXD 2	D18	SP10_SCS_4U4ART0_TXD/G6[3]MIL_RXD2	D18	MMSCD1_CMDUUP_CH0_ENABLE/GPI08[17]	D18
SP10 SCS_3U4ART0 CTS/G6 2MIL RXD 1/SATA MP SWITCH 1	D17	SP10_SCS_3U4ART0_CTS/G6[2]MIL_RXD1/SATA_MP_SWITCH1	D17	UHP1_HCNTL1UUP_CH1_START/GPI08[12]	D17
SP10 SCS_2U4ART0 R1/SATA RXD 0/SATA CP DET 1	F17	SP10_SCS_2U4ART0_R1/SATA_RXD0/SATA_CP_DET1	F17	UHP1_HCNTL1UUP_CH1_START/GPI08[11]	F17
SP10 SCS_1U4ART0 OUT1/GP1 7MDO CLK/TM64P0 IN12	E16	SP10_SCS_1U4ART0_OUT1/GP1[7]MDO_CLK/TM64P0_IN12	E16	UHP1_HCNTL1UUP_CH1_ENABLE/GPI08[9]	E16
SP10 SCS_0TM64P0 OUT12/GP1 6MDO DT/M64P1 IN12	D17	SP10_SCS_0TM64P0_OUT12/GP1[6]MDO_DT/M64P1_IN12	D17	UHP1_HRWNUP_CH1_WAIT/GPI08[8]	D17
SP11 CLK/GP2 13	G19	SP11_CLK/GPI02[13]	G19	UHP1_HINTN/GPI08[12]	G19
SP11 SOMI/GP2 11	H17	SP11_SOMI/GPI02[11]	H17	UHP1_HRDY/GPI08[11]	H17
SP11 SMOI/GP2 10	G17	SP11_SMOI/GPI02[10]	G17		
SP11 ENAG/GP2 12	H16	SP11_ENAG/GPI02[12]	H16		
SP11 SCS_7I2C0 SCL/TM64P2 OUT12/GP1 15	G18	SP11_SCS_7I2C0_SCL/TM64P2_OUT12/GPI015	G18		
SP11 SCS_6I2C0 SDA/TM64P3 OUT12/GP1 4	G16	SP11_SCS_6I2C0_SDA/TM64P3_OUT12/GPI04	G16		
SP11 SCS_5U4ART1 RXD/GC1 SCL/GP1 3	F17	SP11_SCS_5U4ART1_RXD/GC1_SCL/GPI03	F17		
SP11 SCS_4U4ART1 TXD/GC1 SDA/GP1 2	E18	SP11_SCS_4U4ART1_TXD/GC1_SDA/GPI02	E18		
SP11 SCS_3U4ART1 RXD/SATA LEDGP1 1	F18	SP11_SCS_3U4ART1_RXD/SATA_LEDGP1[0]	F18		
SP11 SCS_2U4ART1 TXD/SATA CP PD0/GP1 0	F19	SP11_SCS_2U4ART1_TXD/SATA_CP_PD0/GP1[0]	F19		
SP11 SCS_1EPWM1A/GP2 15TM64P2 IN12	F18	SP11_SCS_1EPWM1_A/GP2[15]TM64P2_IN12	F18		
SP11 SCS_0EPWM1B/GP2 14TM64P3 IN12	F19	SP11_SCS_0EPWM1_B/GP2[14]TM64P3_IN12	F19		
RTC ALARMUART2_CTS/GP0 8DEEPSLEEP	F4	RS0VUT2C_ALARMUART2_CTS/GP0[8]DEEPSLEEP	F4		
AMUTEUART2_RTS/GP0 9	D5	AMUTEUART2_RTS/GP0[9]	D5		
AXR7EPWMW12_UCP1 15	D2	AXR7EPWMW1_T2QUCP1[15]	D2		
AXR15EPWM0TX_0ECAP2_APWM2/GP0 7	A4	AXR15EPWMW0_T2Q0ECAP2_APWM2/GPI07[1]	A4		
AFSX/GP0 12	B2	AFSX/GPI00[12]	B2		
AFSR/GP0 13	C2	AFSR/GPI00[13]	C2		
AHCLKXUSB_REFCLKINUART1_CTS/GP0 10	A3	AFSR/GPI00[13]	A3		
AHCLKXUART1_RTS/GP0 11	A2	AHCLKXUART1_RTS/GPI00[11]	A2		
ACLKX/GP0 14	B1	ACLKX/GPI0[14]	B1		
ACLKRG/GP0 15	A1	ACLKX/GPI0[15]	A1		



P4	VP DOUT_15A1CD 2_15A1UP 7/6P7 7/800T	
P3	VP DOUT_14A1CD 2_14A1UP 6/6P7 6/800T	
R1	VP DOUT_13A1CD 2_13A1UP 5/6P7 5/800T	
R2	VP DOUT_12A1CD 2_12A1UP 4/6P7 4/800T	
T1	VP DOUT_11A1CD 2_11A1UP 3/6P7 3/800T	
T2	VP DOUT_10A1CD 2_10A1UP 2/6P7 2/800T	
T3	VP DOUT_9A1CD 2_9A1UP 1/6P7 1/800T	
U3	VP DOUT_8A1CD 2_8A1UP 0/6P7 0/800T	
U2	VP DOUT_7A1CD 2_7A1UP 15/6P7 15	
U1	VP DOUT_6A1CD 2_6A1UP 14/6P7 14	
V3	VP DOUT_5A1CD 2_5A1UP 13/6P7 13	
V2	VP DOUT_4A1CD 2_4A1UP 12/6P7 12	
V1	VP DOUT_3A1CD 2_3A1UP 11/6P7 11	
W3	VP DOUT_2A1CD 2_2A1UP 10/6P7 10	
W2	VP DOUT_1A1CD 2_1A1UP 9/6P7 9	
W1	VP DOUT_0A1CD 2_0A1UP 8/6P7 8	
H4	MMCS01 DOUT_6A1CD H5VNC6P8 9	
H3	MMCS01 DOUT_5A1CD H5VNC6P8 8	
H2	MMCS01 DOUT_4A1CD H5VNC6P8 7	
F1	MMCS01 DOUT_7A1CD PCLK6P8 11	
R5	LCD AC ENB CS6P6 0	
K4	VP CLKOUT3/GP6_1	
K3	VP CLKOUT2/MMCS01 DOUT_2A1GP6_3	
G1	MMCS01 DOUT_0A1UP CHB CLOCK/GP8_15	
J4	MMCS01 CLKUPUP CHB START/GP8_13	
J2	MMCS01 CMDUPUP CHB ENABLE/GP8_13	
G3	UPP CHB WAIT/GP8_12	
U4	UPP HCNTL0UPUP CHA CLOCK/GP8_11	
W15	UPP HCNTL1UPUP CHA START/GP8_10	
U16	UPP HHRWUPUP CHA ENABLE/GP6_9	
T15	UPP HHRWUPUP CHA WAIT/GP6_8	
R16	UPP HRT/GP6_12	
R17	UPP HRTV/GP6_13	
C1	AXR6/CLKR0/GP1 14MMI TXEN/PRI0_R31_6	
D1	AXR5/CLKR0/GP1 13MMI TXCLK	
F1	AXR0/ACPE APWM/GP6 7MMI TXD DCLKS0	
E3	AXR4/SR0/GP1 12MMI COL	
E2	AXR3/SX0/GP1 11MMI TXD 3	
E1	AXR2/DR/P2 10MMI TXD 2	
E4	AXR1/DX0/GP1 9MMI TXD 1	
B4	AXR14/CLKR1/GP0 6	
B3	AXR13/CLKR1/GP0 5	
E4	AXR8/CLKR1/ACAP1 APWM1/GP0 0	
C4	AXR12/SR1/GP0 4	
G4	AXR11/DX0 3	
G3	AXR10/DR/GP0 2	
G2	AXR9/DX1/GP0 1	
N1	SATA REFCLK N	
N2	SATA REFCLK P	
N3	SATA TX N	
N4	SATA TX P	
L1	SATA RX N	
L2	SATA RX P	
M3		

SPI0	CLKPEWMM4/GP1	8MIU	RXCCLK	163	164	UHP1	HRWUPP	GA	WAITG6	8
SPI0	SOMIEPWSYNGC6P8	6MIU	RKXERR	165	166	UHP1	HINT	GP6	12	
SPI0	SIMOEFPWMSYNGC6P8	5MIU	CXS	167	168	UHP1	HWID	GP6	13	
SPI0	ENAEFPMWMBPRU0	R30	6MIU	RDV	169	VP	DIN	15	VSYNCGD1H	HD
SPI0	SCS	5UJAR0	RXD/GP8	4MIU	RX0	3	171	VP	DIN	14
SPI0	SCS	4UJAR0	TXD/GP8	3MIU	RXD	2	172	VP	DIN	13
SPI0	SCS	3UJAR0	TXD/GP8	2MIU	RXD	1	173	VP	DIN	12
SPI0	SCS	3UJAR0	CTS/GP8	2MIU	RXD	1	174	VP	DIN	11
SPI0	SCS	2UJAR0	RTS/GP8	1MIU	RXD	0	175	VP	DIN	10
SPI0	SCS	1/1M64P0	OUT1/GP1	7	MDIO	CLK/TM64P0	IN1	176	VP	DIN
SPI0	SCS	0/1M64P1	OUT1/GP1	6	MDIO	CLK/TM64P1	IN1	177	VP	DIN
SPI0	SCS	1/EPWMP4P2	15TM64P2	IN1	183	VP	DIN	9	UHP1	HD
SPI0	SCS	2UJAR1	TXD/SATA	CLD/GP1	0	184	VP	DIN	7	UHP1
SPI0	SCS	3UJAR1	RXD/SATA	LED/GP1	1	185	VP	DIN	6	UHP1
SPI0	SCS	4UJAR2	TXD/12C1	SCL/GP1	2	186	VP	DIN	5	UHP1
SPI0	SCS	5UJAR2	RXD/12C1	SCL/GP1	3	187	VP	DIN	4	UHP1
SPI0	SCS	6/2C0	SDA/TM64P3	OUT1/GP1	4	188	VP	DIN	3	UHP1
SPI0	SCS	7/2C0	SCL/TM64P3	OUT1/GP1	5	189	VP	DIN	2	UHP1
SPI0	ENAGP2	12			190	VP	DIN	1	UHP1	HD
SPI0	SIM0/GP2	10			191	VP	DIN	0	UHP1	HD
SPI0	SOMI/GP2	11			192	VP	DIN	0	UHP1	HD
SPI0	CLK/GP2	13			201	VP	DIN	0	UHP1	HD
					202	VP	DIN	0	UHP1	HD
					203	VP	DIN	0	UHP1	HD
					204	SP1	OCFPMW1BG2P	14	TM64P3	IN12

