

DATE	Revision	Description
20,11,28	0.1	TI CPU-AM5749ABZXEA 신규

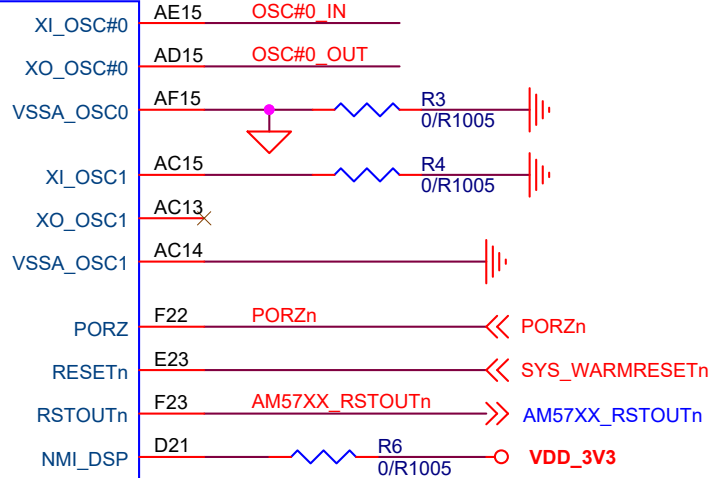
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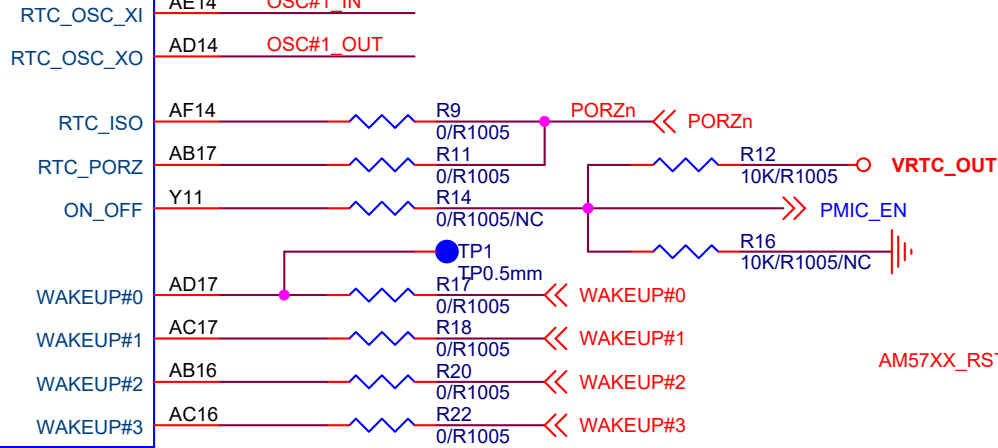
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U1-1

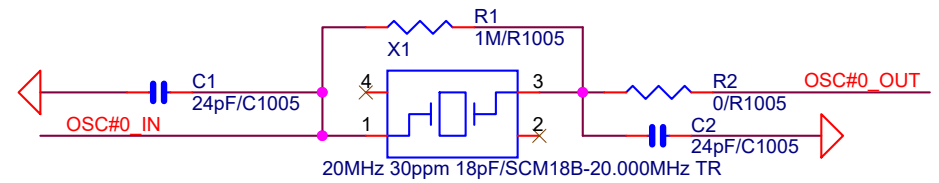
AM574X Main Osc/Ctrl



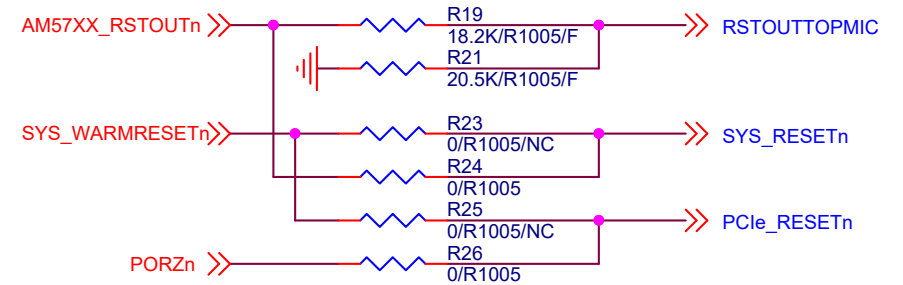
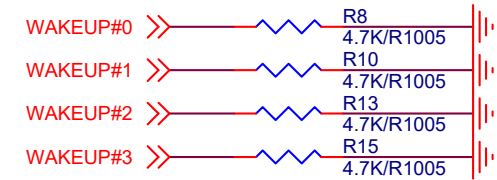
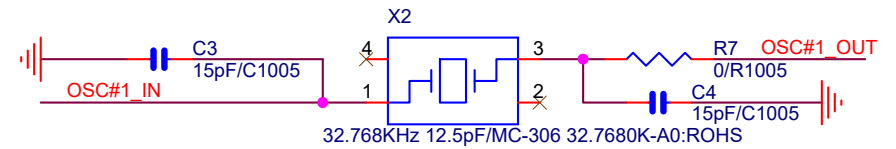
AM574X RTC Osc



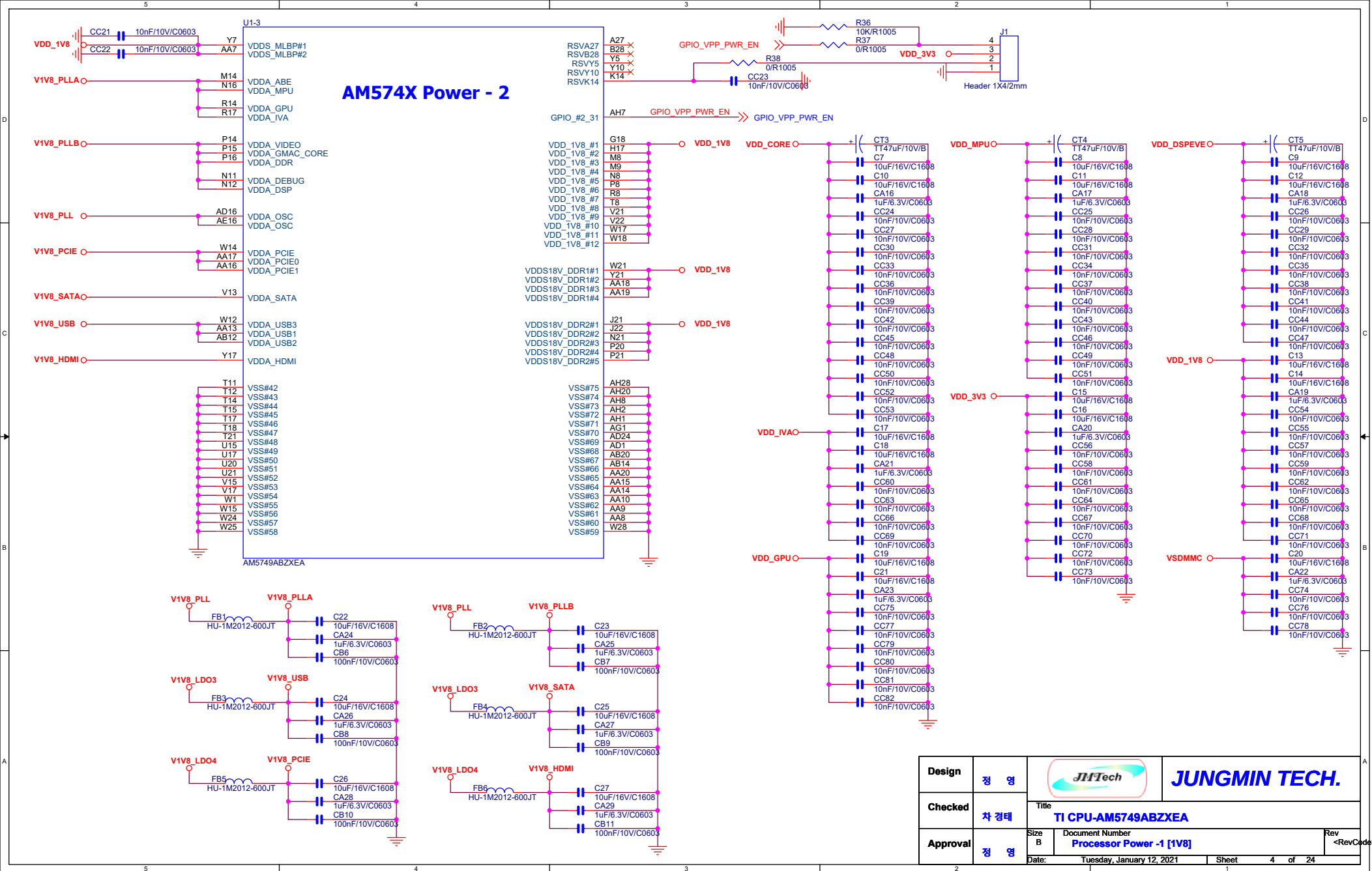
AM5749ABZXEA



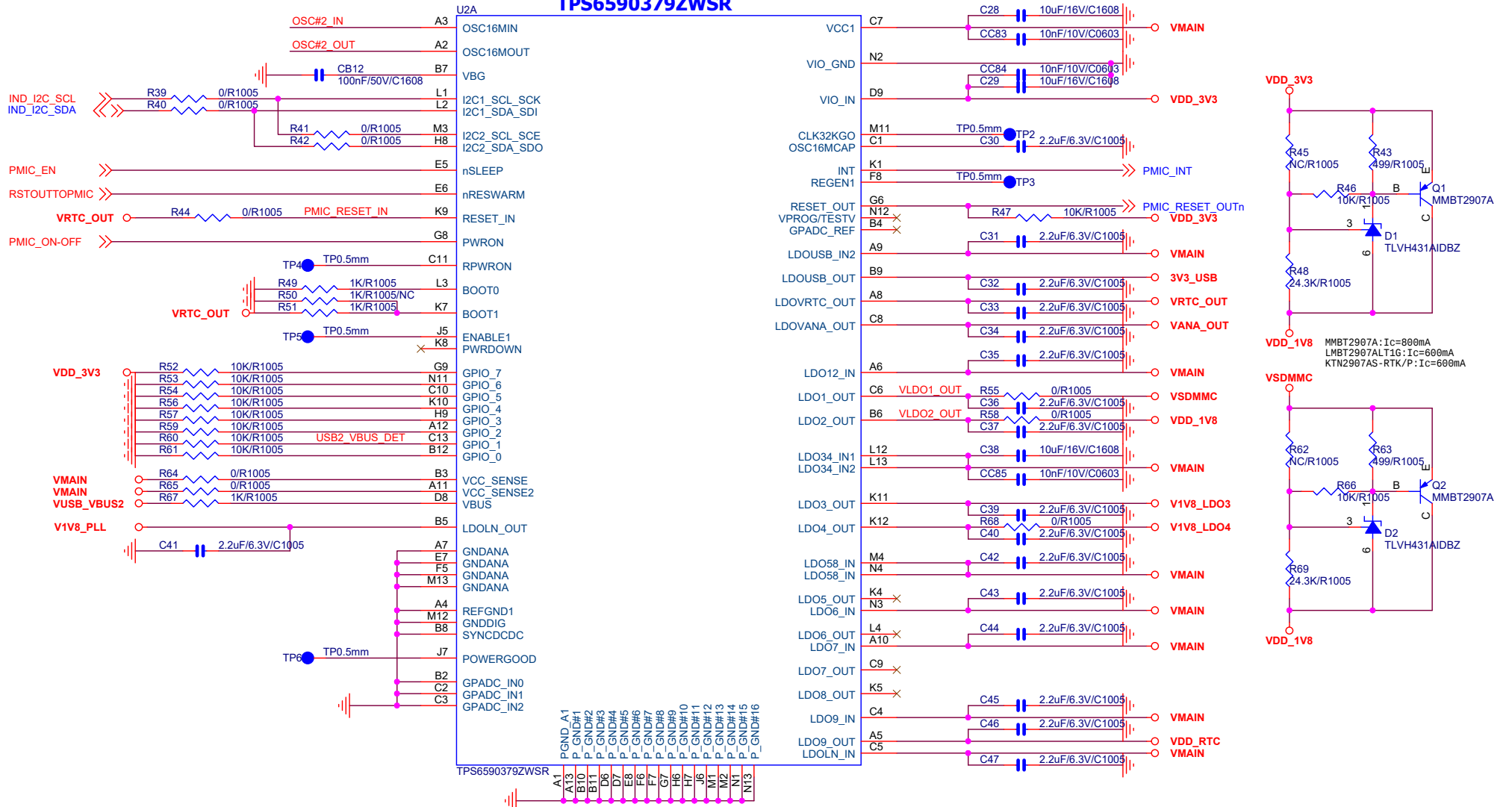
SYS_WARMRESETn >> R5 10K/R1005 VDD_3V3



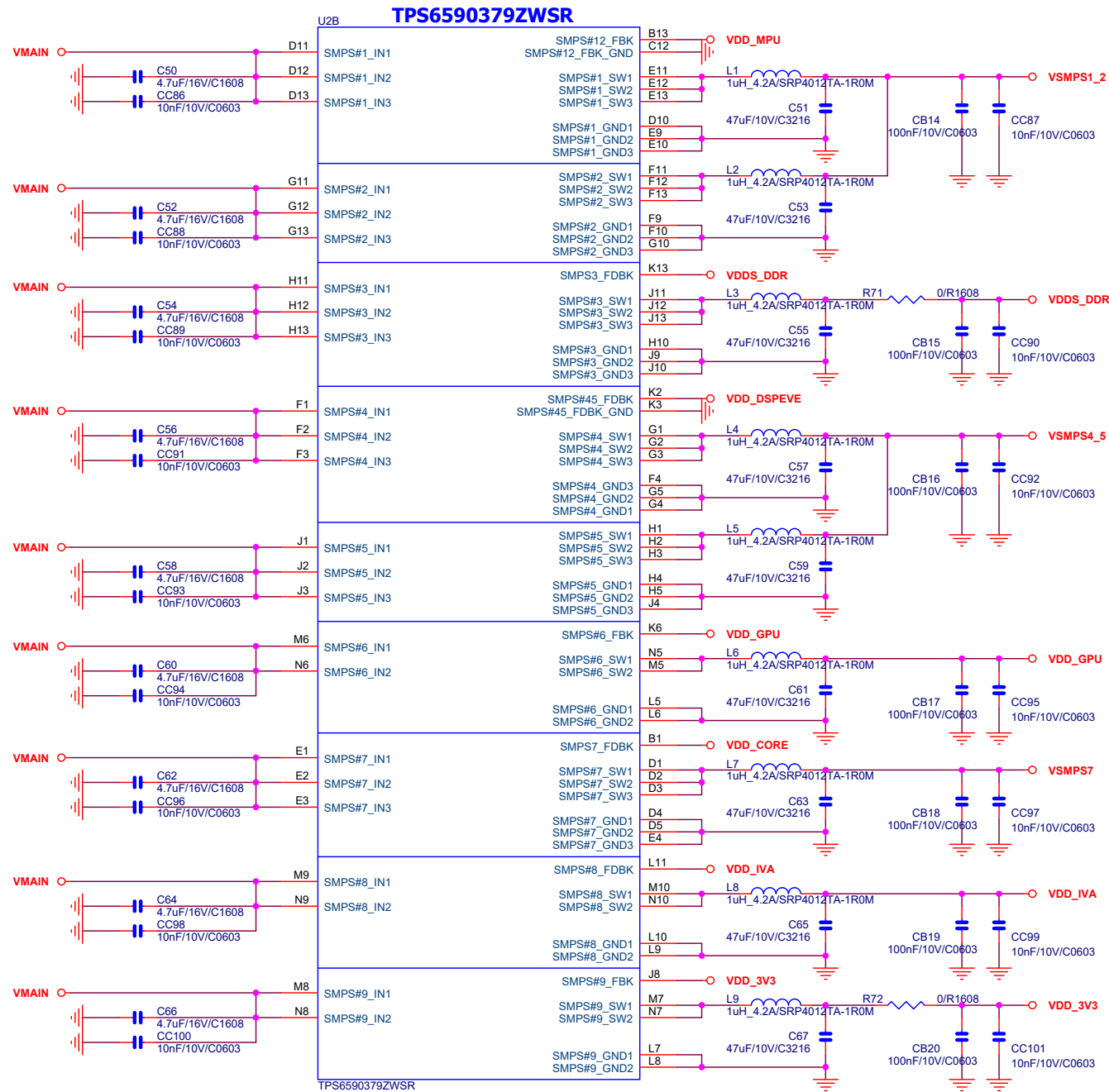
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Checked	차 경태	Title TI CPU-AM5749ABZXEA			
Approval	정 영	Size A	Document Number Main Osc/Ctrl, RTC Osc		Rev <RevCode
		Date: Tuesday, January 12, 2021		Sheet 2 of 24	



TPS6590379ZWSR



Design	정영	 JUNGMIN TECH.	
Checked	차경태	Title TI CPU-AM5749ABZXEA	
Approval	정영	Size Custom Document Number Power Supply [PMIC] -1	Rev <RevCode>
Date:		Thursday, January 14, 2021	Sheet 5 of 24



Design	정영	 JUNGMIN TECH.	
Checked	차경태	Title TI CPU-AM5749ABZXEA	
Approval	정영	Size Custom Document Number Power Supply [PMIC] -2	Rev <RevCode>
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**AM574X
QSPI**

GPMC_A18/QSPI1_SCLK
GPMC_A13/QSPI1_RTCLK
GPMC_CS2/QSPI1_CS0

GPMC_A16/QSPI1_D0
GPMC_A17/QSPI1_D1
GPMC_A15/QSPI1_D2
GPMC_A14/QSPI1_D3

AM5749ABZXEA

R2
R3
P2

U1
P3
U2
T2

R73
0/R1005
R74
0/R1005
R75
0/R1005

R76
0/R1005
R77
0/R1005
R78
0/R1005
R79
0/R1005

SYS_RESETn

U4
S25FL256S
SCK
CS
SI/IO0
SO/IO1
WP/IO2
HOLD/IO3
RESET

VCC
VIO/RFU

VSS

NC11
NC12
NC13

NC6
NC5
NC4

VDD_3V3

CC102
10nF/10V/C0603

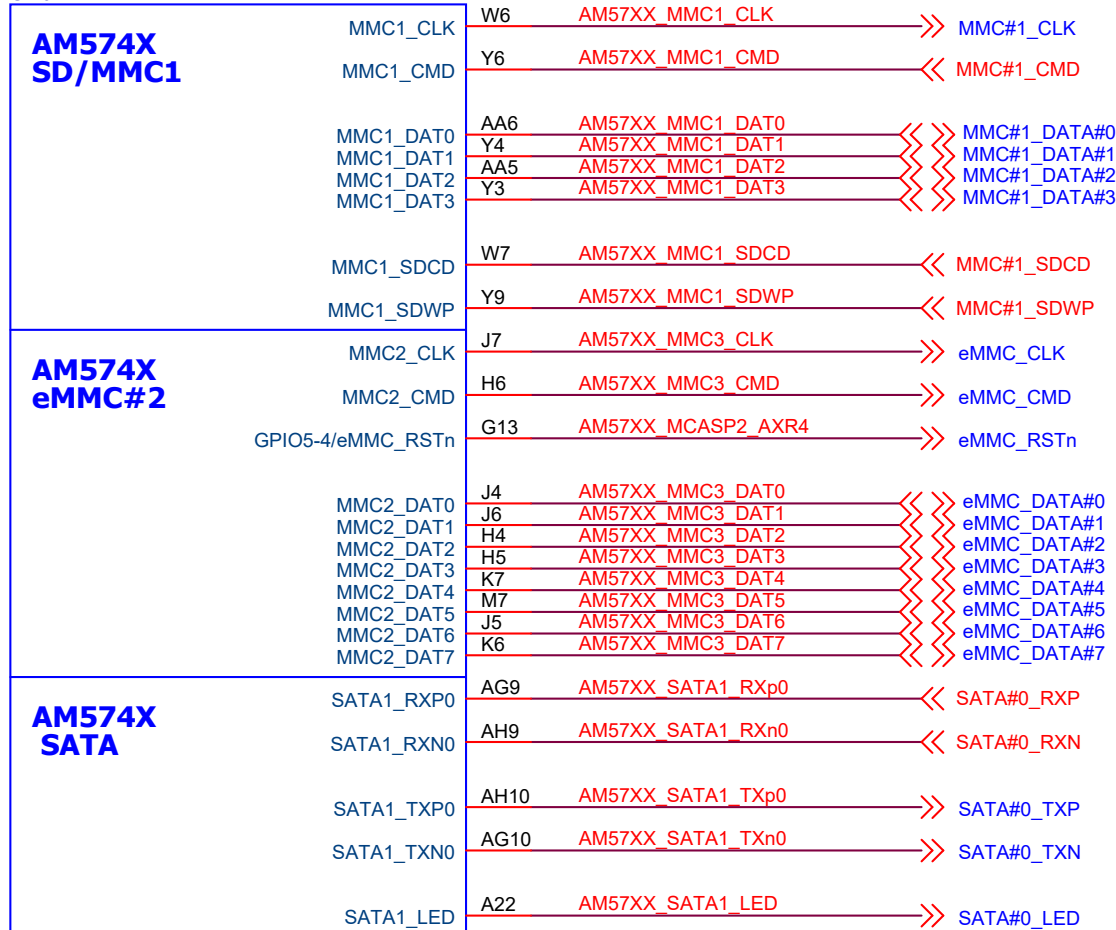
SPI_MEM_D#2
SPI_MEM_D#3

R81
10K/R1005
R82
10K/R1005

VDD_3V3
VDD_3V3

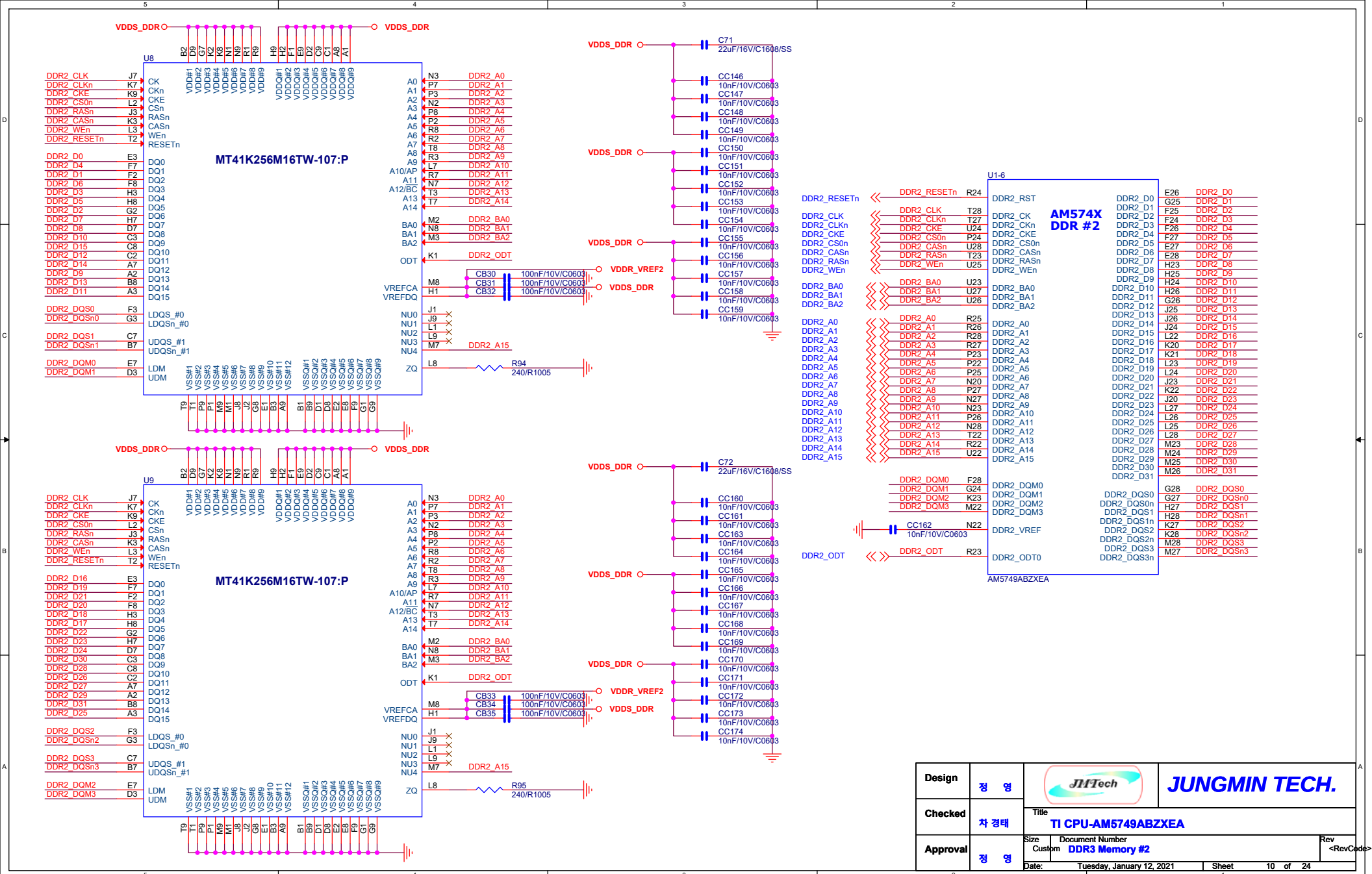
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Approval	정 영	Size A	Document Number QSPI NOR Flash Memory / eMMC Memory		Rev <RevCode
		Date: Tuesday, January 12, 2021		Sheet 7 of 24	

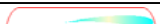
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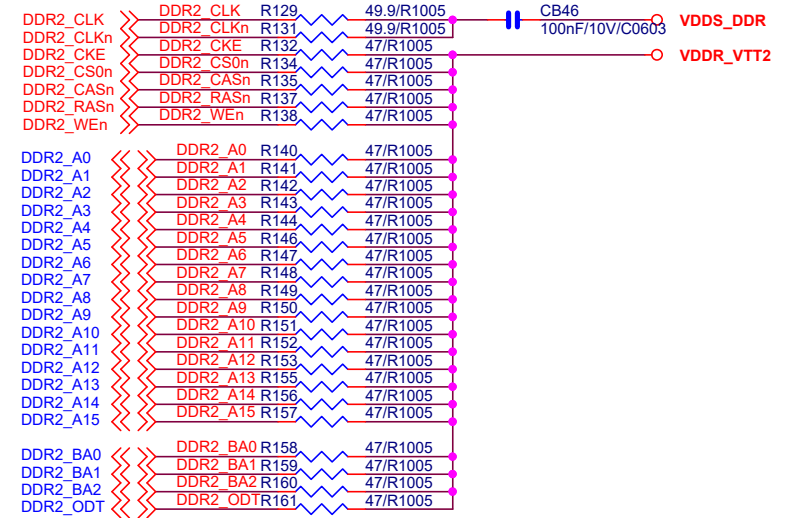
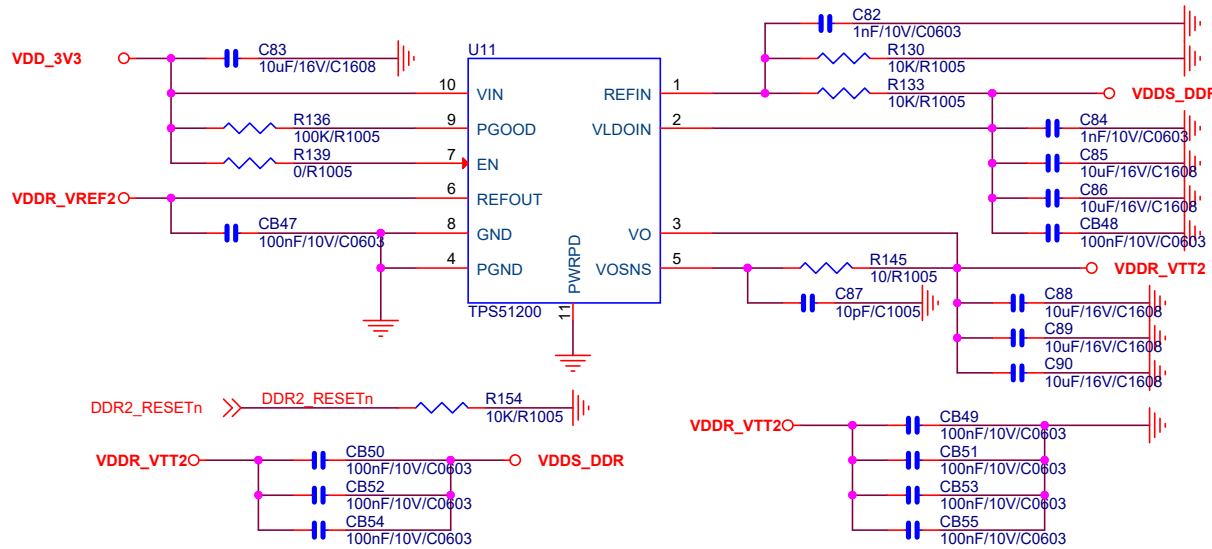
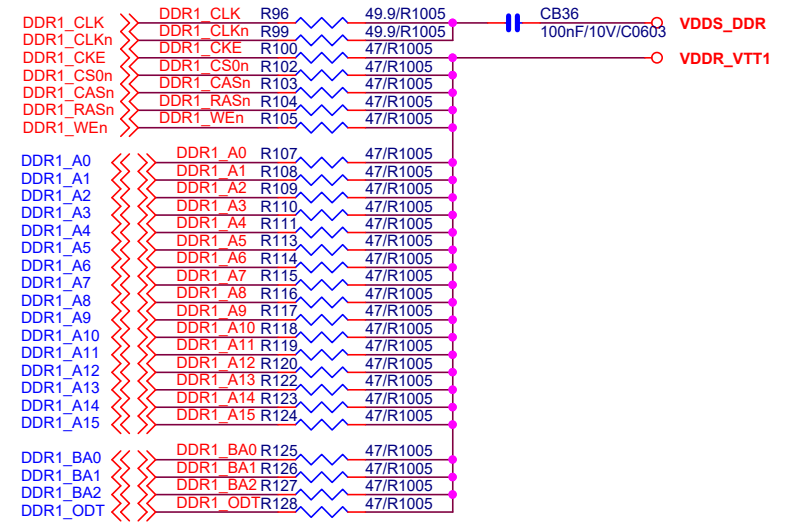
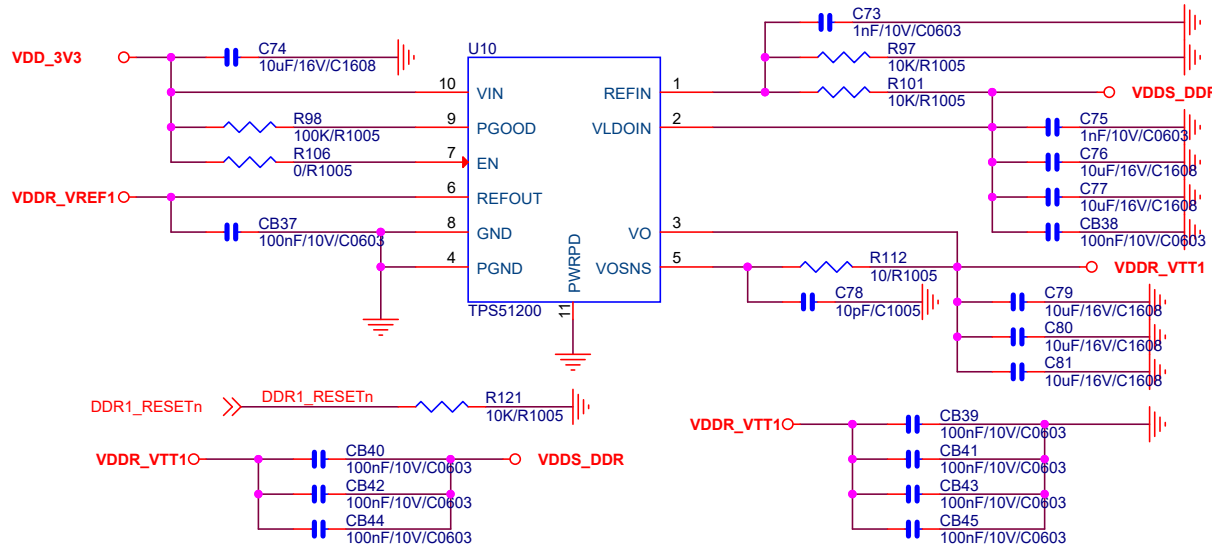


AM5749ABZXEA

Design	정 영			JUNGMIN TECH.	
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Approval	정 영	Size A	Document Number SDMMC #1/ eMMC#2 / SATA Interface		Rev <RevCode
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Design	정영			JUNGMIN TECH.		
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Approval	정영	Size	Document Number			Rev
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		Date:	Tuesday, January 12, 2021	Sheet	10 of 24	



Design	정 영	 JUNGMIN TECH.	
Checked	차 경태	Title TI CPU-AM5749ABZXEA	
Approval	정 영	Size Custom Document Number DDR3 VTT Termination	Rev <RevCode>
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U1-7

AM574X RGMII#0

RGMII0_TXC
RGMII0_TXCTL

RGMII0_TXD0
RGMII0_TXD1
RGMII0_TXD2
RGMII0_TXD3

RGMII0_RXC
RGMII0_RXCTL

RGMII0_RXD0
RGMII0_RXD1
RGMII0_RXD2
RGMII0_RXD3

MDIO_MCLK
MDIO_D

GB_ETH0_INTn

AM574X RGMII#1

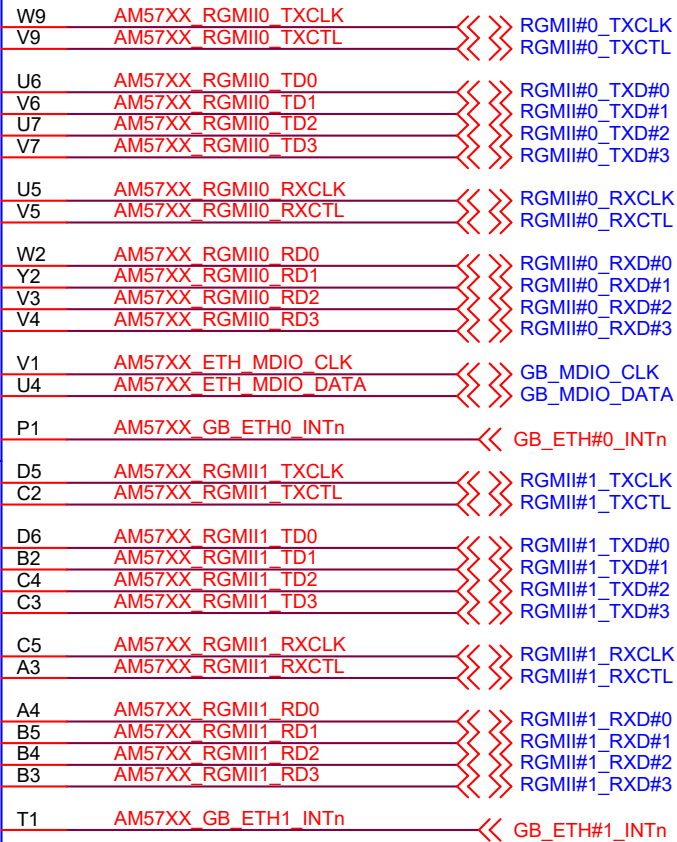
RGMII1_TXC
RGMII1_TXCTL

RGMII1_TXD0
RGMII1_TXD1
RGMII1_TXD2
RGMII1_TXD3

RGMII1_RXC
RGMII1_RXCTL

RGMII1_RXD0
RGMII1_RXD1
RGMII1_RXD2
RGMII1_RXD3

GB_ETH1_INTn



AM5749ABZXEA

Design	정 영			JUNGMIN TECH.	
Checked	차 경태	Title TI CPU-AM5749ABZXEA			
Approval	정 영	Size	Document Number		Rev
		Custom	GB Ethernet #1,#2		<RevCode
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U1-8

**AM574X
PRU2_MII#0**

PR2_MII_MT0_CLK
PR2_MII0_TXEN

PR2_MII0_TXD0
PR2_MII0_TXD1
PR2_MII0_TXD2
PR2_MII0_TXD3

PR2_MII_MR0_CLK
PR2_MII0_RXDV
PR2_MII0_RXER

PR2_MII0_RXD0
PR2_MII0_RXD1
PR2_MII0_RXD2
PR2_MII0_RXD3

PR2_MII0_CRS
PR2_MII0_COL
PR2_MII0_RXLINK

PR2_MDIO_MDCLK
PR2_MDIO_DATA

gpio5_5/PRU2_ETH#0_RESEtN
gpio4_11/PRU2ETH#0_INT

**AM574X
PRU2_MII#1**

PR2_MII_MT1_CLK
PR2_MII1_TXEN

PR2_MII1_TXD0
PR2_MII1_TXD1
PR2_MII1_TXD2
PR2_MII1_TXD3

PR2_MII_MR1_CLK
PR2_MII1_RXDV
PR2_MII1_RXER

PR2_MII1_RXD0
PR2_MII1_RXD1
PR2_MII1_RXD2
PR2_MII1_RXD3

PR2_MII1_CRS
PR2_MII1_COL
PR2_MII1_RXLINK

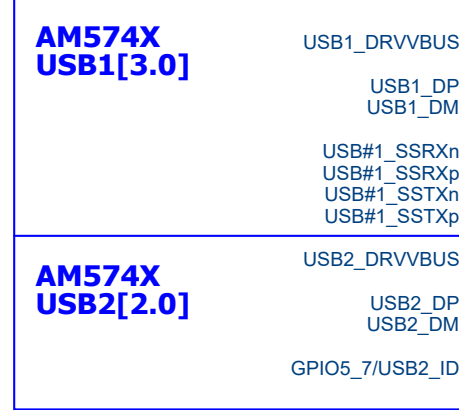
gpio5_6/PRU2_ETH#1_RESEtN
gpio4_12/PRU2ETH#1_INT

F12	AM57XX PRU2ETH0_TXCLK	PRU2ETH0_TXCLK
B12	AM57XX PRU2ETH0_TXEN	PRU2ETH0_TXEN
E14	AM57XX PRU2ETH0_TXD0	PRU2ETH0_TXD#0
A12	AM57XX PRU2ETH0_TXD1	PRU2ETH0_TXD#1
B13	AM57XX PRU2ETH0_TXD2	PRU2ETH0_TXD#2
A11	AM57XX PRU2ETH0_TXD3	PRU2ETH0_TXD#3
A13	AM57XX PRU2ETH0_MRCLK	PRU2ETH0_MRCLK
G14	AM57XX PRU2ETH0_RXDV	PRU2ETH0_RXDV
G12	AM57XX PRU2ETH0_RXER	PRU2ETH0_RXER
C15	AM57XX PRU2ETH0_RXD0	PRU2ETH0_RXD#0
A18	AM57XX PRU2ETH0_RXD1	PRU2ETH0_RXD#1
A19	AM57XX PRU2ETH0_RXD2	PRU2ETH0_RXD#2
F14	AM57XX PRU2ETH0_RXD3	PRU2ETH0_RXD#3
B18	AM57XX PRU2ETH0_CRS	PRU2ETH0_CRS
F15	AM57XX PRU2ETH0_COL	PRU2ETH0_COL
A16	AM57XX PRU2ETH0_RXLINK	PRU2ETH0_RXLINK
C14	PRU2ETH0_MDCLK	PRU2ETH0_MDCLK
D14	PRU2ETH0_MDIO	PRU2ETH0_MDIO
J11	AM57XX PRU2ETH0_RSTn	PRU2ETH0_RSTn
D3	AM57XX PRU2ETH0_INTn	PRU2ETH0_INTn
AC5	AM57XX PRU2ETH1_TXCLK	PRU2ETH1_TXCLK
AB4	AM57XX PRU2ETH1_TXEN	PRU2ETH1_TXEN
AC6	AM57XX PRU2ETH1_TXD0	PRU2ETH1_TXD#0
AC7	AM57XX PRU2ETH1_TXD1	PRU2ETH1_TXD#1
AC4	AM57XX PRU2ETH1_TXD2	PRU2ETH1_TXD#2
AD4	AM57XX PRU2ETH1_TXD3	PRU2ETH1_TXD#3
AC9	AM57XX PRU2ETH1_MRCLK	PRU2ETH1_MRCLK
AC3	AM57XX PRU2ETH1_RXDV	PRU2ETH1_RXDV
B19	AM57XX PRU2ETH1_RXER	PRU2ETH1_RXER
AB5	AM57XX PRU2ETH1_RXD0	PRU2ETH1_RXD#0
AB8	AM57XX PRU2ETH1_RXD1	PRU2ETH1_RXD#1
AD6	AM57XX PRU2ETH1_RXD2	PRU2ETH1_RXD#2
AC8	AM57XX PRU2ETH1_RXD3	PRU2ETH1_RXD#3
E17	AM57XX PRU2ETH1_CRS	PRU2ETH1_CRS
D18	AM57XX PRU2ETH1_COL	PRU2ETH1_COL
C17	AM57XX PRU2ETH1_RXLINK	PRU2ETH1_RXLINK
E12	AM57XX PRU2ETH1_RSTn	PRU2ETH1_RSTn
F6	AM57XX PRU2ETH1_INTn	PRU2ETH1_INTn

AM5749ABZXEA

Design	정 영	 JUNGMIN TECH.
Checked	차 경태	
Approval	정 영	Title TI CPU-AM5749ABZXEA
Date: Tuesday, January 12, 2021		Size A Document Number PRU Ethernet #1,#2
Sheet 13 of 24		Rev <RevCode

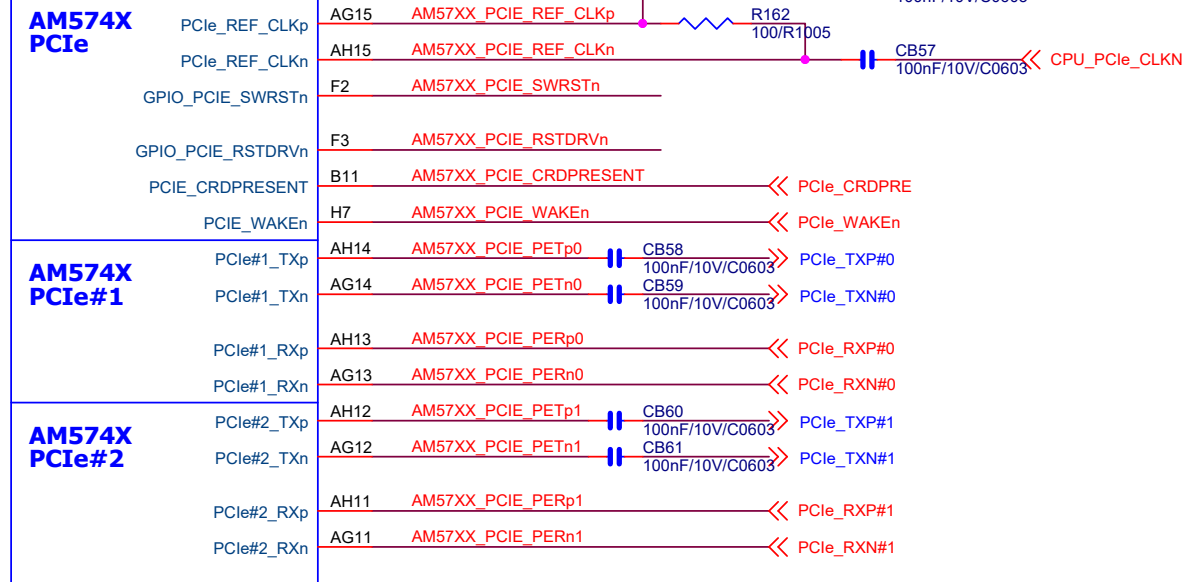
U1-12



AM5749ABZXEA

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		Document Number USB #1[3.0] / USB #2[2.0]		Rev <RevCode
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AM5749ABZXEA



Design	정 영	 JUNGMIN TECH.		
Checked	차 경태		Title TI CPU-AM5749ABZXEA	
Approval	정 영	Size A4	Document Number PCIE Interface	Rev <RevCode>
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U1-19

**AM574X Audio
(I2S)**

PRCM_CLKOUT1

mcasep8_ahclkx

McASP8_FSX

McASP8_ACLKX

McASP8_AXR#0

McASP8_AXR#1

F21

C23

A17

B17

D15

B16

R163

0/R1005

R164

0/R1005/NC

AM57XX_AIC_MCLKR

>>> AIC_MCLK

>>> AIC_WCLK

>>> AIC_BCLK

<<< AIC_DIN

>>> AIC_DOUT

AM5749ABZXEA

U1-14

**AM574X Camera
#1**

VIN4B_CLK0

VIN4B_VSYNC1

VIN4B_HSYNC1

VIN4B_DE1

VIN4B_FLD1

GPIO_#4_6/CAM_EN

N9

R4

N7

P9

P4

F4

R6

T9

T6

T7

P6

R9

R5

P5

<<< VIN4B_PCLK

<<< VIN4B_VSYNC

<<< VIN4B_HSYNC

<<< VIN4B_DE

<<< VIN4B_FLD

<<< CAM_EN

<<>> VIN4B_DATA#0

<<>> VIN4B_DATA#1

<<>> VIN4B_DATA#2

<<>> VIN4B_DATA#3

<<>> VIN4B_DATA#4

<<>> VIN4B_DATA#5

<<>> VIN4B_DATA#6

<<>> VIN4B_DATA#7

AM5749ABZXEA

Design	정 영		JUNGMIN TECH.	
Checked	차 경태			
Approval	정 영	Size A	Title TI CPU-AM5749ABZXEA Document Number Audio / CAMERA Interface	
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		2		1

U1-11

**AM574X
LVDS**

VOUT3_CLK

AF9 AM57XX LVDS_PCLK >> LVDS_PCLK

VOUT3_VSYNC

AF8 AM57XX LVDS_VSYNC >> LVDS_VSYNC

VOUT3_HSYNC

AE9 AM57XX LCD_HSYNC >> LVDS_HSYNC

VOUT3_DE

AD9 AM57XX LVDS_DE << LVDS_DE

VOUT3_D0

AD3 AM57XX VOUT1_D0 >> LVDS_D#00

VOUT3_D1

AD2 AM57XX VOUT1_D1 >> LVDS_D#01

VOUT3_D2

AE6 AM57XX VOUT1_D2 >> LVDS_D#02

VOUT3_D3

AE2 AM57XX VOUT1_D3 >> LVDS_D#03

VOUT3_D4

AE1 AM57XX VOUT1_D4 >> LVDS_D#04

VOUT3_D5

AE5 AM57XX VOUT1_D5 >> LVDS_D#05

VOUT3_D6

AE3 AM57XX VOUT1_D6 >> LVDS_D#06

VOUT3_D7

AF1 AM57XX VOUT1_D7 >> LVDS_D#07

VOUT3_D8

AF4 AM57XX VOUT1_D8 >> LVDS_D#08

VOUT3_D9

AF3 AM57XX VOUT1_D9 >> LVDS_D#09

VOUT3_D10

AF6 AM57XX VOUT1_D10 >> LVDS_D#10

VOUT3_D11

AF2 AM57XX VOUT1_D11 >> LVDS_D#11

VOUT3_D12

AG5 AM57XX VOUT1_D12 >> LVDS_D#12

VOUT3_D13

AG3 AM57XX VOUT1_D13 >> LVDS_D#13

VOUT3_D14

AG2 AM57XX VOUT1_D14 >> LVDS_D#14

VOUT3_D15

AG4 AM57XX VOUT1_D15 >> LVDS_D#15

VOUT3_D16

AH4 AM57XX VOUT1_D16 >> LVDS_D#16

VOUT3_D17

AG6 AM57XX VOUT1_D17 >> LVDS_D#17

VOUT3_D18

AH5 AM57XX VOUT1_D18 >> LVDS_D#18

VOUT3_D19

AH3 AM57XX VOUT1_D19 >> LVDS_D#19

VOUT3_D20

AH6 AM57XX VOUT1_D20 >> LVDS_D#20

VOUT3_D21

AG7 AM57XX VOUT1_D21 >> LVDS_D#21

VOUT3_D22

AD8 AM57XX VOUT1_D22 >> LVDS_D#22

VOUT3_D23

AE8 AM57XX VOUT1_D23 >> LVDS_D#23

VIN2A_D2/eCAP1_IN_PWM1

D1 AM57XX LCD_BACKLIGHT >> LVDS_PWM

GPIO#2_#30/LCD_BL

AG8 AM57XX LCD_BL >> LVDS_BKLT_EN

**AM574X
HDMI**

HDMI1_CLOCKn

AG16 AM57XX HDMI_CLK- >> HDMI_CLKn

HDMI1_CLOCKp

AH16 AM57XX HDMI_CLK+ >> HDMI_CLKp

HDMI1_DATA0n

AG17 AM57XX HDMI_TX0- <<< HDMI_TX#0n

HDMI1_DATA0p

AH17 AM57XX HDMI_TX0+ <<< HDMI_TX#0p

HDMI1_DATA1n

AG18 AM57XX HDMI_TX1- <<< HDMI_TX#1n

HDMI1_DATA1p

AH18 AM57XX HDMI_TX1+ <<< HDMI_TX#1p

HDMI1_DATA2n

AG19 AM57XX HDMI_TX2- <<< HDMI_TX#2n

HDMI1_DATA2p

AH19 AM57XX HDMI_TX2+ <<< HDMI_TX#2p

SPI1_CS3/HDMI_CEC

B20 AM57XX HDMI_CEC >> HDMI_CEC

SPI1_CS2/HDMI_HPD

B21 AM57XX HDMI_HPD << HDMI_HPD

I2C#2_SCL/HDMI_DDC_SCL

C25 AM57XX HDMI_DDC_SCL >> HDMI_DDC_SCL

I2C#2_SDA/HDMI_DDC_SDA

F17 AM57XX HDMI_DDC_SDA << HDMI_DDC_SDA

AM5749ABZXEA

Design

정 영

**JUNGMIN TECH.**

Checked

차 경태

Title

TI CPU-AM5749ABZXEA

Approval

정 영

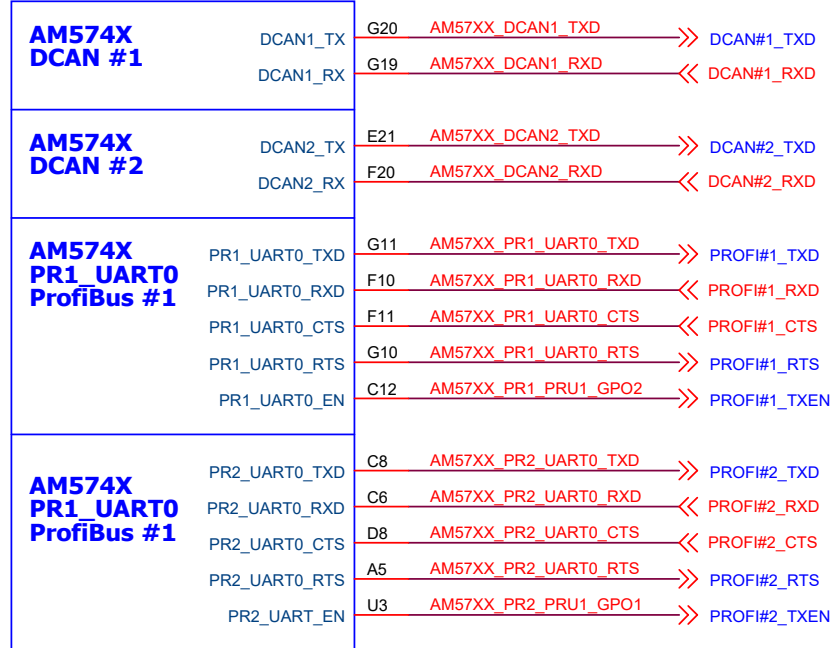
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LCD / HDMIRev
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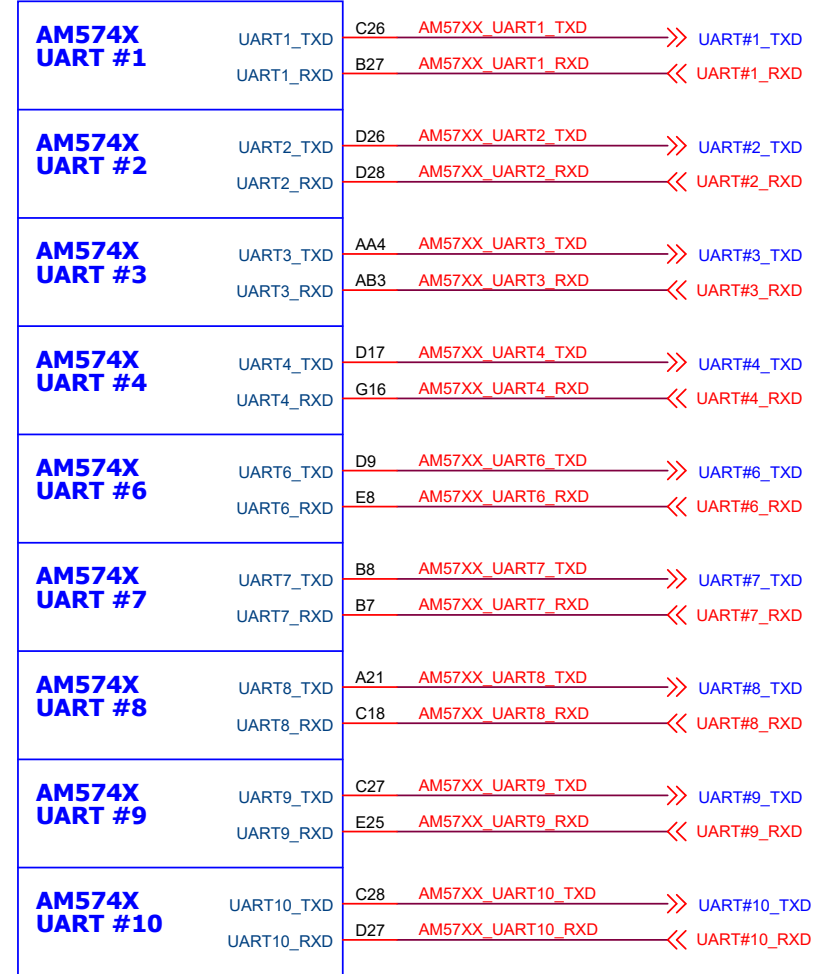
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AM5749ABZXEA

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AM5749ABZXEA

Design	정영	 JUNGMIN TECH.		
Checked	차경태		Title TI CPU-AM5749ABZXEA	
Approval	정영	Size A4	Document Number DCAN / Profibus / UART	Rev <RevCode
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U1-10

**AM574X
SPI#1**

SPI1_SCLK A25 AM57XX_SPI1_SCLK >>> SPI#1_SCLK
 SPI1_CS0 A24 AM57XX_SPI1_CS0 <<< SPI#1_CS#0
 SPI1_D0 B25 AM57XX_SPI1_D0 <<>> SPI#1_D#0
 SPI1_D1 F16 AM57XX_SPI1_D1 <<>> SPI#1_D#1

**AM574X
SPI#2**

SPI2_SCLK A26 AM57XX_SPI2_SCLK >>> SPI#2_SCLK
 SPI2_CS0 B24 AM57XX_SPI2_CS0 <<< SPI#2_CS#0
 SPI2_D0 G17 AM57XX_SPI2_D0 <<>> SPI#2_D#0
 SPI2_D1 B22 AM57XX_SPI2_D1 <<>> SPI#2_D#1

**AM574X
SPI#3**

SPI3_SCLK E11 AM57XX_SPI3_SCLK >>> SPI#3_SCLK
 SPI3_CS0 D11 AM57XX_SPI3_CS0 <<< SPI#3_CS#0
 SPI3_D0 C11 AM57XX_SPI3_D0 <<>> SPI#3_D#0
 SPI3_D1 B10 AM57XX_SPI3_D1 <<>> SPI#3_D#1

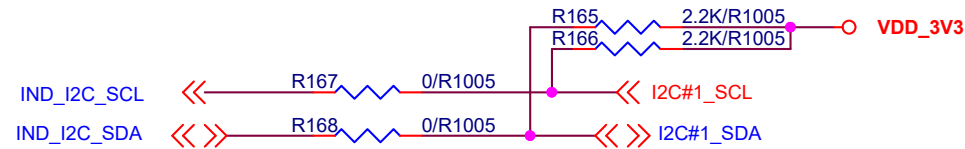
**AM574X
I2C#1**

I2C#1_SCL C20 AM57XX_I2C1_SCL >>> I2C#1_SCL
 I2C#1_SDA C21 AM57XX_I2C1_SDA <<>> I2C#1_SDA

**AM574X
I2C#4**

I2C#4_SCL J14 AM57XX_I2C4_SCL >>> I2C#4_SCL
 I2C#4_SDA B14 AM57XX_I2C4_SDA <<>> I2C#4_SDA

AM5749ABZXEA



Design	정 영			JUNGMIN TECH.	
Checked	차 경태	Title TI CPU-AM5749ABZXEA			
Approval	정 영	Size A	Document Number Industrial I/O [SPI/I2C]		Rev <RevCode
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U1-16

AM574X Boot Configuration

GPMC_AD0/GPIO	M6	AM57XX_GPMC_AD0	SYSBOOT0	<<	SYS_BOOT#0
GPMC_AD1/GPIO	M2	AM57XX_GPMC_AD1	SYSBOOT1	<<	SYS_BOOT#1
GPMC_AD2/GPIO	L5	AM57XX_GPMC_AD2	SYSBOOT2	<<	SYS_BOOT#2
GPMC_AD3/GPIO	M1	AM57XX_GPMC_AD3	SYSBOOT3	<<	SYS_BOOT#3
GPMC_AD4/GPIO	L6	AM57XX_GPMC_AD4	SYSBOOT4	<<	SYS_BOOT#4
GPMC_AD5/GPIO	L4	AM57XX_GPMC_AD5	SYSBOOT5	<<	SYS_BOOT#5
GPMC_AD6/GPIO	L3	AM57XX_GPMC_AD6	SYSBOOT6	<<	SYS_BOOT#6
GPMC_AD7/GPIO	L2	AM57XX_GPMC_AD7	SYSBOOT7	<<	SYS_BOOT#7
GPMC_AD8	L1	AM57XX_GPMC_AD8	SYSBOOT8	<<	SYS_BOOT#8
GPMC_AD9	K2	AM57XX_GPMC_AD9	SYSBOOT9	<<	SYS_BOOT#9
GPMC_AD10	J1	AM57XX_GPMC_AD10	SYSBOOT10	<<	SYS_BOOT#10
GPMC_AD11	J2	AM57XX_GPMC_AD11	SYSBOOT11	<<	SYS_BOOT#11
GPMC_AD12	H1	AM57XX_GPMC_AD12	SYSBOOT12	<<	SYS_BOOT#12
GPMC_AD13	J3	AM57XX_GPMC_AD13	SYSBOOT13	<<	SYS_BOOT#13
GPMC_AD14	H2	AM57XX_GPMC_AD14	SYSBOOT14	<<	SYS_BOOT#14
GPMC_AD15	H3	AM57XX_GPMC_AD15	SYSBOOT15	<<	SYS_BOOT#15

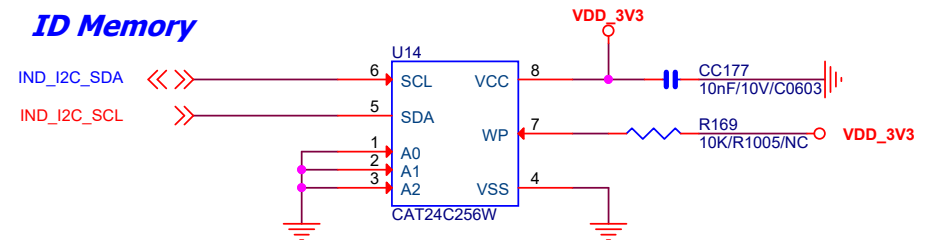
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AM574X JTAG

JTAG_TCLK	E20	AM57XX_JTAG_TCK	>>>	JTAG_TCK
JTAG_RTCK	E18	AM57XX_JTAG_RTCK	>>>	JTAG_RTCK
JTAG_TMS	F18	AM57XX_JTAG_TMS	>>>	JTAG_TMS
JTAG_TDI	D23	AM57XX_JTAG_TDI	<<<	JTAG_TDI
JTAG_TDO	F19	AM57XX_JTAG_TDO	>>>	JTAG_TDO
JTAG_TRSTn	D20	AM57XX_JTAG_TRSTn	>>>	JTAG_TRSTn
JTAG_EMU0	G21	AM57XX_JTAG_EMU0	>>>	JTAG_EMU0
JTAG_EMU1	D24	AM57XX_JTAG_EMU1	>>>	JTAG_EMU1

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ID Memory

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**AM574X PWM/
TIMER**

vin2a_vsync0/ehrpwm1A

vin2a_d9//ehrpwm2A

mcasp1_axr7/TIMER#4

xref_clk2/TIMER#15

G6 AM57XX_ehrpwm1A >>> PWM_#1A

E6 AM57XX_ehrpwm2A >>> PWM_#2A

D12 AM57XX_TIMER#4 >>> TIMER#4

B26 AM57XX_TIMER#15 >>> TIMER#15

AM574X STATUS_LED

gpio8_4/Status_Led#1

gpio8_5/Status_Led#2

gpio8_6/Status_Led#3

gpio8_7/Status_Led#4

E9 AM57XX_STATUS#1 >>> STATUS#1

F9 AM57XX_STATUS#2 >>> STATUS#2

F8 AM57XX_STATUS#3 >>> STATUS#3

E7 AM57XX_STATUS#4 >>> STATUS#4

AM574X KEY

VIN2A_D6/KEY_COL#3

VIN2A_D7/KEY_COL#4

VIN2A_D86/KEY_COL#3

VIN2A_CLK0/KEY_ROW#0

VIN2A_DE0/KEY_ROW#1

VIN2A_HSYNC0/KEY_ROW#2

C1 AM57XX_KEY_COL#3 >>> KEY_COL#3

E4 AM57XX_KEY_COL#4 >>> KEY_COL#4

F5 AM57XX_KEY_COL#5 >>> KEY_COL#5

E1 AM57XX_KEY_ROW#0 >>> KEY_ROW#0

G2 AM57XX_KEY_ROW#1 >>> KEY_ROW#1

G1 AM57XX_KEY_ROW#2 >>> KEY_ROW#2

**AM574X
MLBP**

MLBP_DAT_p

MLBP_DAT_n

MLBP_CLK_p

MLBP_CLK_n

MLBP_SIG_p

MLBP_SIG_n

AA1 AM57XX_MLBP_DAT_p >>> MLBP_DAT_p

AA2 AM57XX_MLBP_DAT_n >>> MLBP_DAT_n

AB1 AM57XX_MLBP_CLK_p >>> MLBP_CLK_p

AB2 AM57XX_MLBP_CLK_n >>> MLBP_CLK_n

AC1 AM57XX_MLBP_SIG_p >>> MLBP_SIG_p

AC2 AM57XX_MLBP_SIG_n >>> MLBP_SIG_n

AM5749ABZXEA

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AM574X GPIO

GPIO_#2_22	P7	AM57XX GPIO #2 22	>>>	GPIO_#2_22
GPIO_#2_23	N1	AM57XX GPIO #2 23	>>>	GPIO_#2_23
GPIO_#2_24	M5	AM57XX GPIO #2 24	>>>	GPIO_#2_24
GPIO_#2_25	M3	AM57XX GPIO #2 25	>>>	GPIO_#2_25
GPIO_#2_26	N6	AM57XX GPIO #2 26	>>>	GPIO_#2_26
GPIO_#2_27	M4	AM57XX GPIO #2 27	>>>	GPIO_#2_27
GPIO_#2_28	N2	AM57XX GPIO #2 28	>>>	GPIO_#2_28

GPIO_#5_18	V2	R170	0/R1005	>>>	GPIO_USB2_VBUS_DET
GPIO_#5_19	Y1	R171	0/R1005	>>>	PMIC_INT

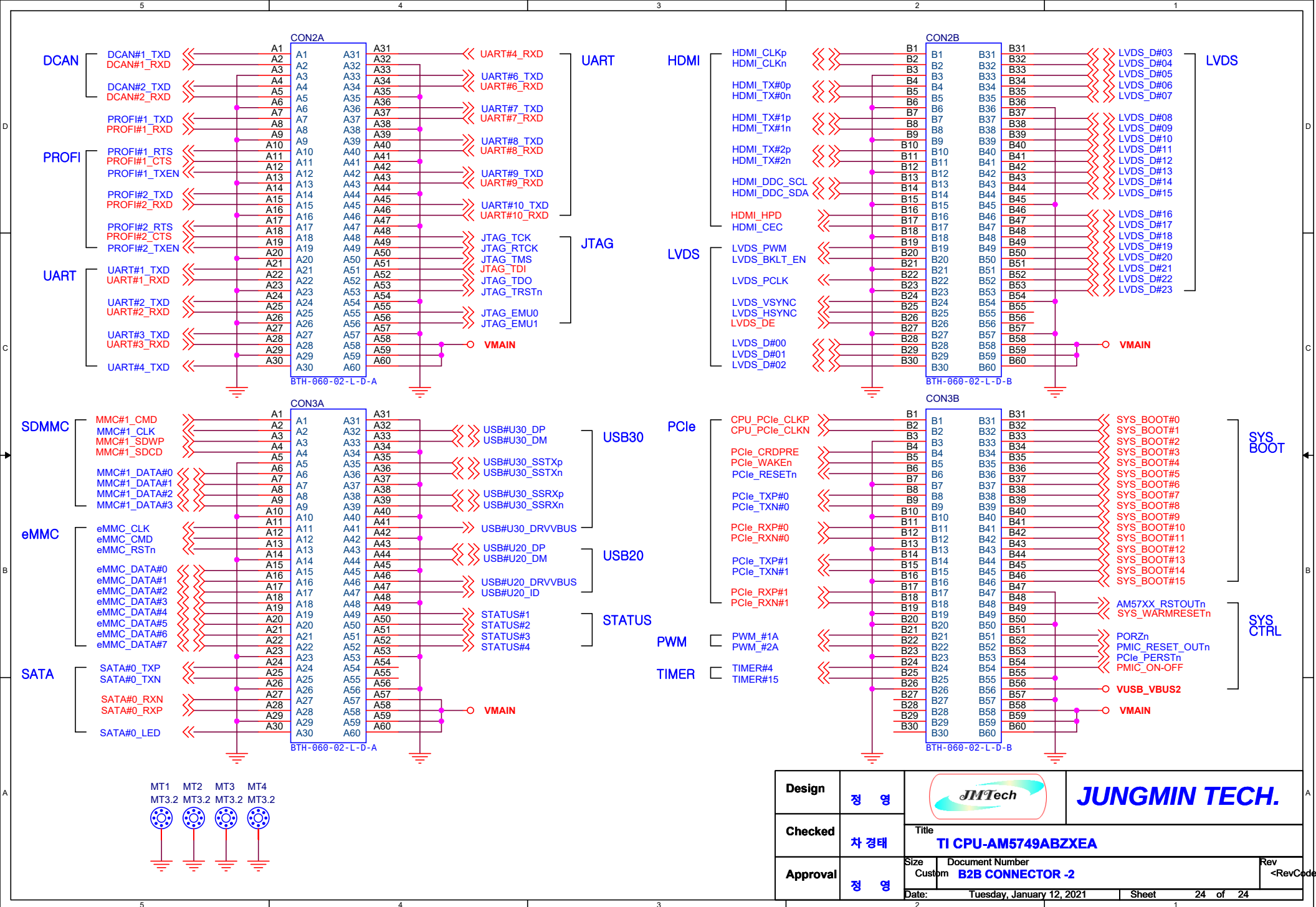
GPIO_#8_10	D7	AM57XX GPIO #8 10	>>>	GPIO_#8_10
GPIO_#8_15	C7	AM57XX GPIO #8 15	>>>	GPIO_#8_15
GPIO_#8_18	A7	AM57XX GPIO #8 18	>>>	GPIO_#8_18
GPIO_#8_19	A8	AM57XX GPIO #8 19	>>>	GPIO_#8_19
GPIO_#8_20	C9	AM57XX GPIO #8 20	>>>	GPIO_#8_20
GPIO_#8_21	A9	AM57XX GPIO #8 21	>>>	GPIO_#8_21
GPIO_#8_22	B9	AM57XX GPIO #8 22	>>>	GPIO_#8_22
GPIO_#8_23	A10	AM57XX GPIO #8 23	>>>	GPIO_#8_23

AM574X Unused Pins

mcasp8_axr2	E15	AM57XX mcasp8_axr2	>>>	mcasp8_axr2
mcasp8_axr3	A20	AM57XX mcasp8_axr3	>>>	mcasp8_axr3
mcasp2_axr0	B15	AM57XX mcasp2_axr0	>>>	mcasp2_axr0
mcasp2_axr1	A15	AM57XX mcasp2_axr1	>>>	mcasp2_axr1
pr2_pru1_gpo1	AA3	AM57XX pr2_pru1_gpo1	>>>	pr2_pru1_gpo1
pr2_pru1_gpi2	AB9	AM57XX pr2_pru1_gpi2	>>>	pr2_pru1_gpi2
pr1_edc_sync0_out	D2	AM57XX pr1_edc_sync0_out	>>>	pr1_edc_sync0_out
pr1_edc_latch0_in	E2	AM57XX pr1_edc_latch0_in	>>>	pr1_edc_latch0_in

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