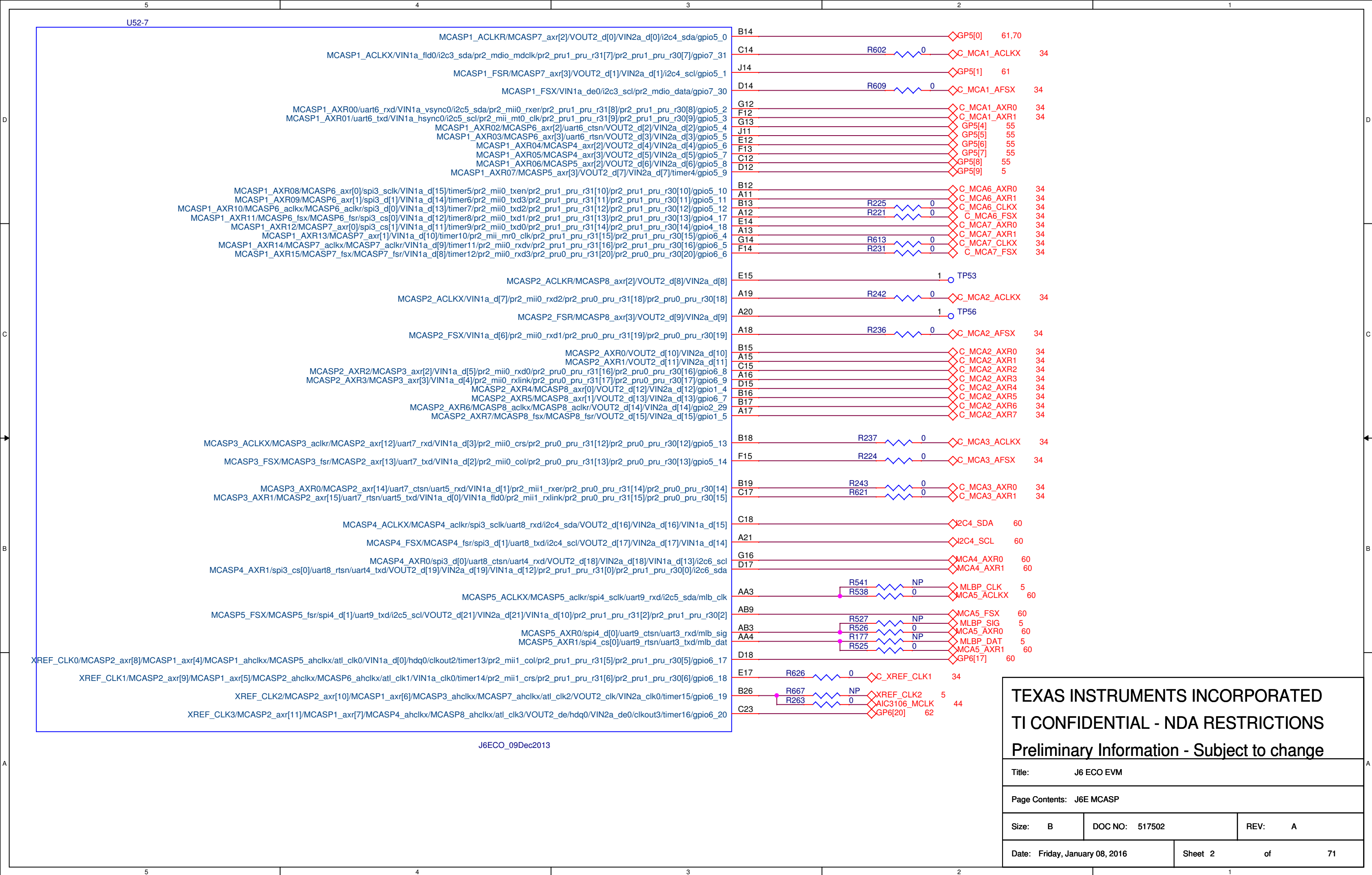
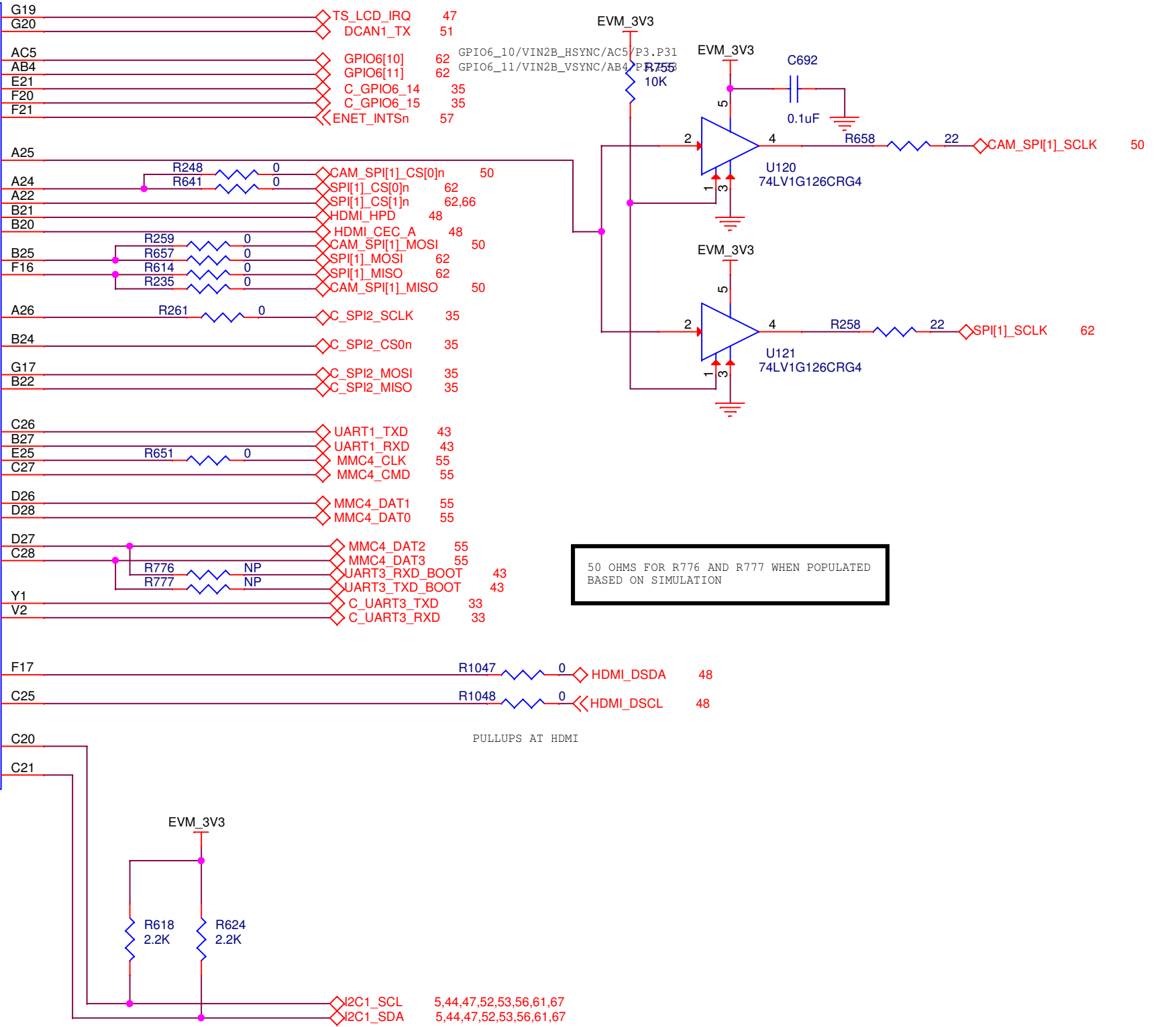


5										4										3										2										1									





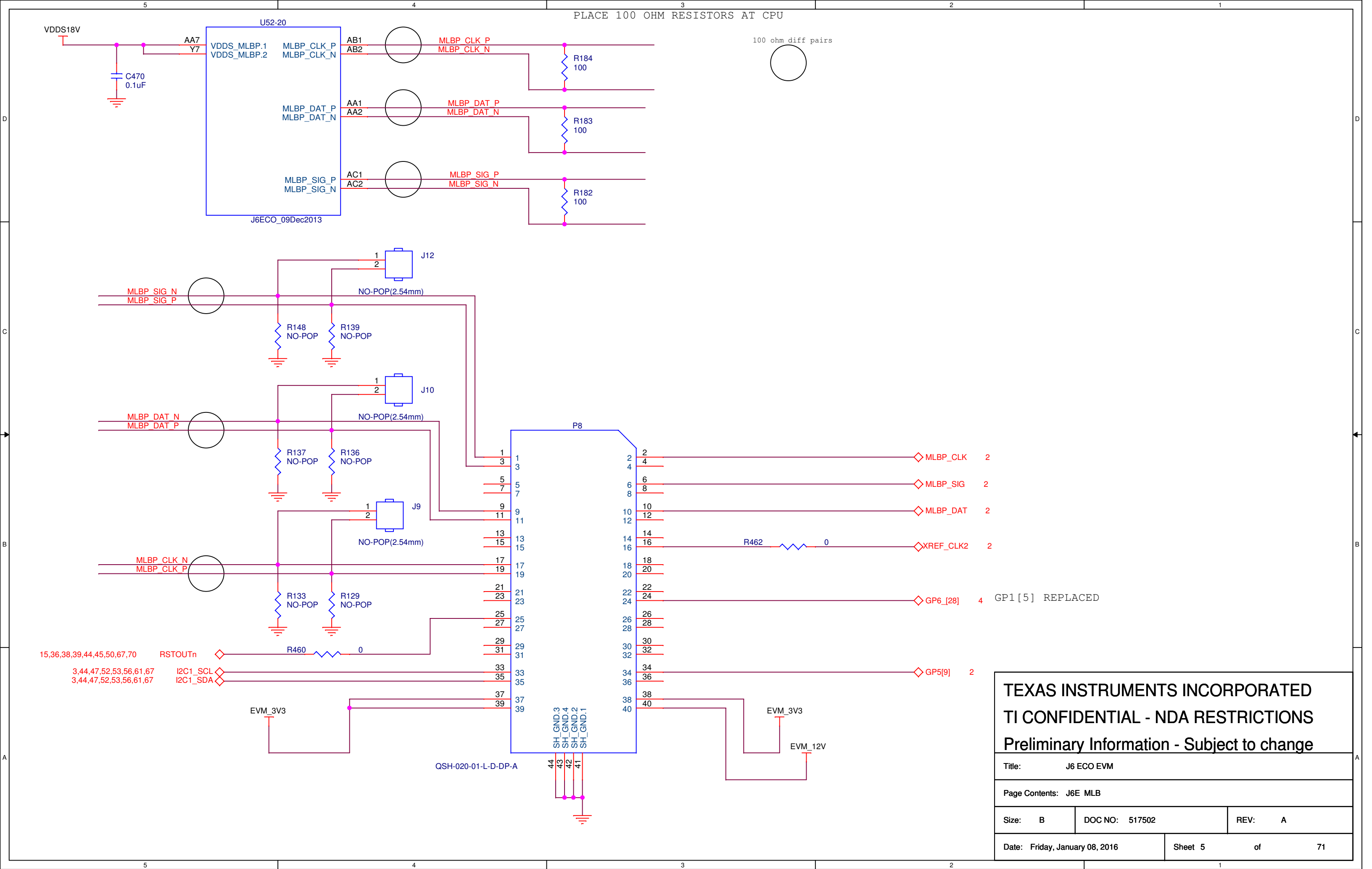
TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

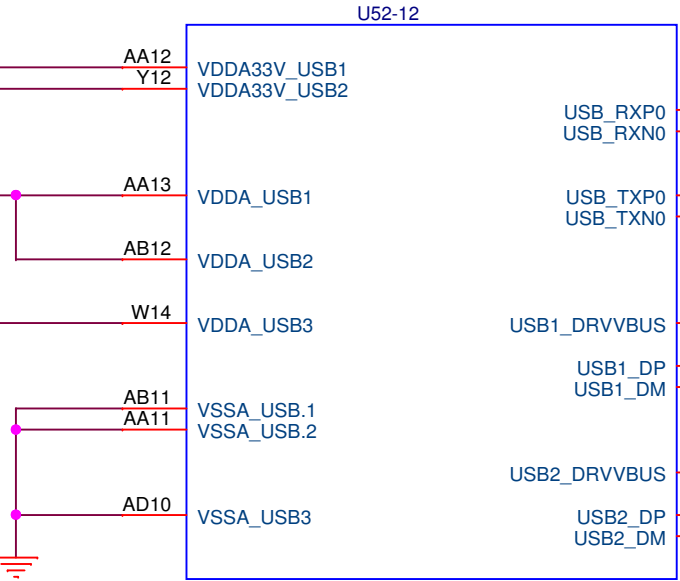
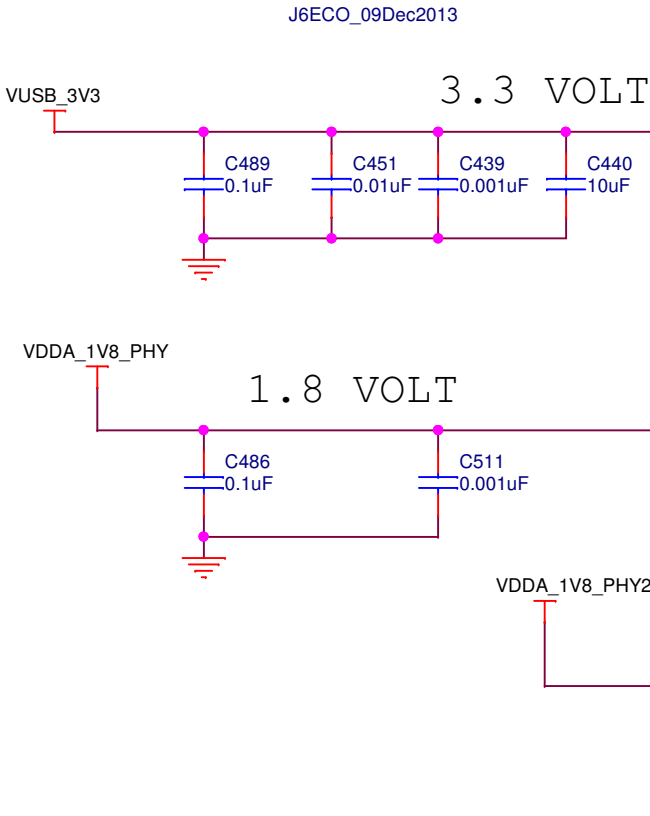
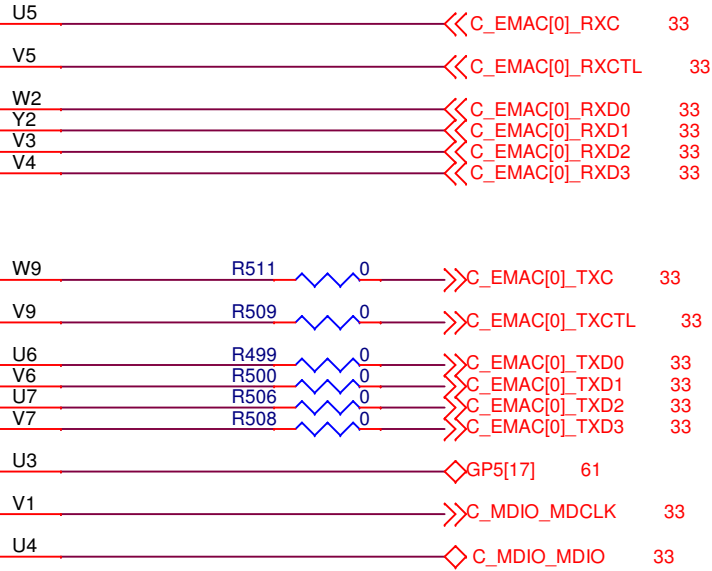
Title: J6 ECO EVM		
Page Contents: J6E SERIAL I/O		
Size: B	DOC NO: 517502	REV: C
Date: Friday, January 08, 2016	Sheet 3	of 71



J6ECO_09Dec2013

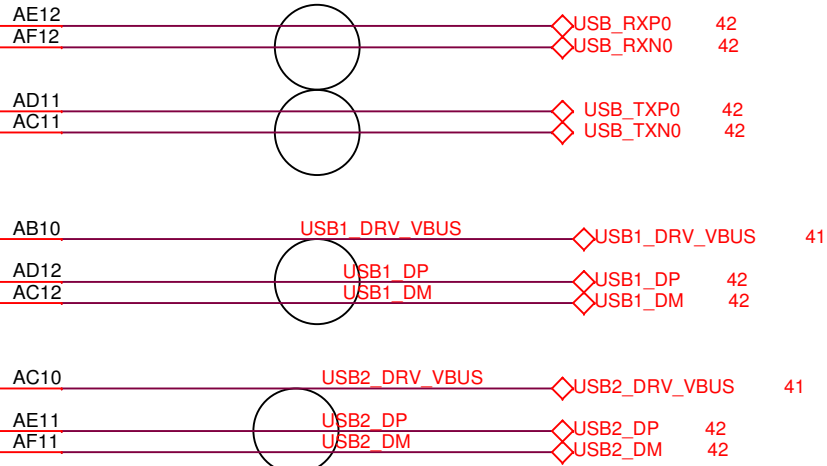
TEXAS INSTRUMENTS INCORPORATED		
TI CONFIDENTIAL - NDA RESTRICTIONS		
Preliminary Information - Subject to change		
Title: J6 ECO EVM		
Page Contents: J6E SD/MMC IF		
Size: B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016	Sheet 4	of 71





J6ECO_09Dec2013

DIFFERENTIAL PAIR
90 OHM DIFFERENTIAL
IMPEDANCE
SHORT AND STRAIGHT AS
POSSIBLE,
MINIMUM NUMBER OF VIAS



TEXAS INSTRUMENTS INCORPORATED

TI CONFIDENTIAL - NDA RESTRICTIONS

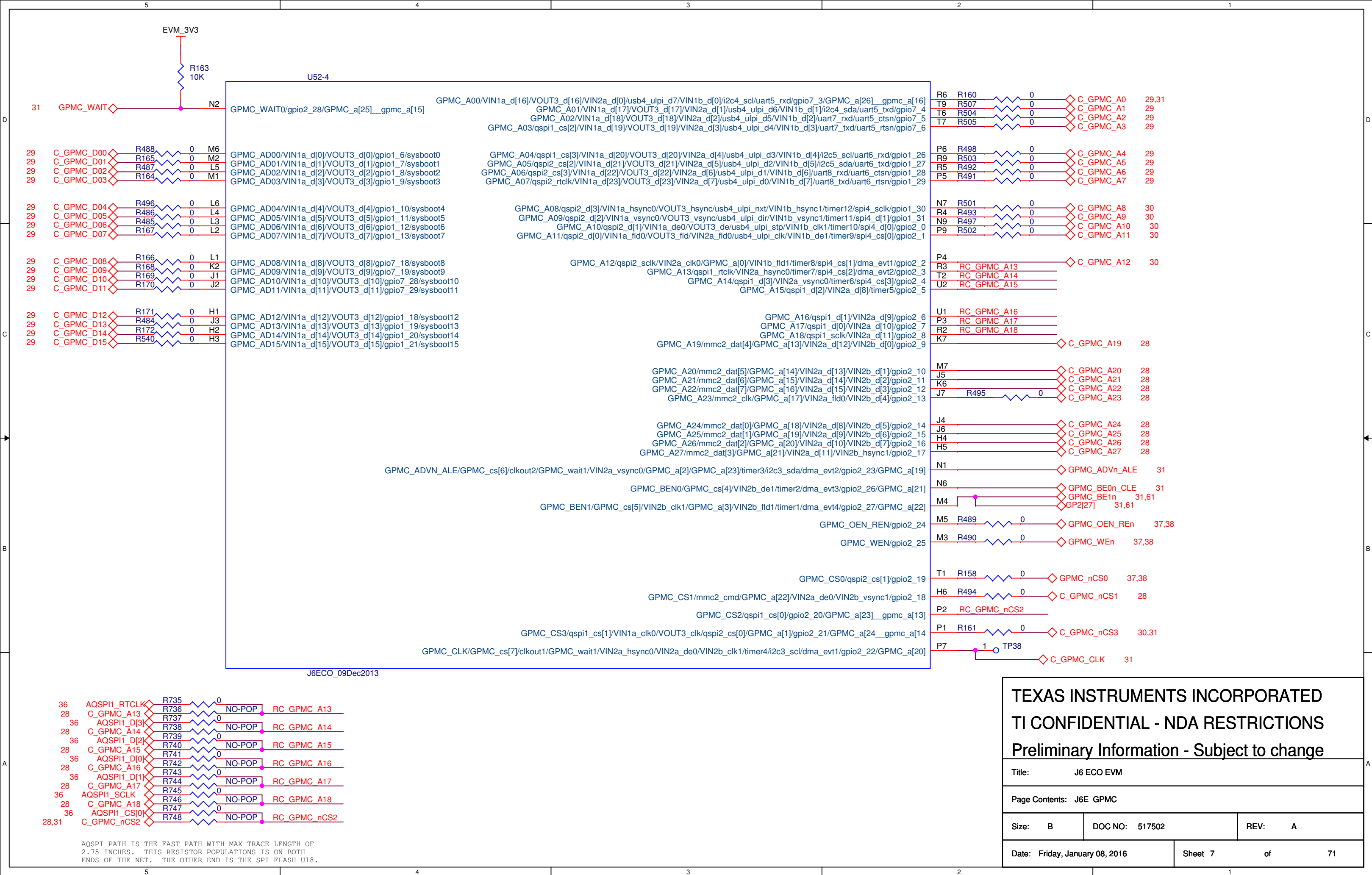
Preliminary Information - Subject to change

Title: J6 ECO EVM

Page Contents: J6E ETHERNET/USB

Size: B	DOC NO: 517502	REV: A
---------	----------------	--------

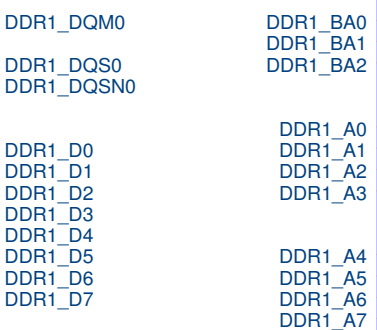
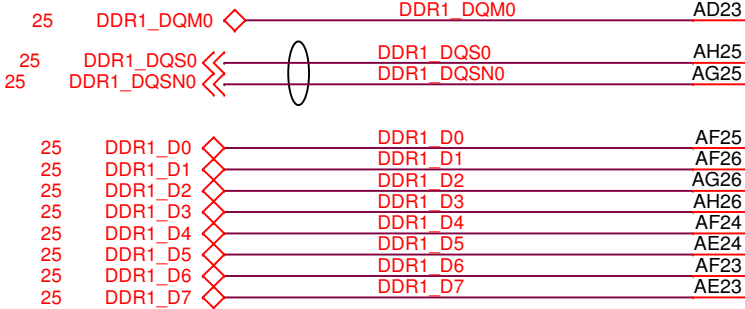
Date: Friday, January 08, 2016	Sheet 6 of 71
--------------------------------	---------------



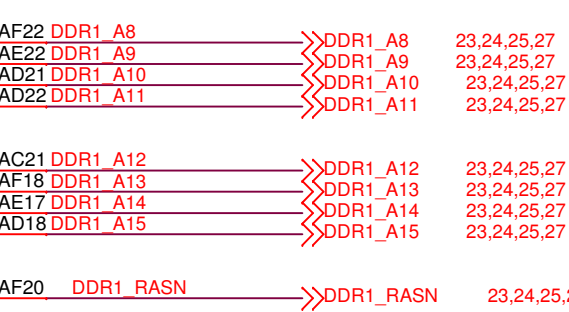
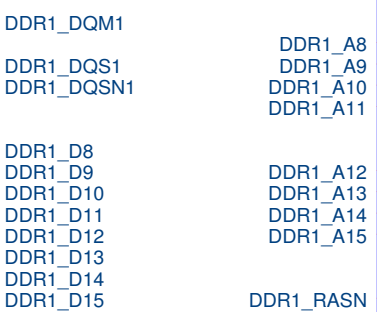
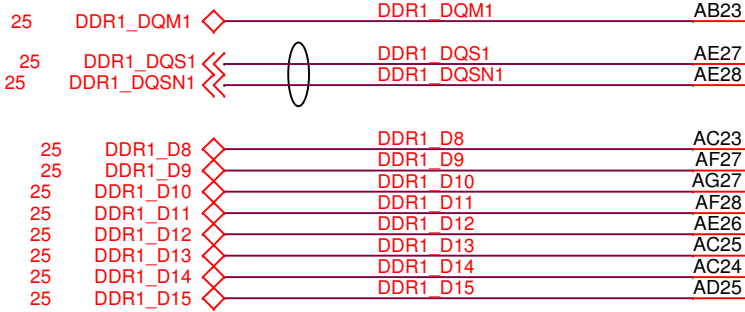
EACH DATA GROUP IS MATCHED WITHIN THE GROUP. SEE PCB GUIDELINES FOR SPECIFIC RULES.

0 differential pairs 100 ohm
6 places

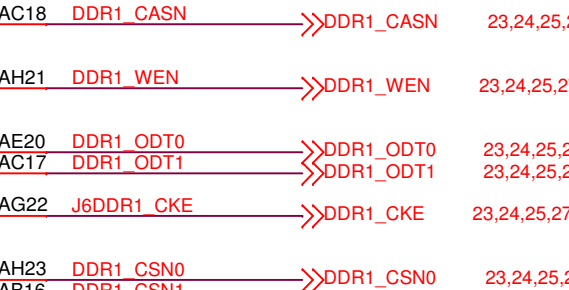
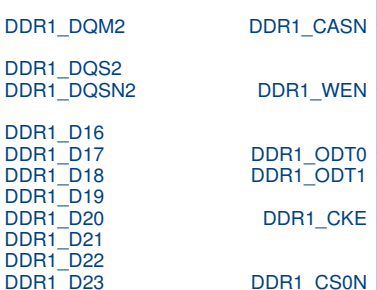
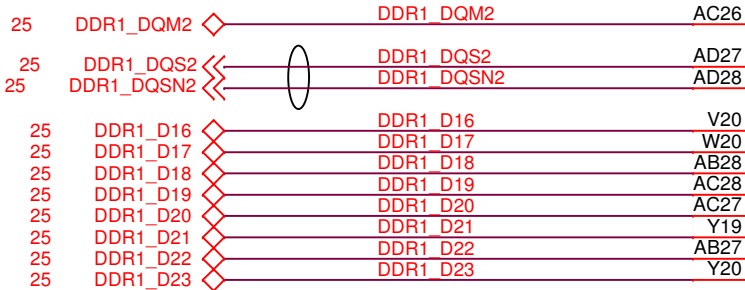
Data Group 0-0



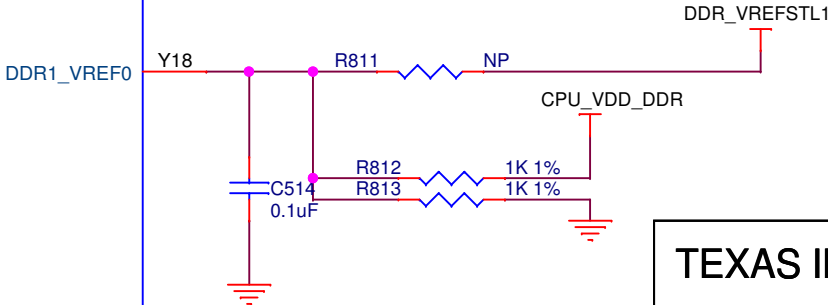
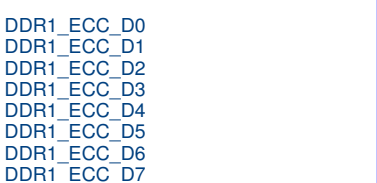
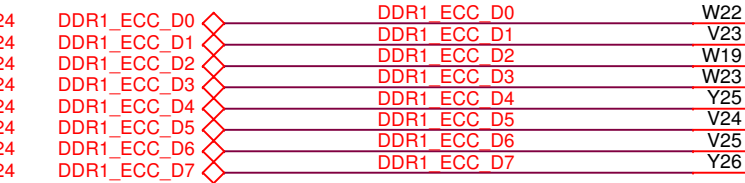
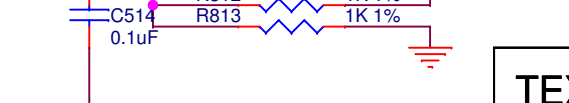
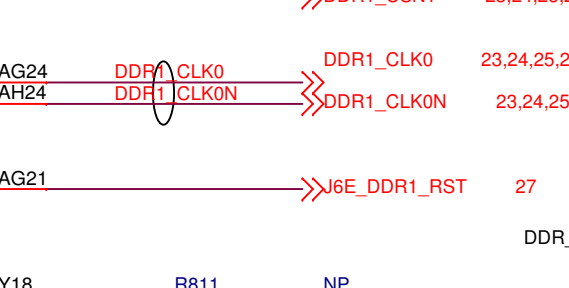
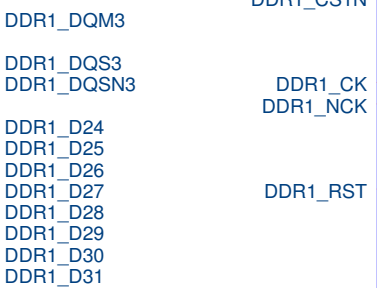
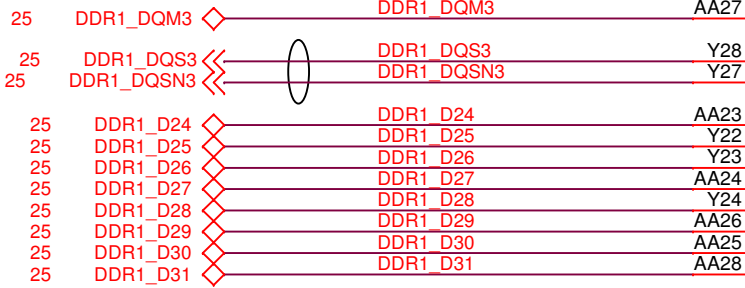
Data Group 0-1



Data Group 0-2



Data Group 0-3



TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

Title: J6 ECO EVM

Page Contents: J6E DDR EMIF1 INTERFACE

Size: B

DOC NO: 517502

REV: B

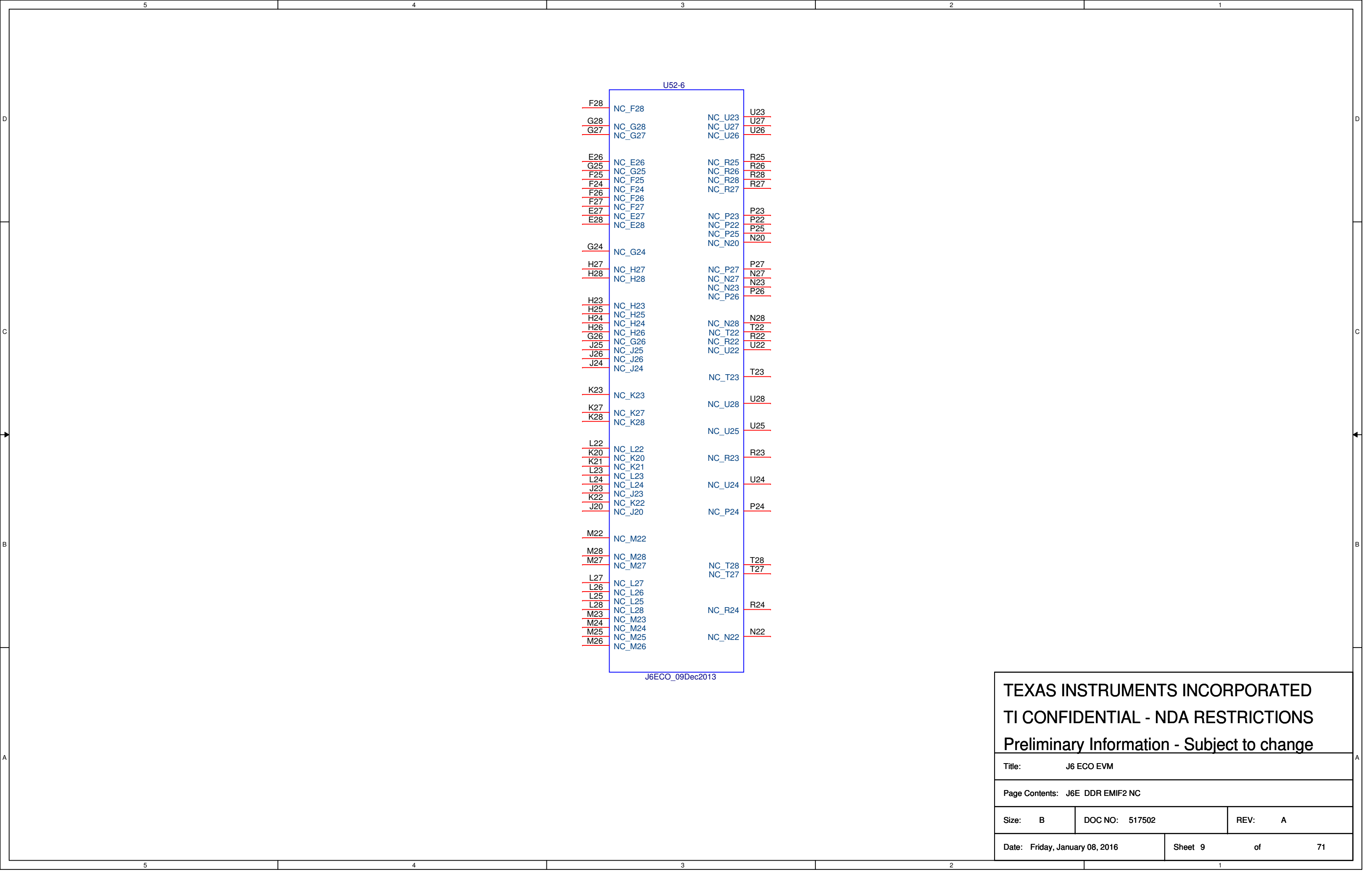
Date: Friday, January 08, 2016

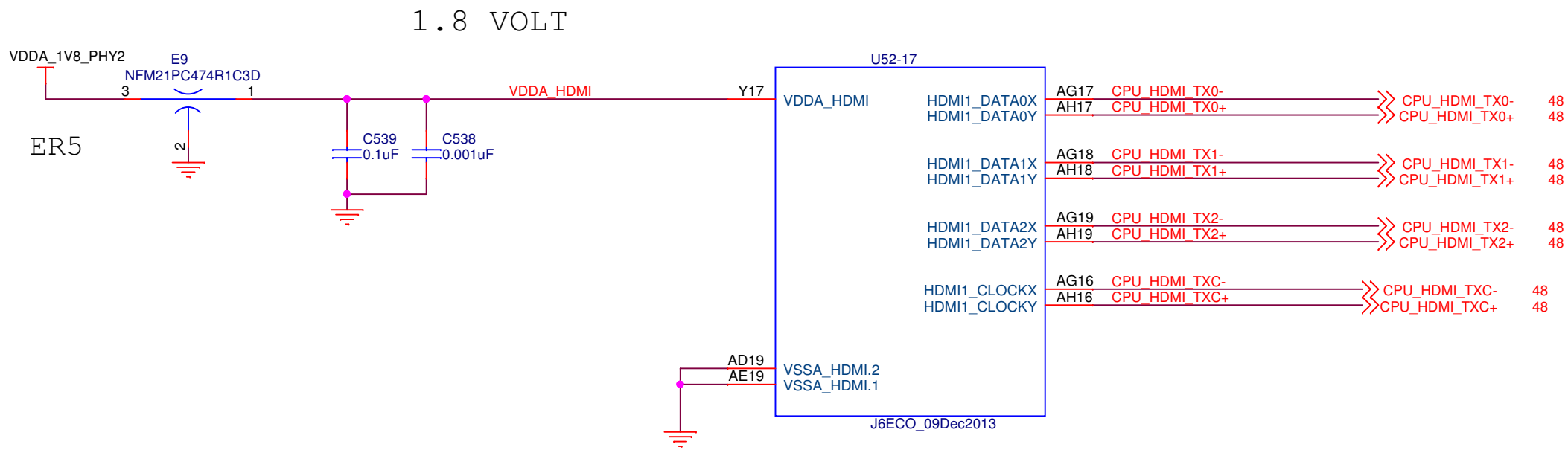
Sheet 8

of

71

J6ECO_09Dec2013





TEXAS INSTRUMENTS INCORPORATED			
TI CONFIDENTIAL - NDA RESTRICTIONS			
Preliminary Information - Subject to change			
Title:		J6 ECO EVM	
Page Contents: J6E HDMI			
Size:	B	DOC NO:	517502
REV:	C		
Date:	Friday, January 08, 2016	Sheet 10	of 71

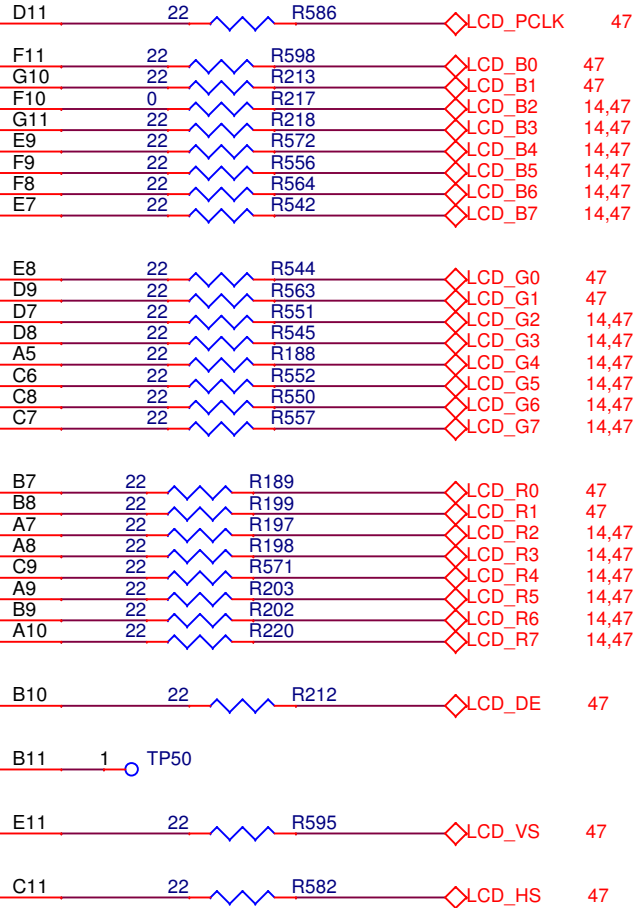
22 OHM OK FOR LCD AS IT IS NOT GOING THROUGH MUXES.

U52-2



J6ECO_09Dec2013

LCD_B2 = EMU2 TRACE CLOCK



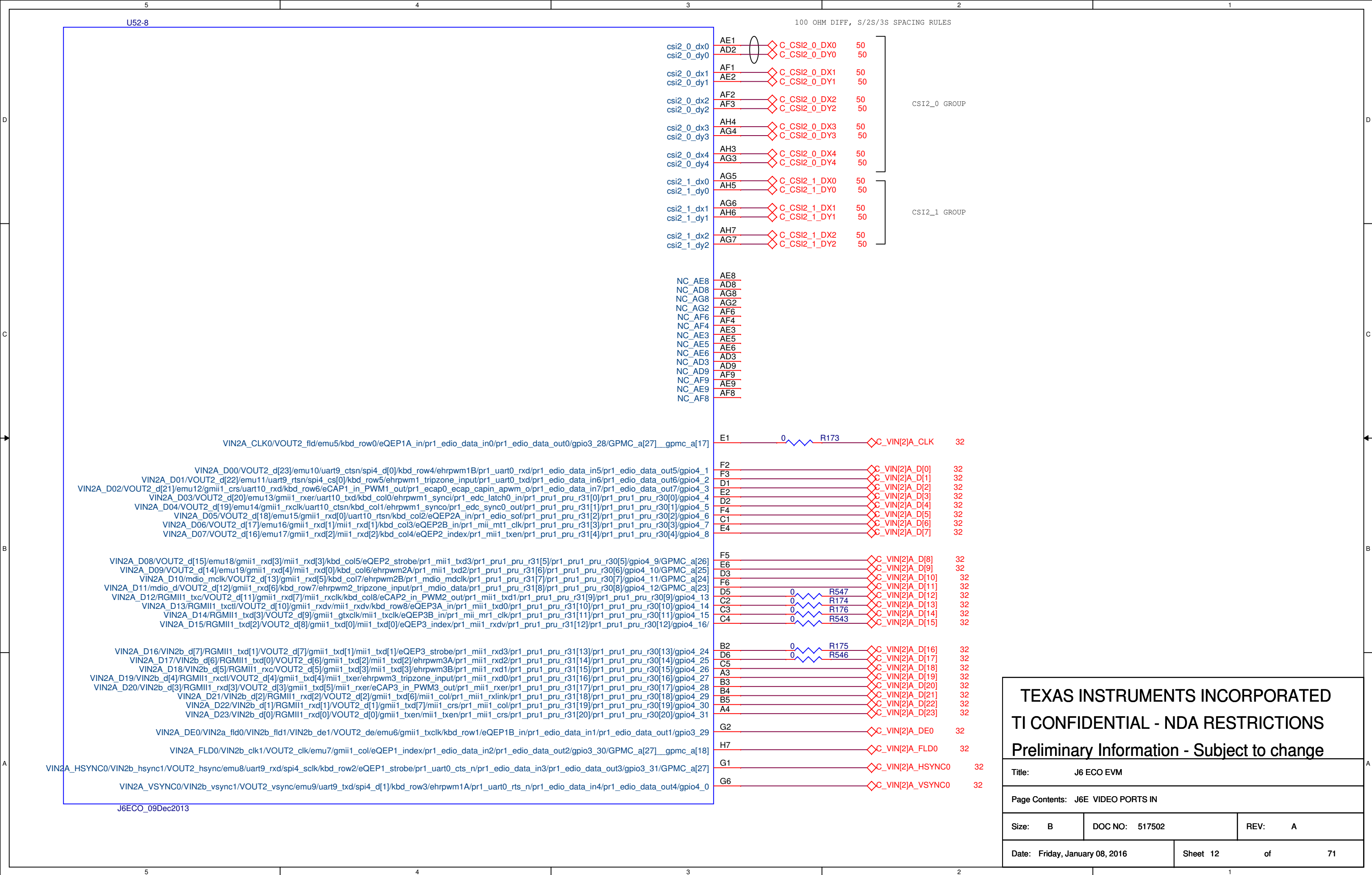
TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

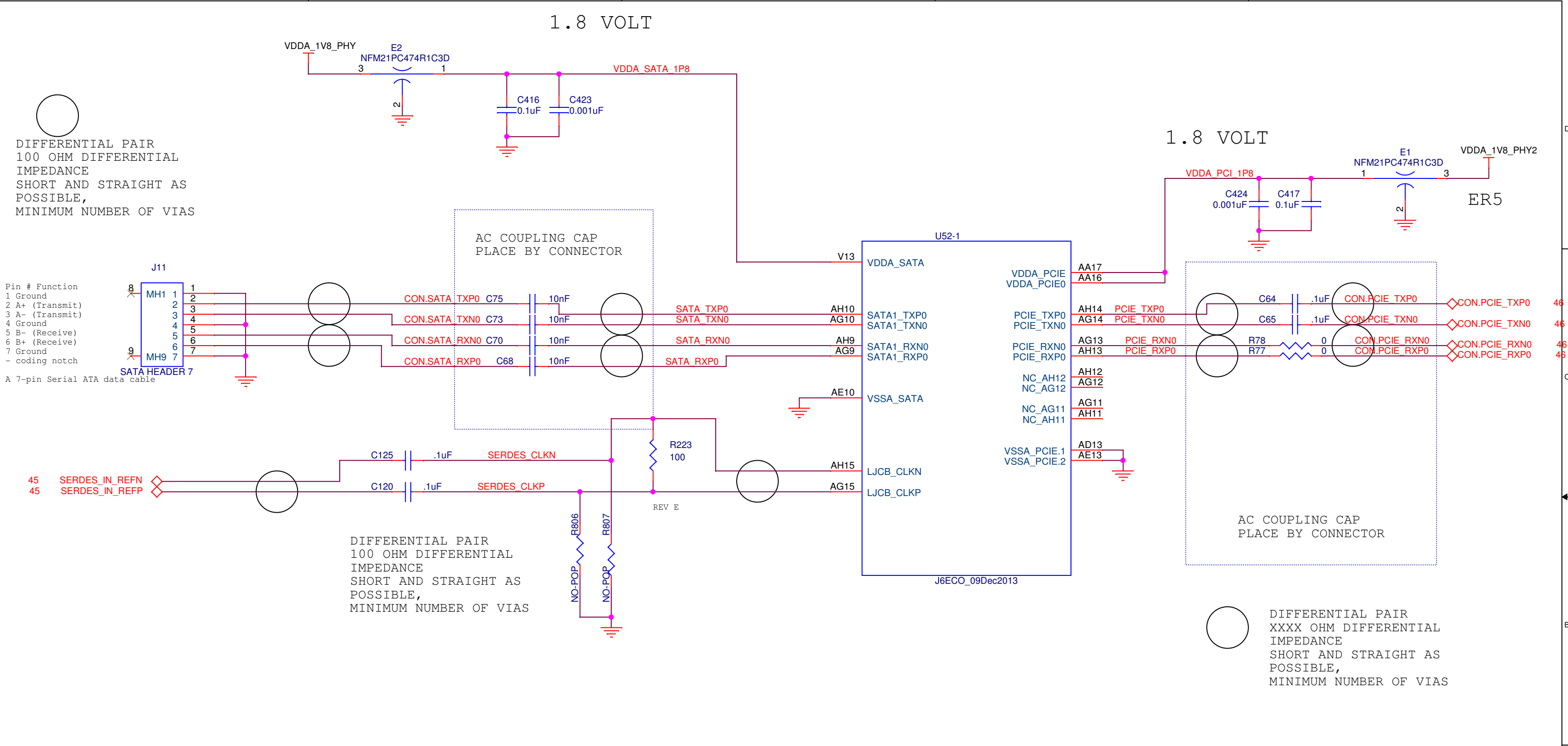
Title: J6 ECO EVM

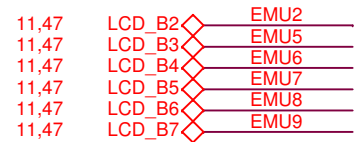
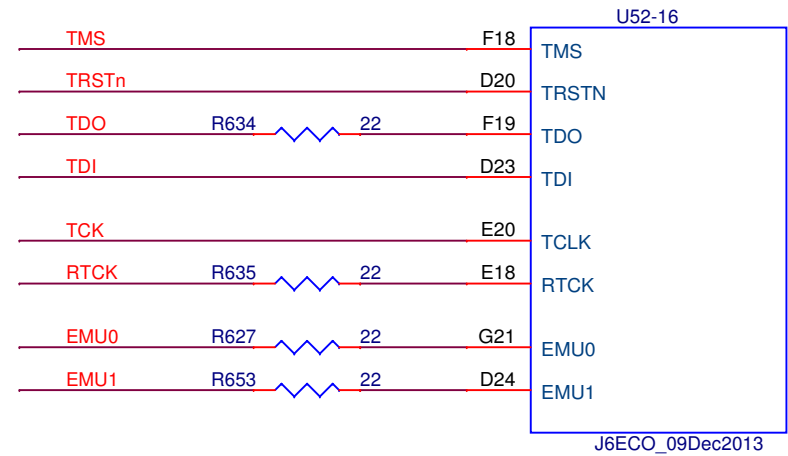
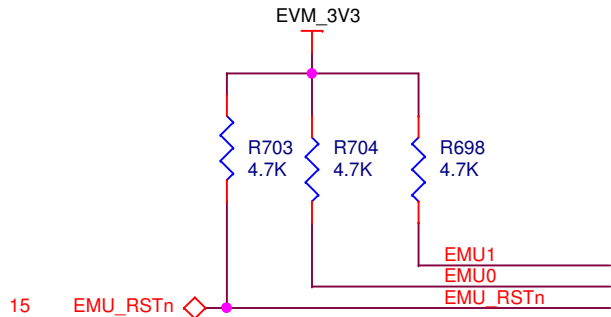
Page Contents: J6E VOUT1 PORT LCD

Size: B DOC NO: 517502 REV: A

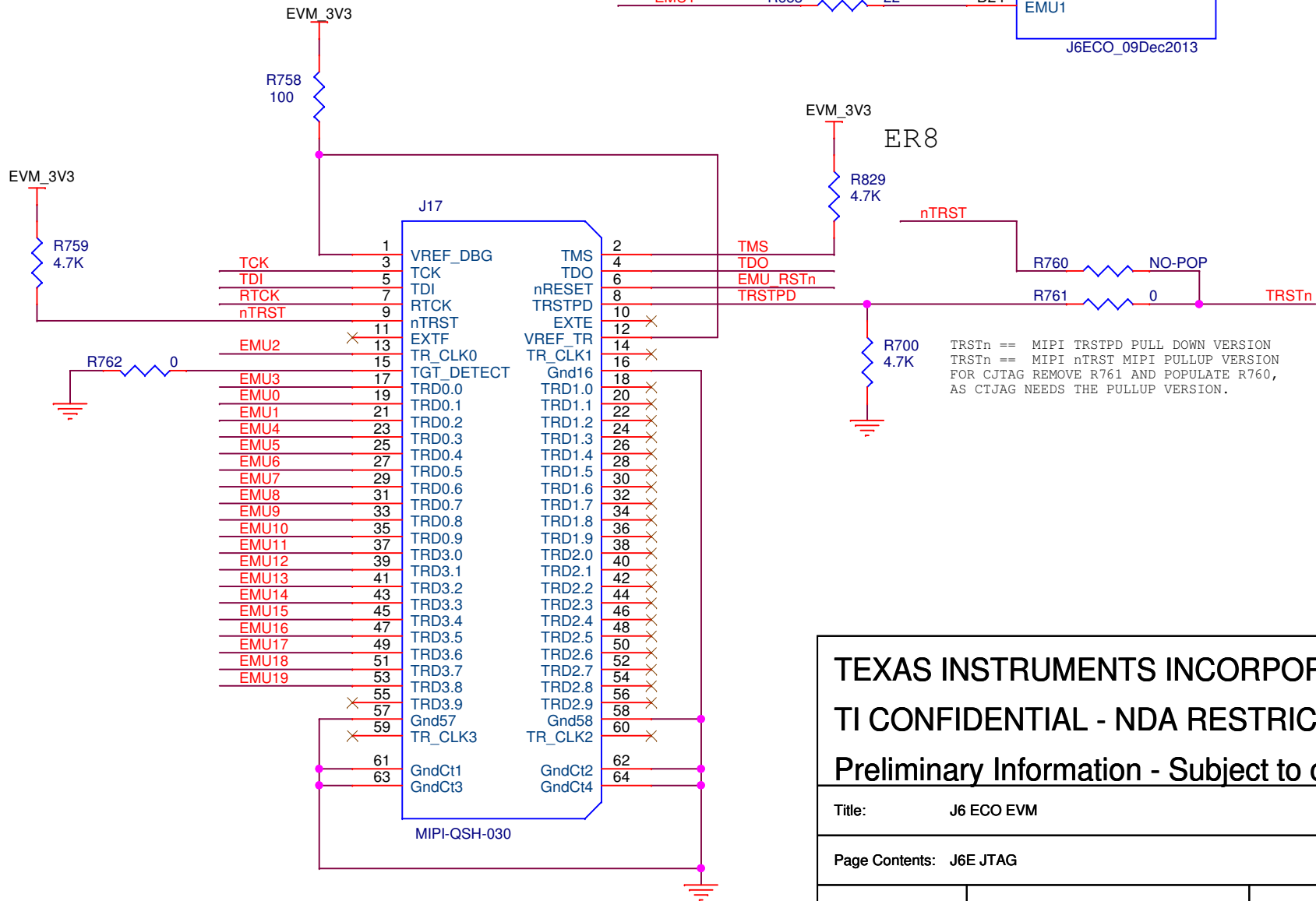
Date: Friday, January 08, 2016 Sheet 11 of 71

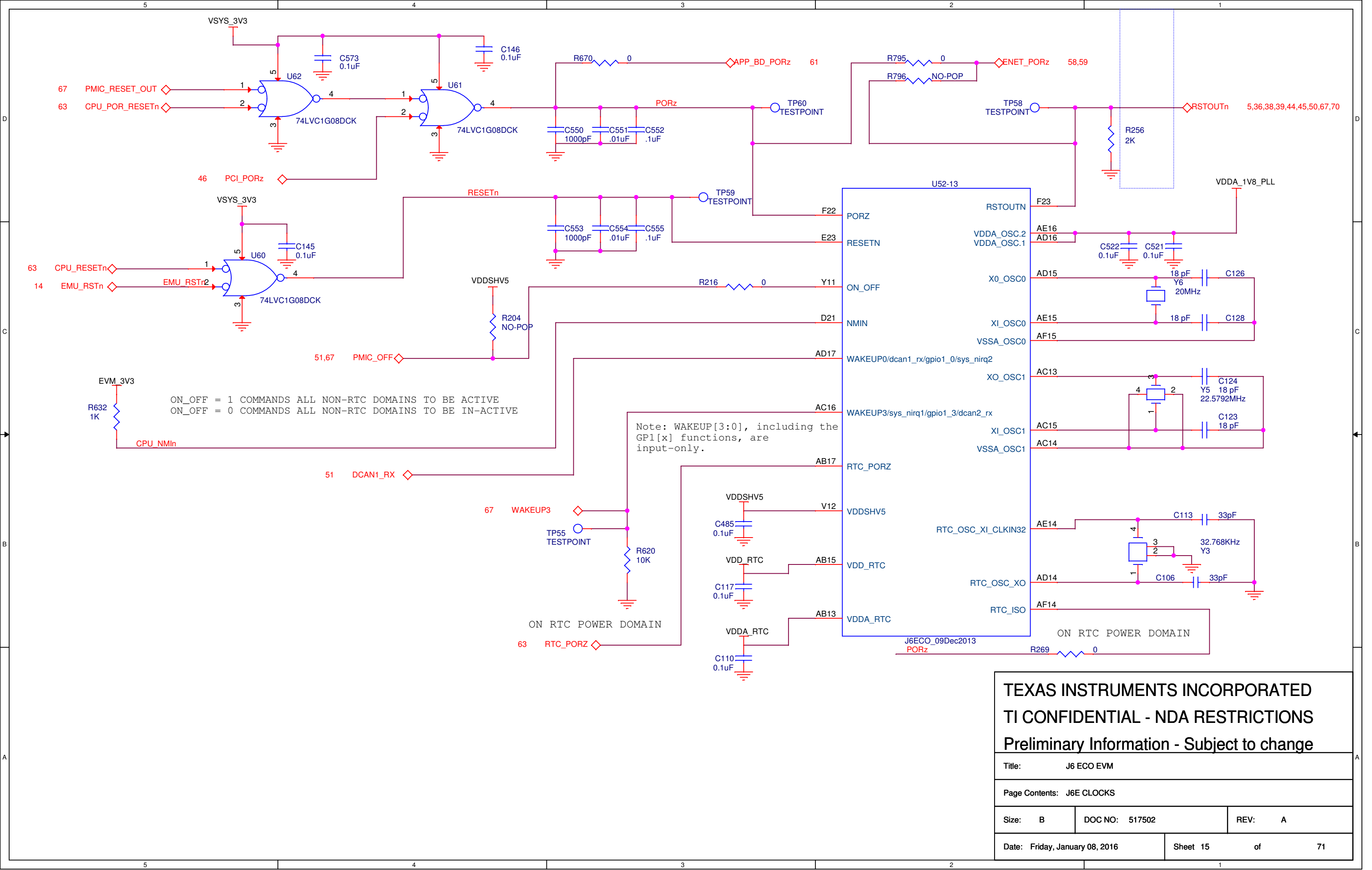


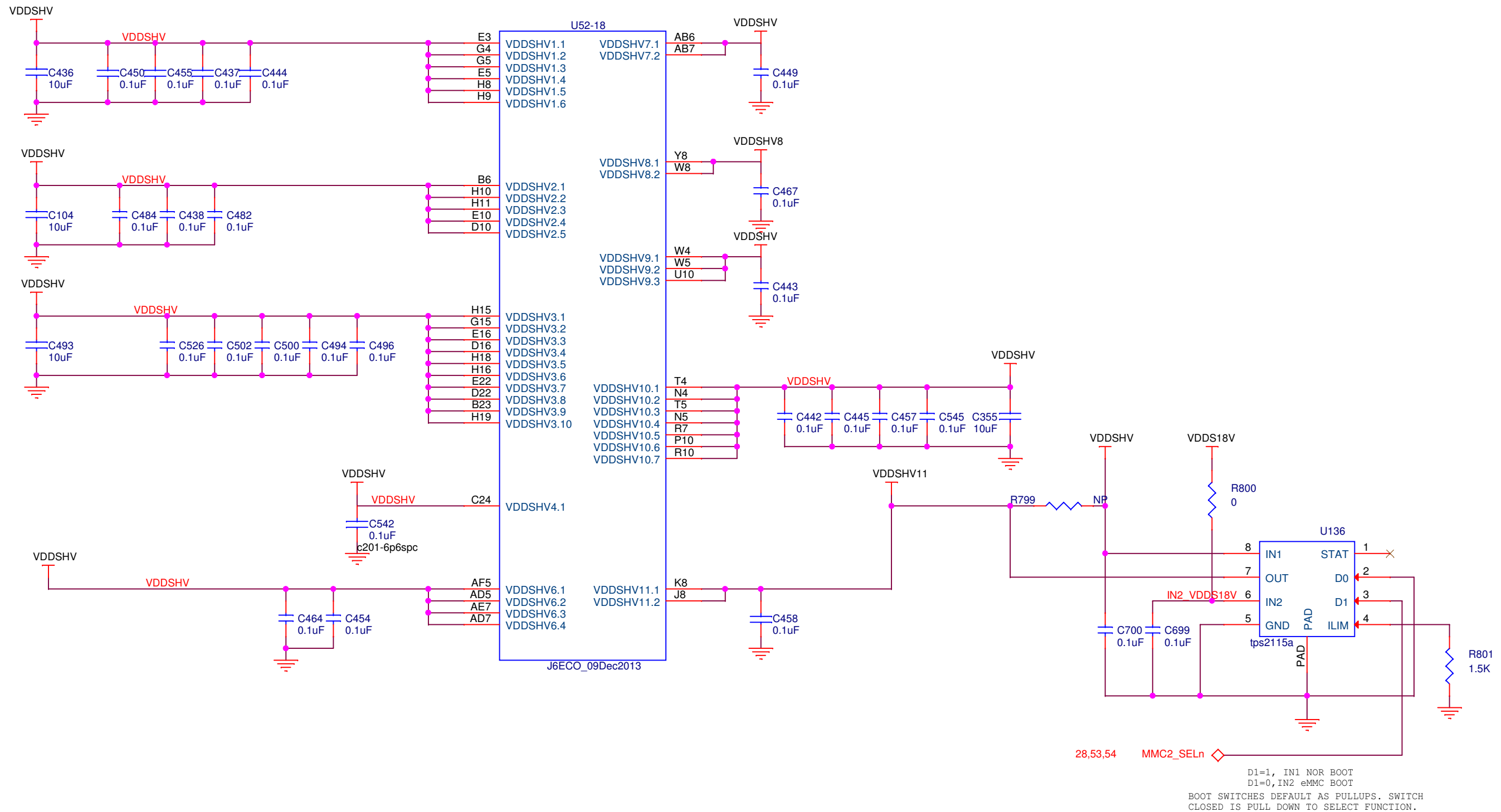




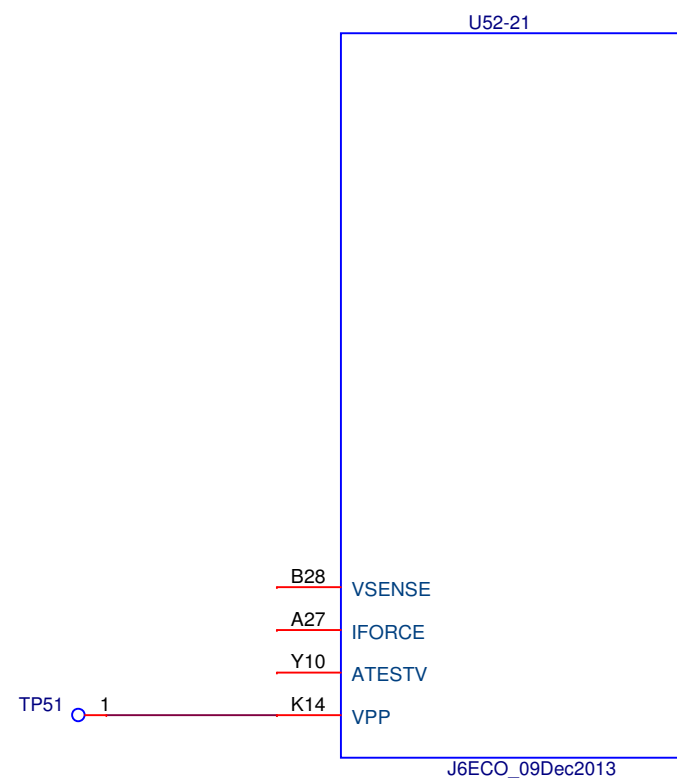
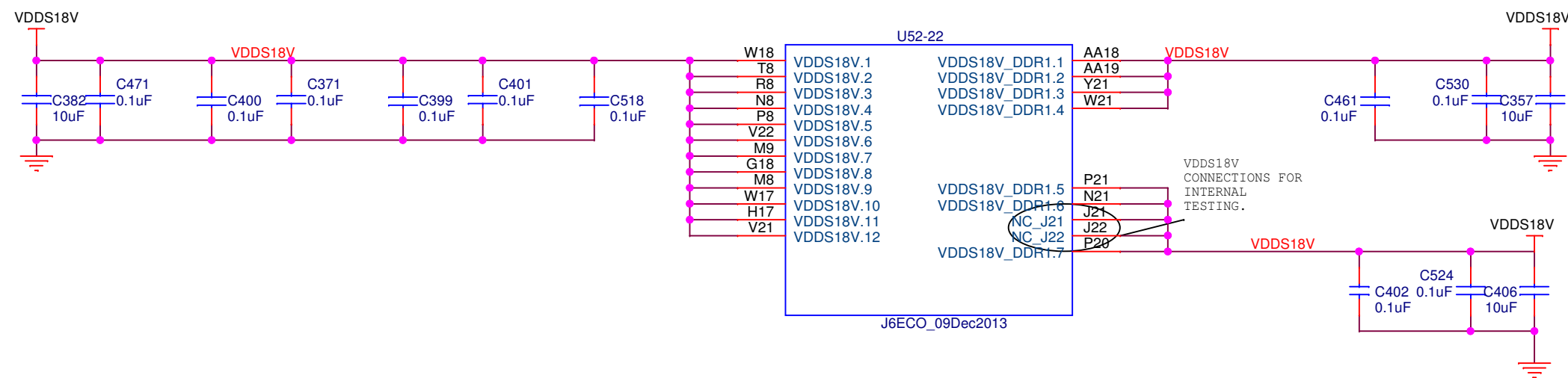
EMUx SIGNALS NEED TO ROUTE FROM U52 THROUGH J17/J14 THEN TO LCD. THE TRACE LENGTH RULES FOR THE LCD SIGNALS NEEDS TO BE MAINTAINED.







TEXAS INSTRUMENTS INCORPORATED			
TI CONFIDENTIAL - NDA RESTRICTIONS			
Preliminary Information - Subject to change			
Title: J6 ECO EVM			
Page Contents: J6E IO POWER			
Size: B	DOC NO: 517502		REV: A
Date: Friday, January 08, 2016		Sheet 17	of 71



TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

Title: J6 ECO EVM

Page Contents: J6E POWER 1V8

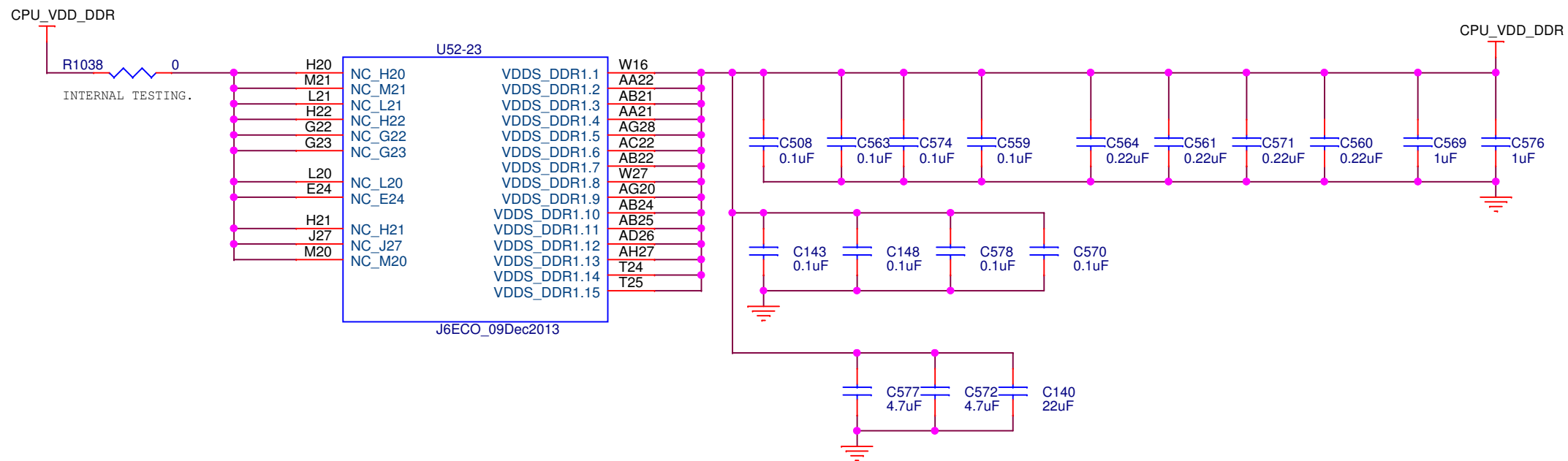
Size: B

DOC NO: 517502

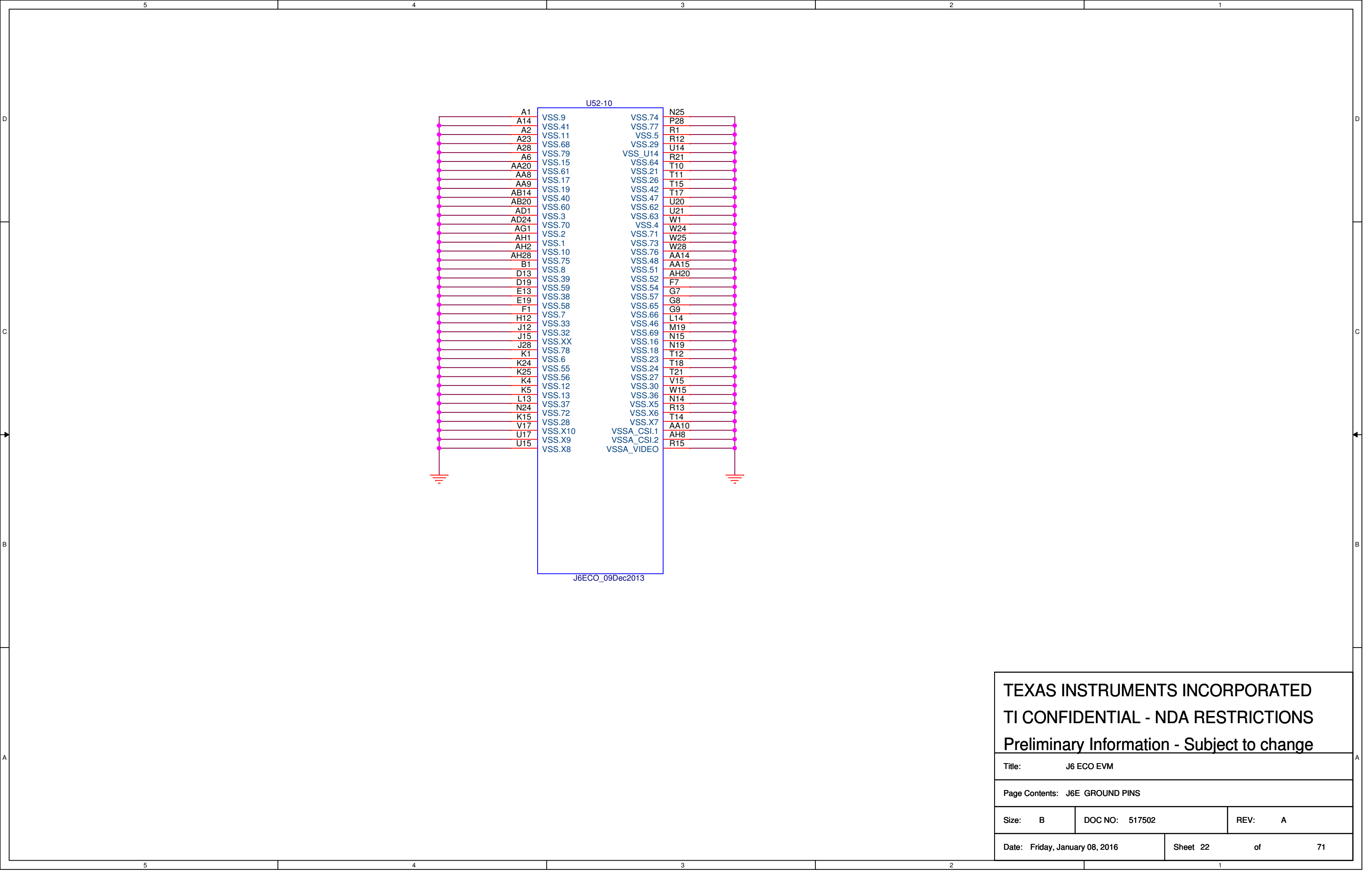
REV: A

Date: Friday, January 08, 2016

Sheet 18 of 71

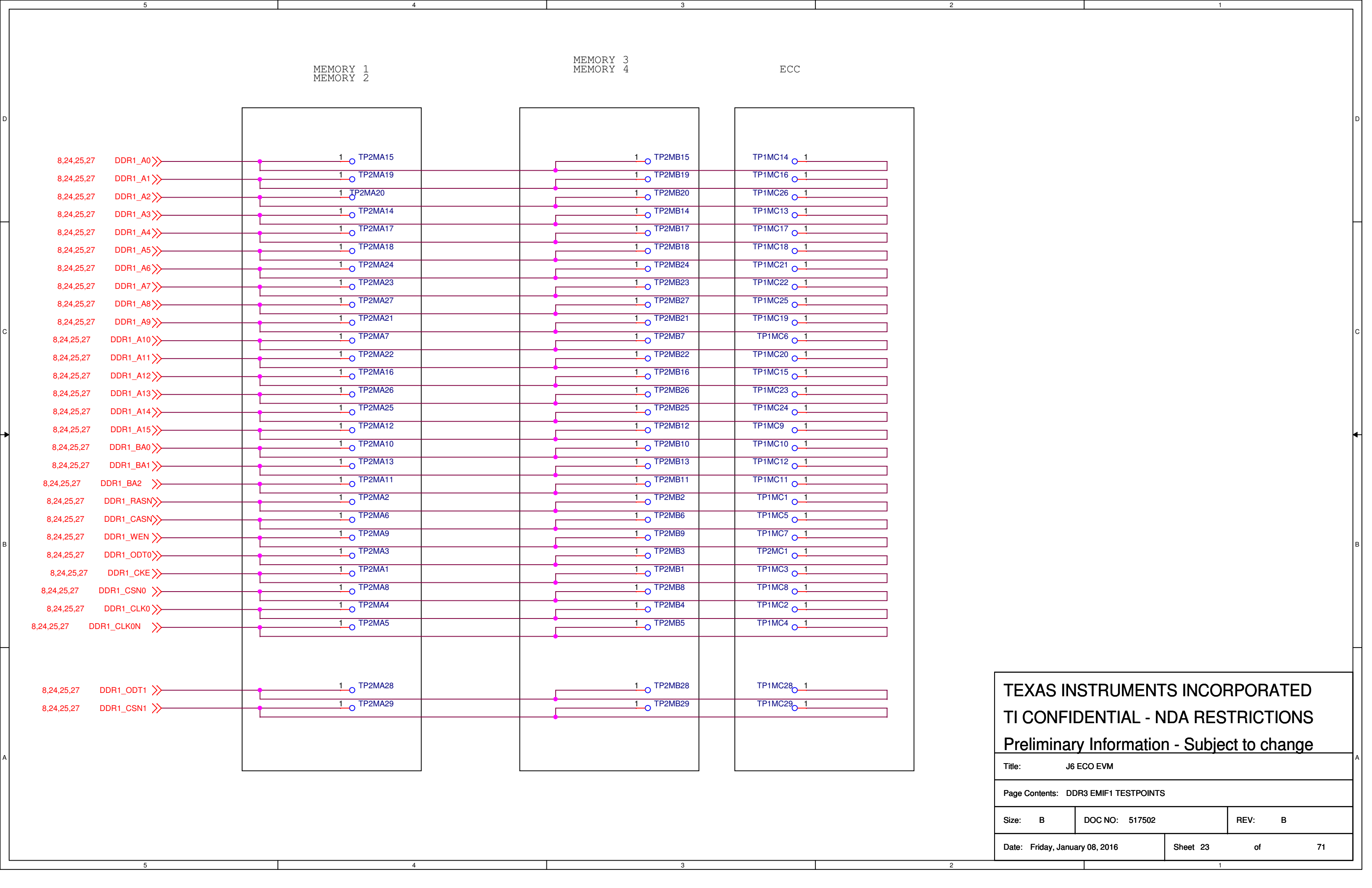


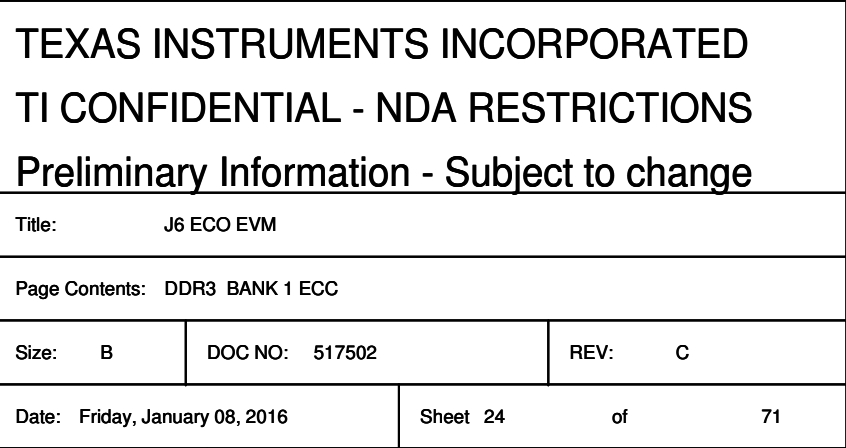
TEXAS INSTRUMENTS INCORPORATED			
TI CONFIDENTIAL - NDA RESTRICTIONS			
Preliminary Information - Subject to change			
Title:		J6 ECO EVM	
Page Contents: J6E DDR POWER			
Size:	B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016		Sheet 19 of 71	

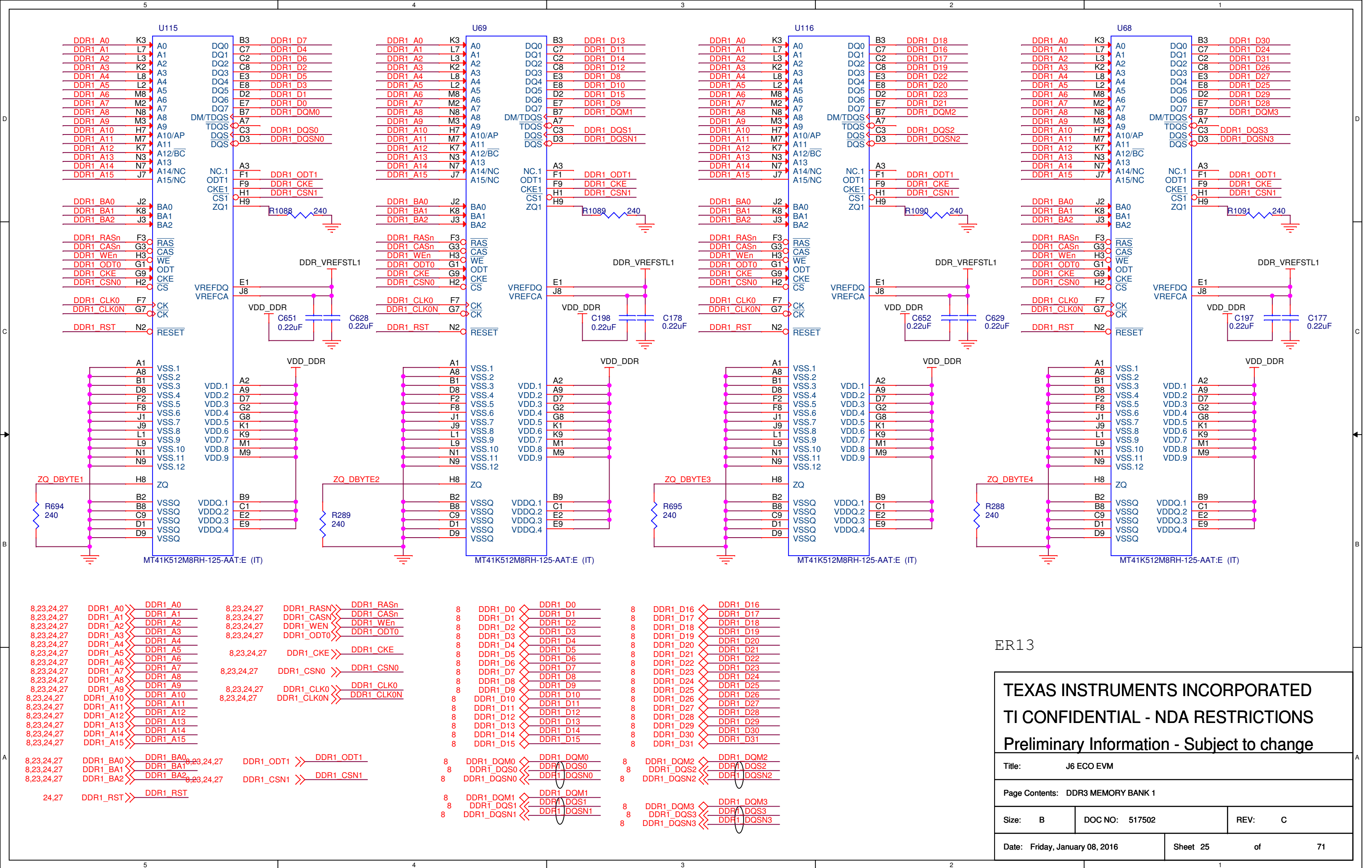


TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

Title: J6 ECO EVM		
Page Contents: J6E GROUND PINS		
Size: B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016	Sheet 22	of 71







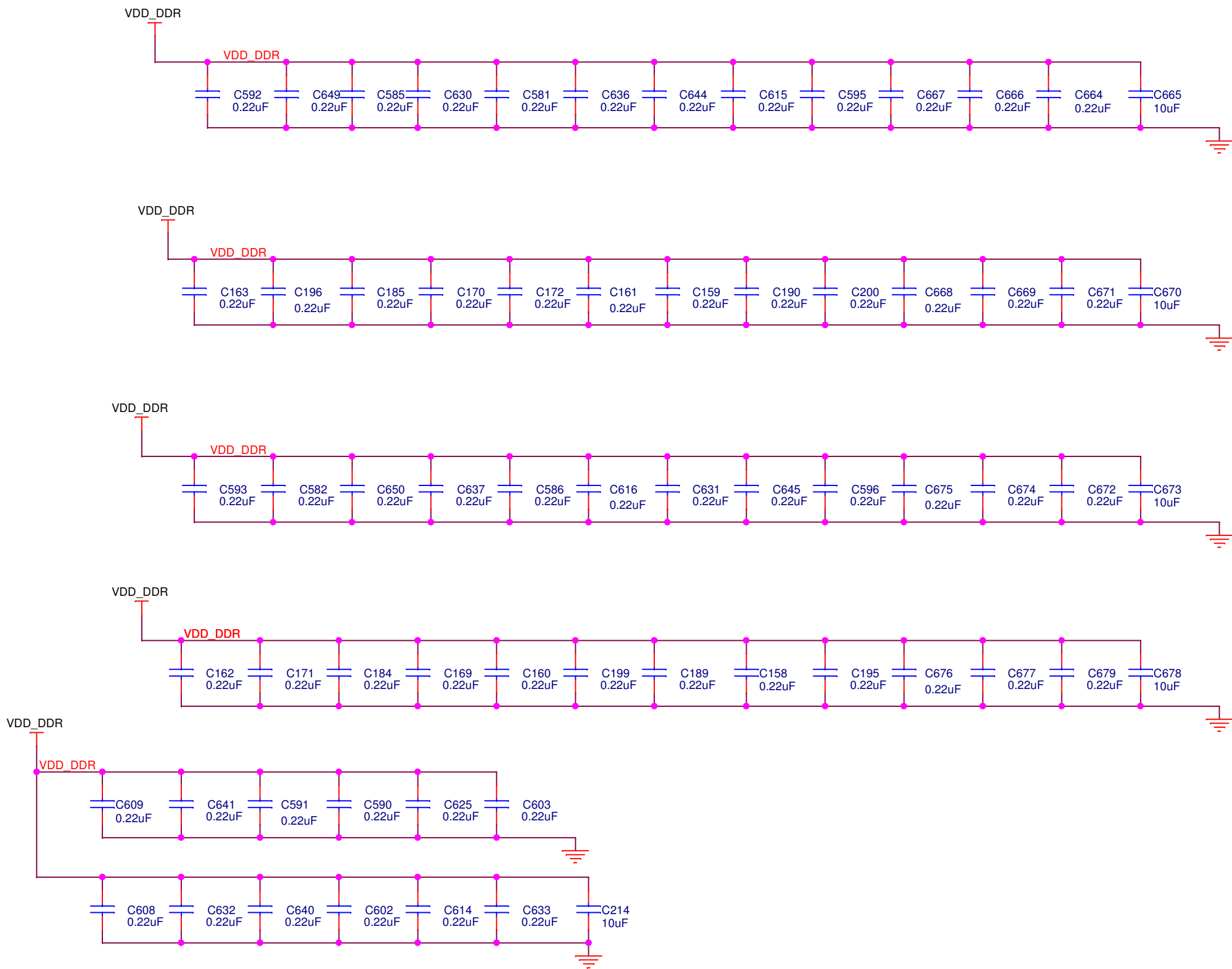
ER13

TEXAS INSTRUMENTS INCORPORATED

TI CONFIDENTIAL - NDA RESTRICTIONS

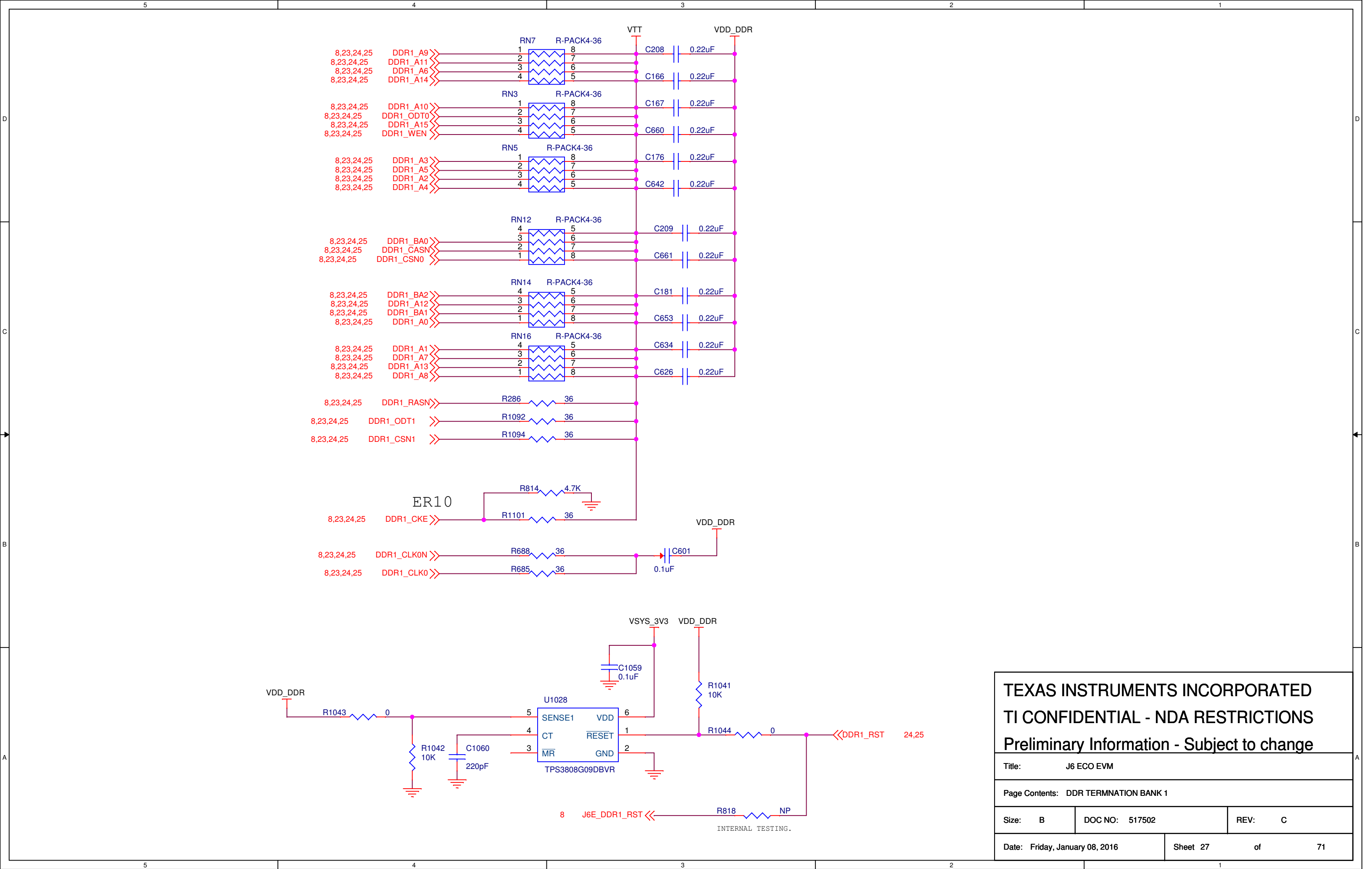
Preliminary Information - Subject to change

Title: J6 ECO EVM		
Page Contents: DDR3 MEMORY BANK 1		
Size: B	DOC NO: 517502	REV: C
Date: Friday, January 08, 2016		Sheet 25 of 71



TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

Title: J6 ECO EVM		
Page Contents: DDR3 MEMORY CAPS BANK 1		
Size: B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016	Sheet 26	of 71



TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

Title: J6 ECO EVM

Page Contents: DDR TERMINATION BANK 1

Size: B

DOC NO: 517502

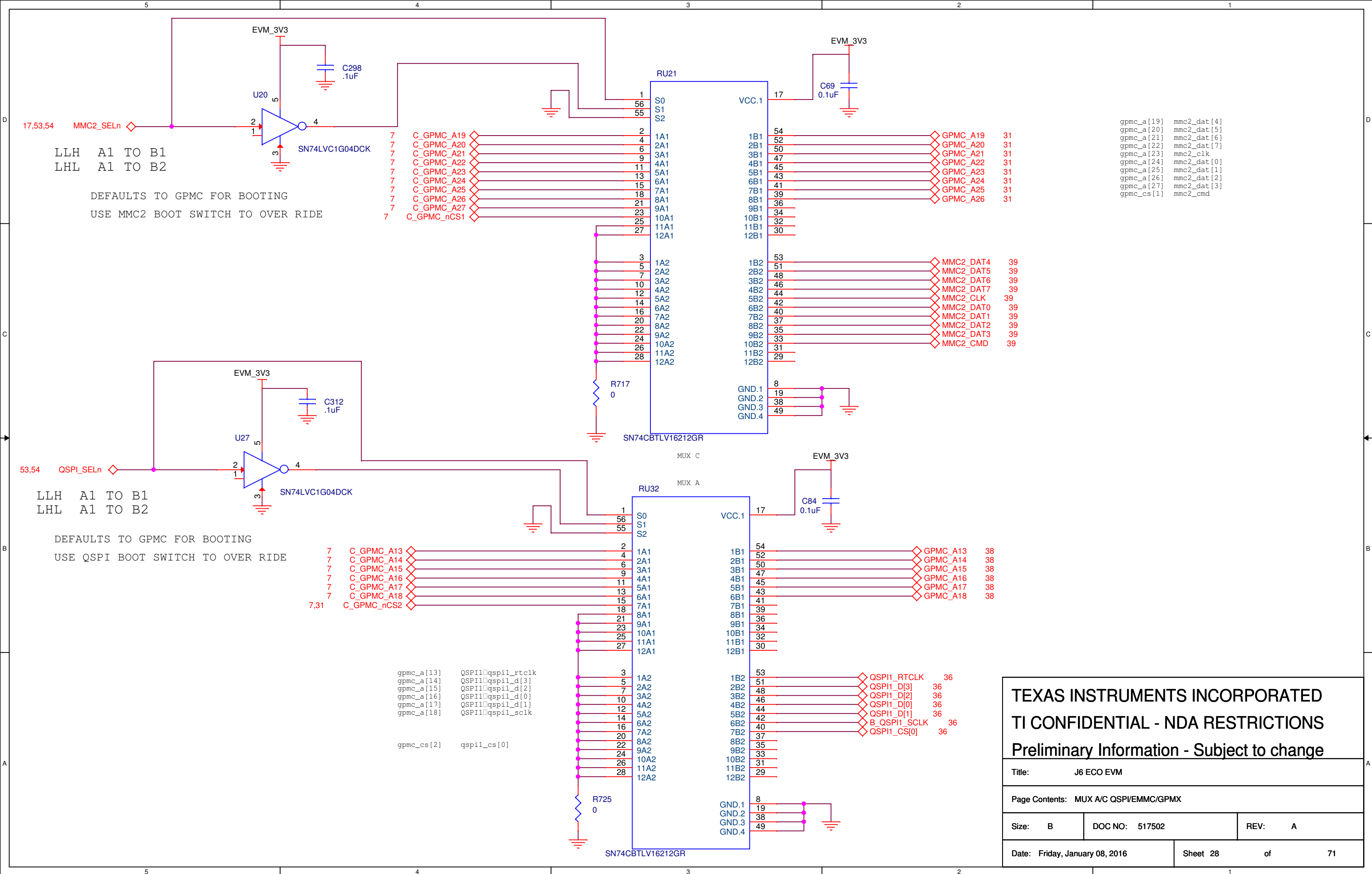
REV: C

Date: Friday, January 08, 2016

Sheet 27

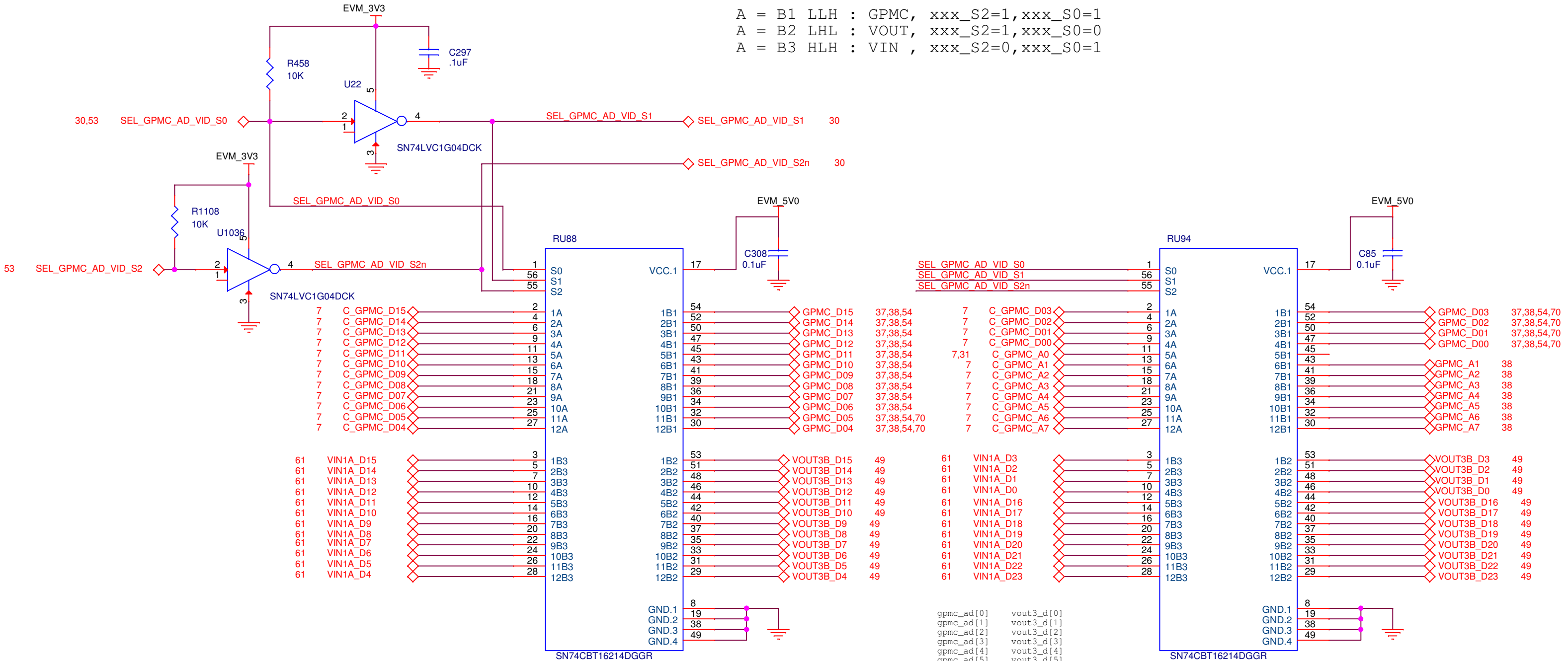
of

71



DEFAULTS TO GPMC FOR BOOTING

A = B1 LLH : GPMC, xxx_S2=1,xxx_S0=1
A = B2 LHL : VOUT, xxx_S2=1,xxx_S0=0
A = B3 HLH : VIN , xxx_S2=0,xxx_S0=1



TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

Title: J6 ECO EVM

Page Contents: MUX B VOUT3/GPMC

Size: B

DOC NO: 517502

REV: C

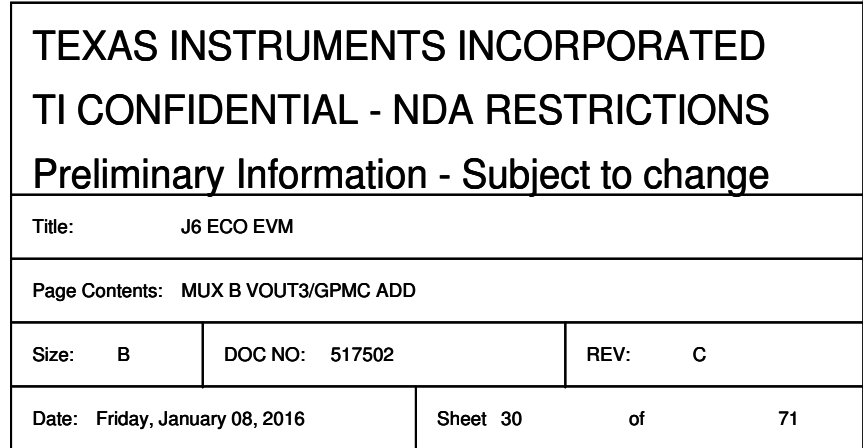
Date: Friday, January 08, 2016

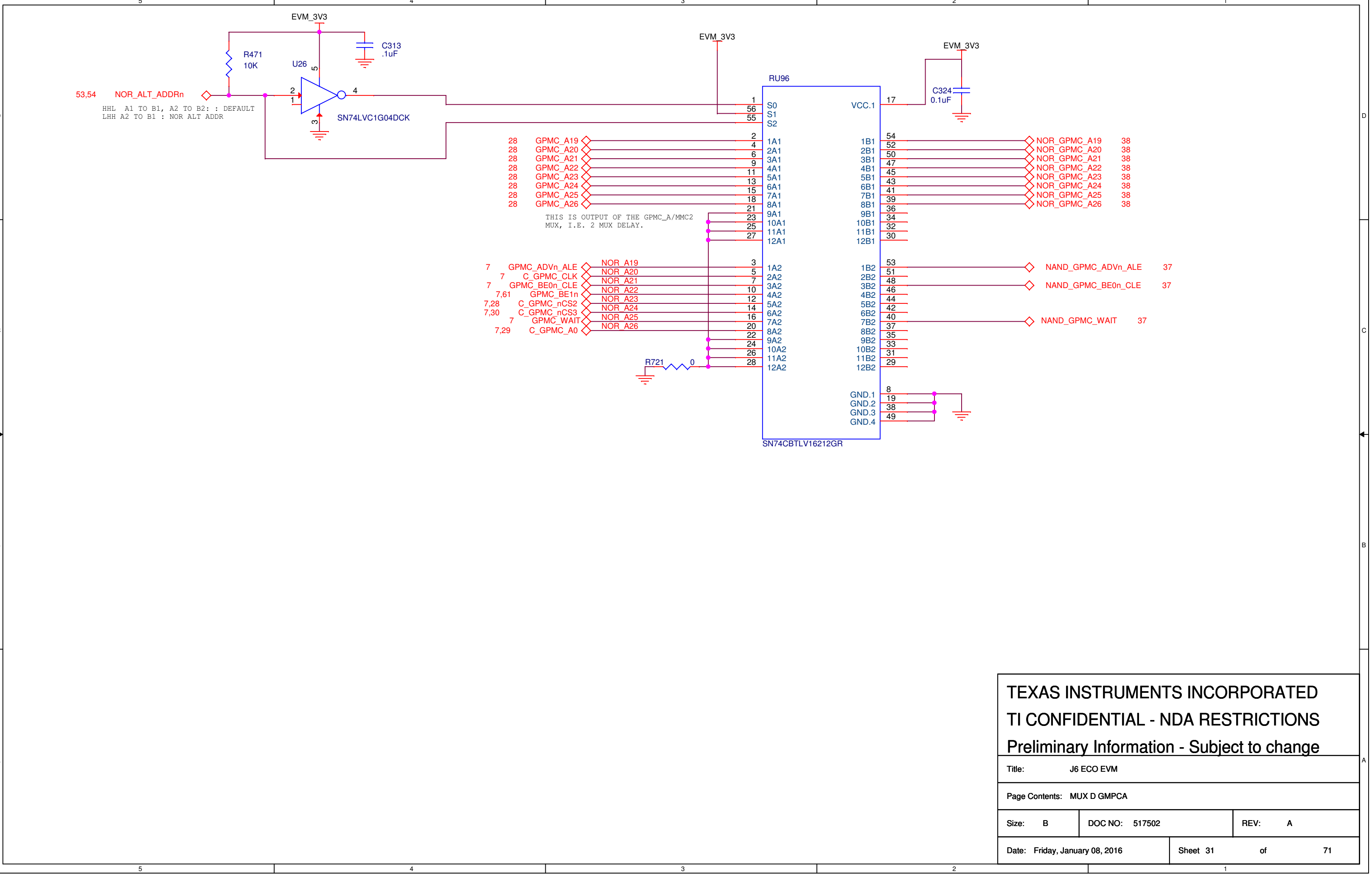
Sheet 29

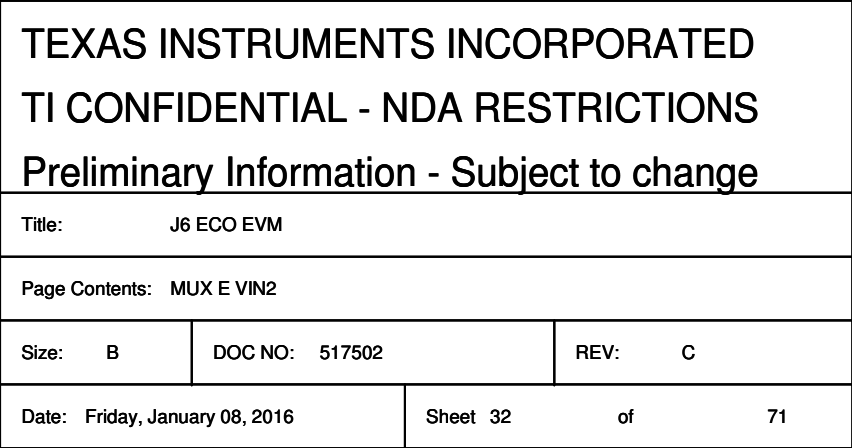
of

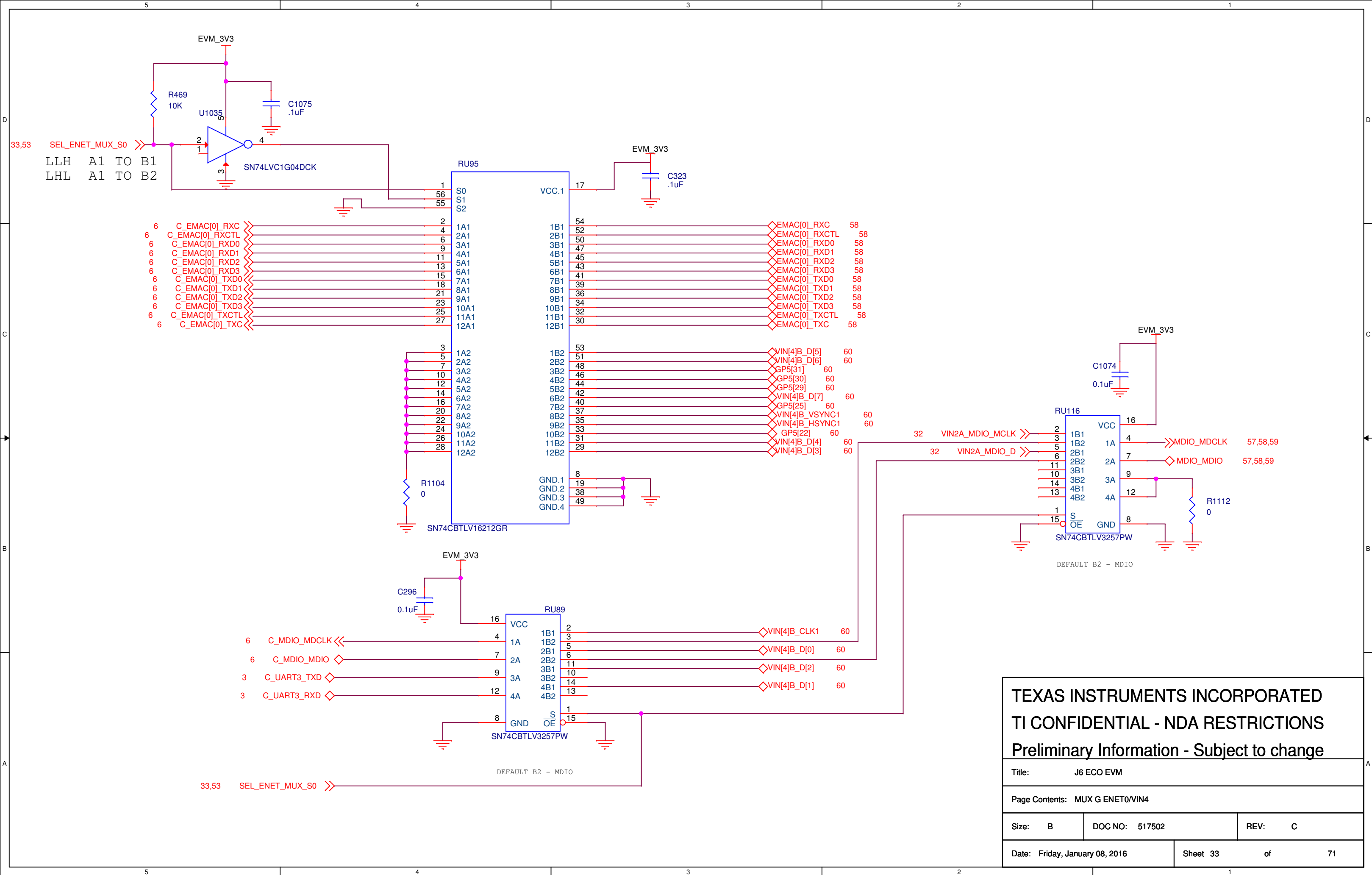
71

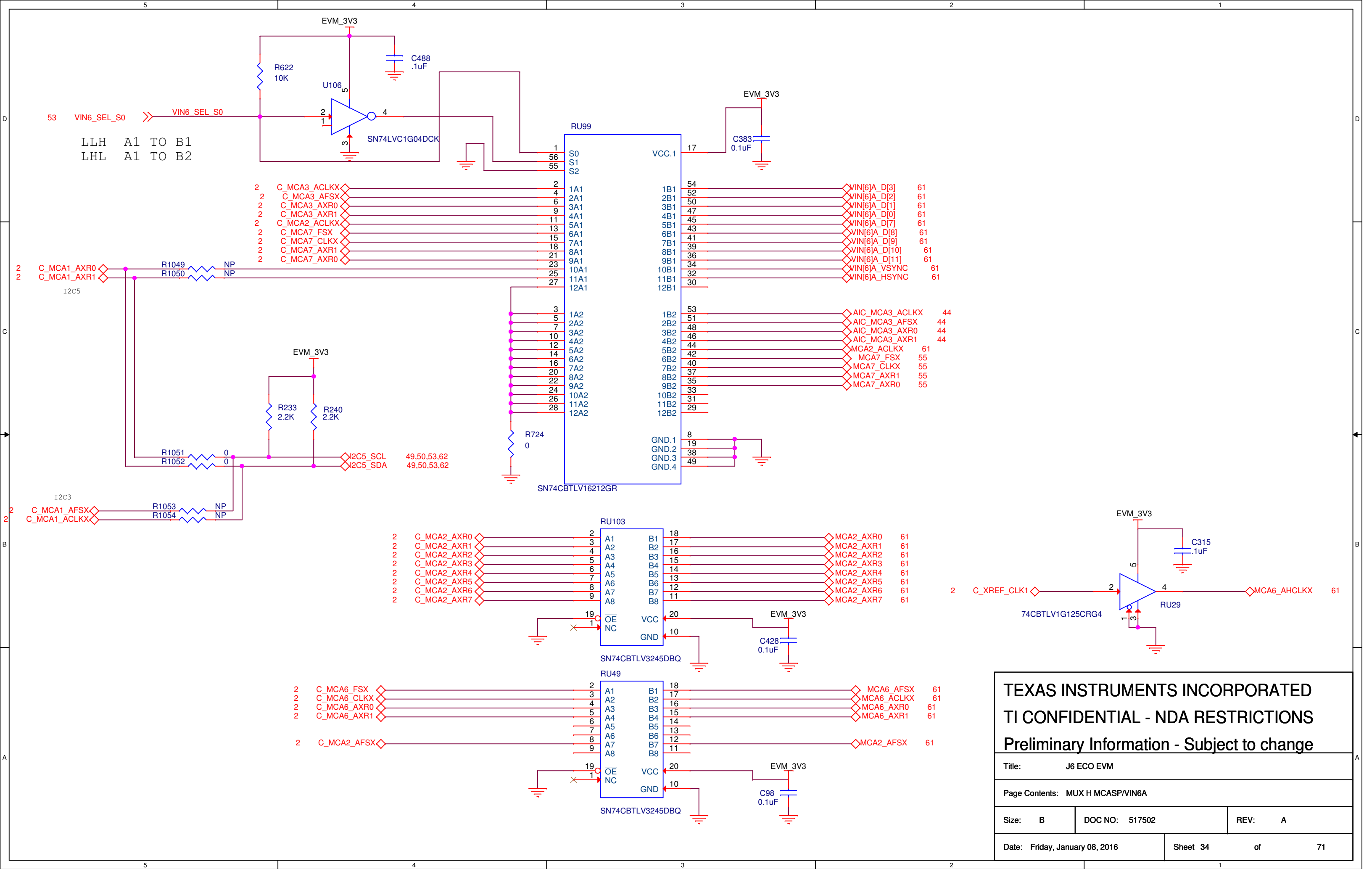
```
A = B1 LLH : GPMC, xxx_S2=1, xxx_S0=1
A = B2 LHL : VOUT, xxx_S2=1, xxx_S0=0
A = B3 HLH : VIN , xxx_S2=0, xxx_S0=1
```

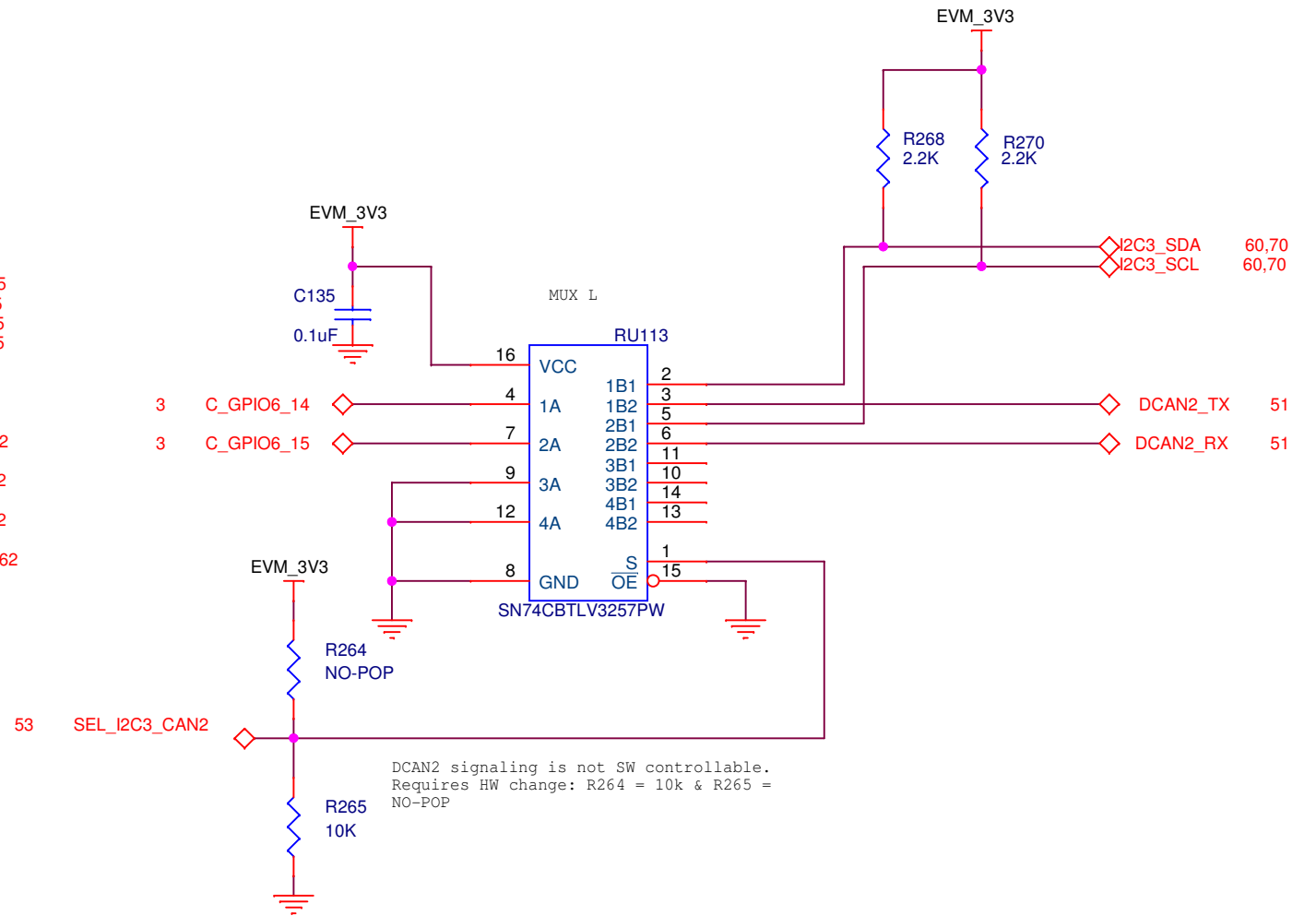
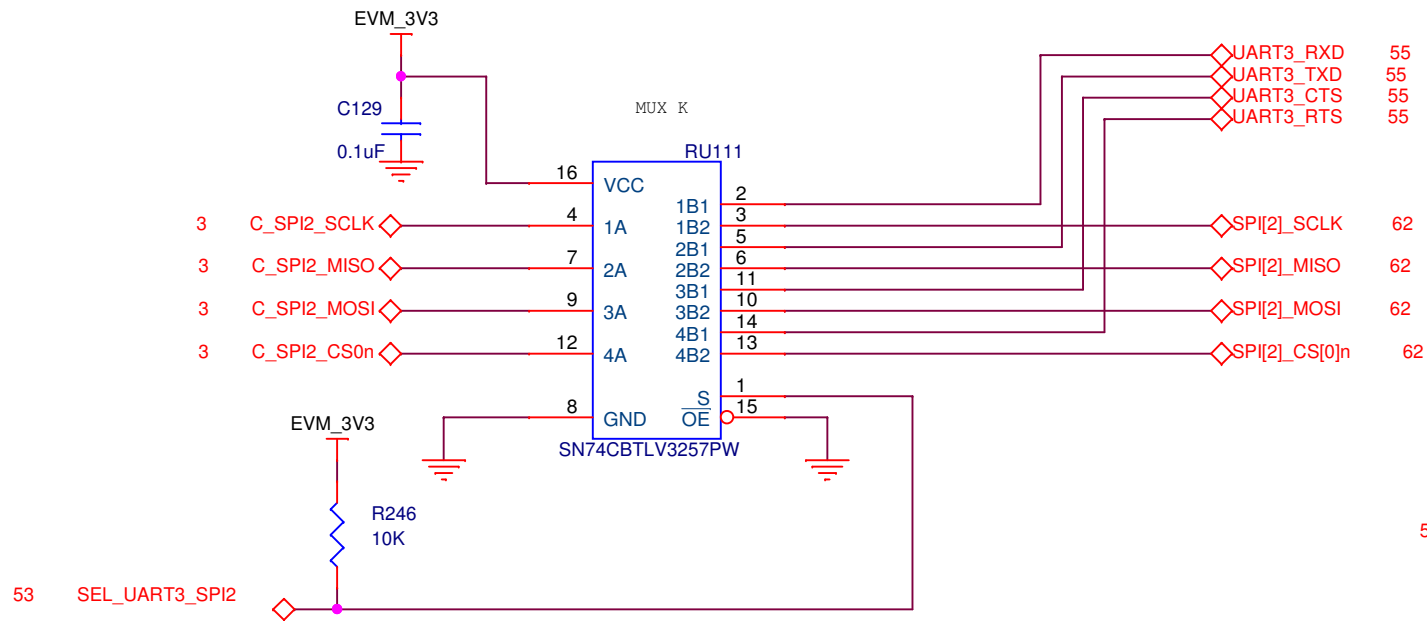










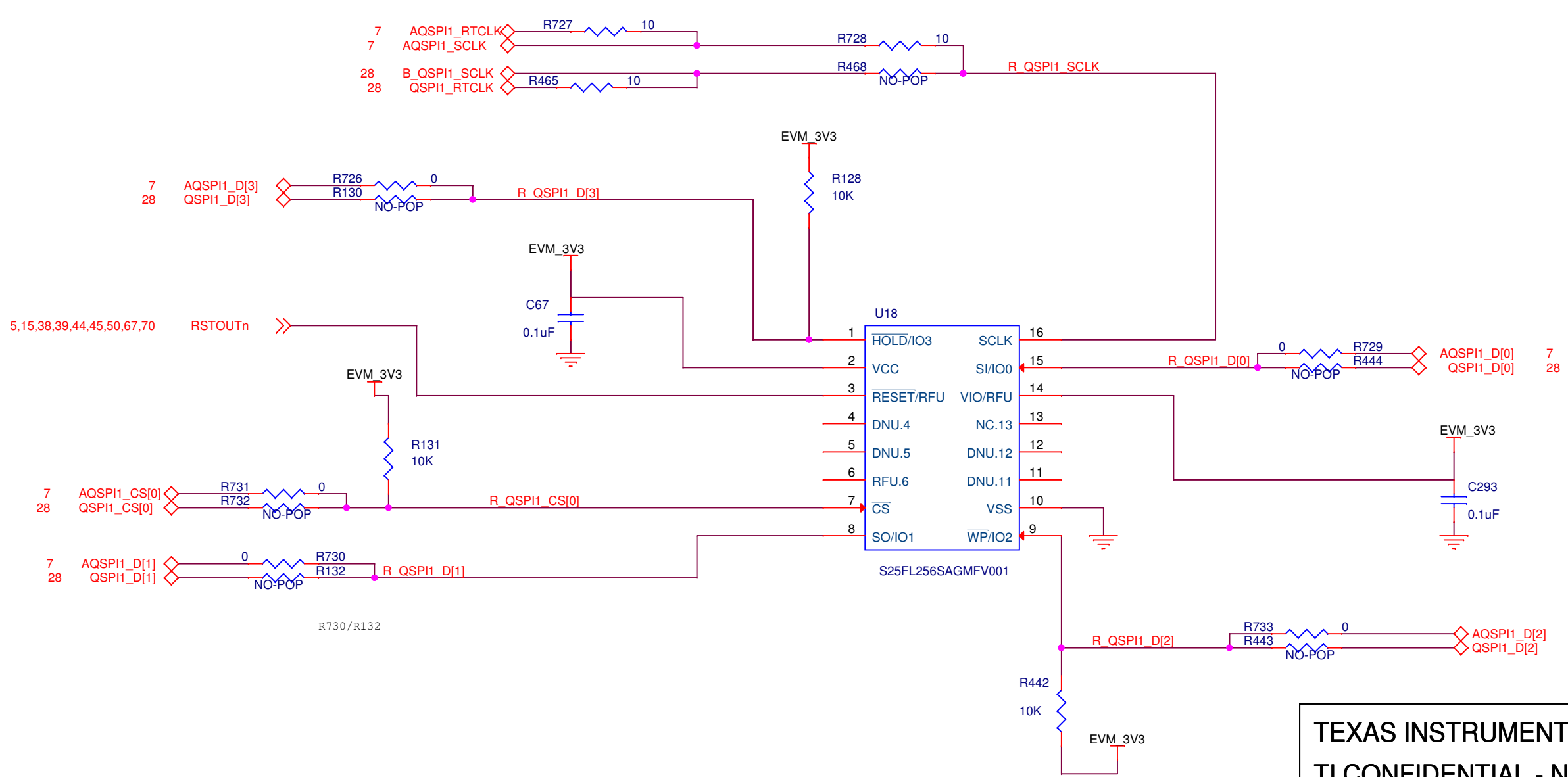


TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

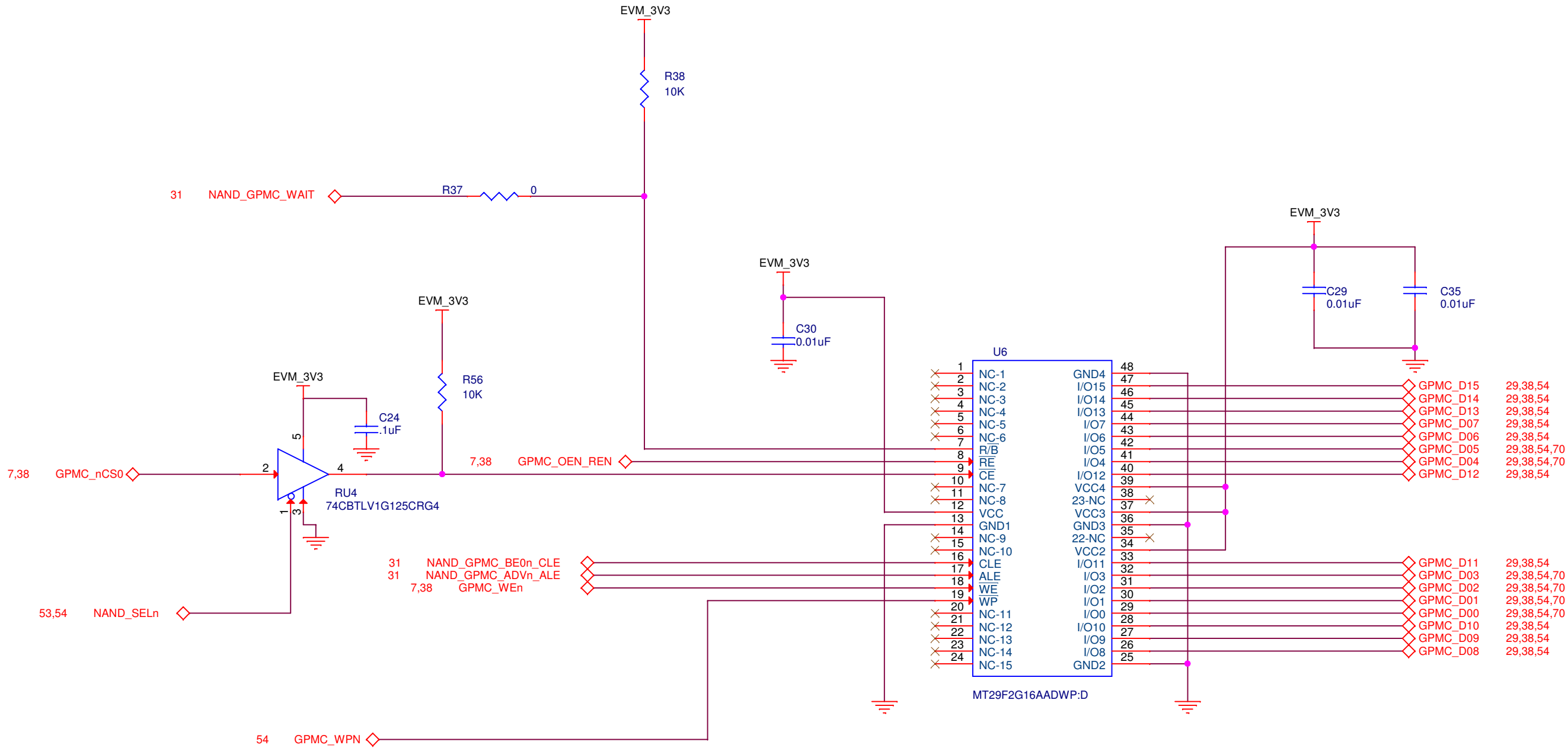
Title: J6 ECO EVM		
Page Contents: MUX J/K/L SERIAL I/O		
Size: B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016	Sheet 35	of 71

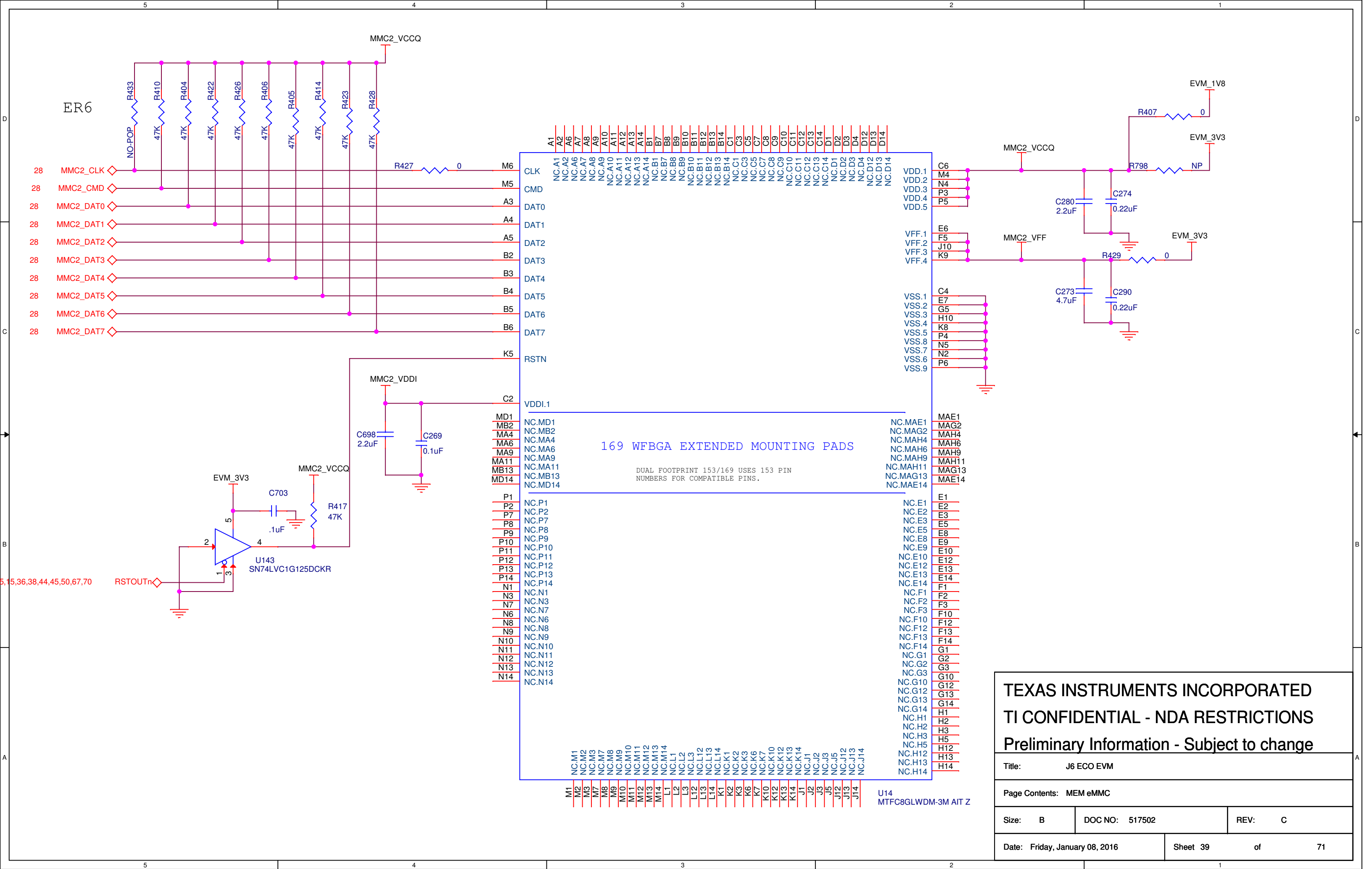
ROUTE THE FOLLOWING SIGNALS AS A GROUP
WITH A NET LENGTH LESS THEN 2.75
INCHES AND 60ps OF SKEW. PLACE THE
DUAL RESISTOR CLOSE TOGETHER NEAR THE
PIN OF THE FLASH.

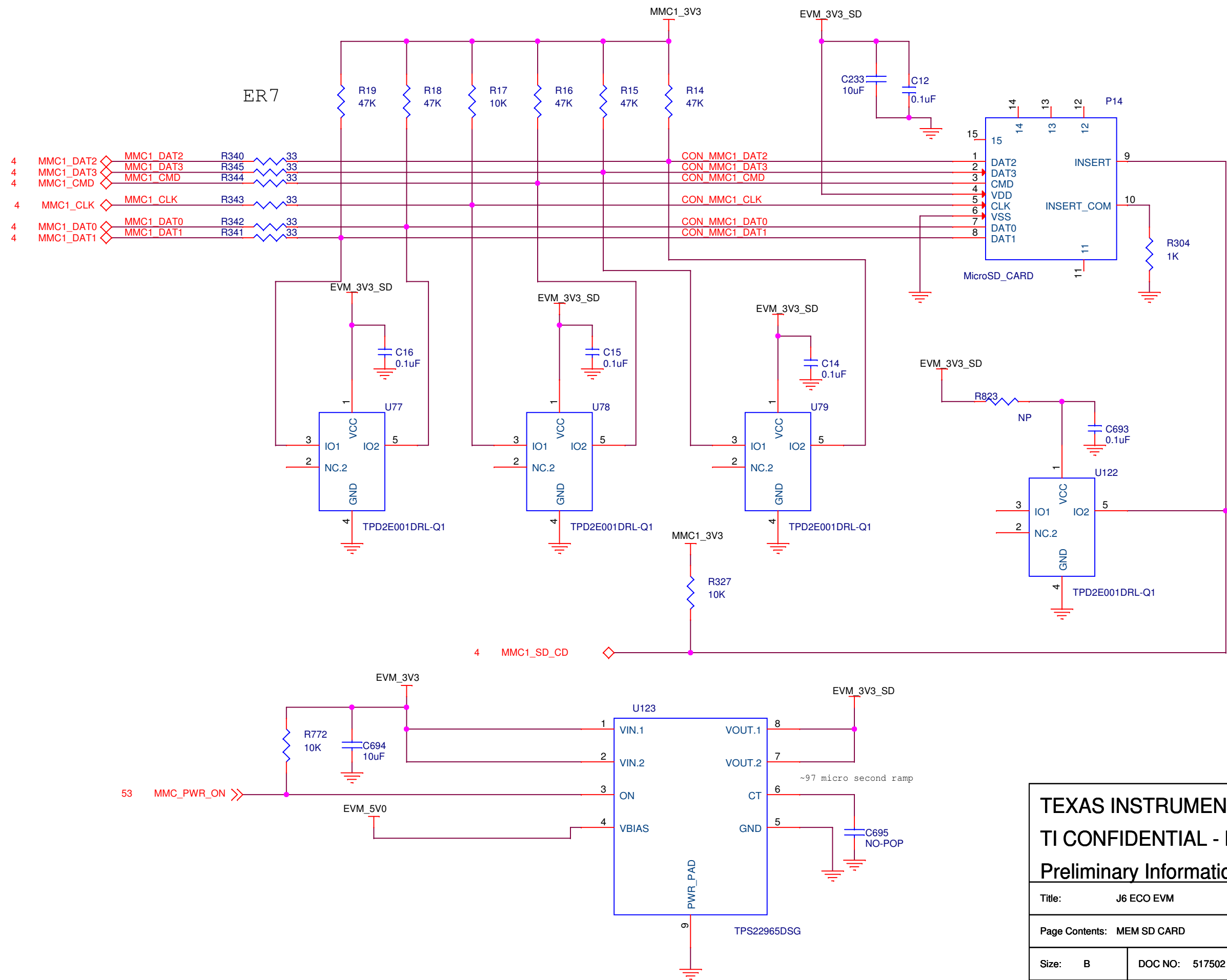
AQSPI1_SCLK
AQSPI1_RTCLK
AQSPI1_CS[0]
AQSPI1_D[0]
AQSPI1_D[1]
AQSPI1_D[2]
AQSPI1_D[3]



TEXAS INSTRUMENTS INCORPORATED		
TI CONFIDENTIAL - NDA RESTRICTIONS		
Preliminary Information - Subject to change		
Title: J6 ECO EVM		
Page Contents: MEM SPI FLASH		
Size: B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016	Sheet 36	of 71

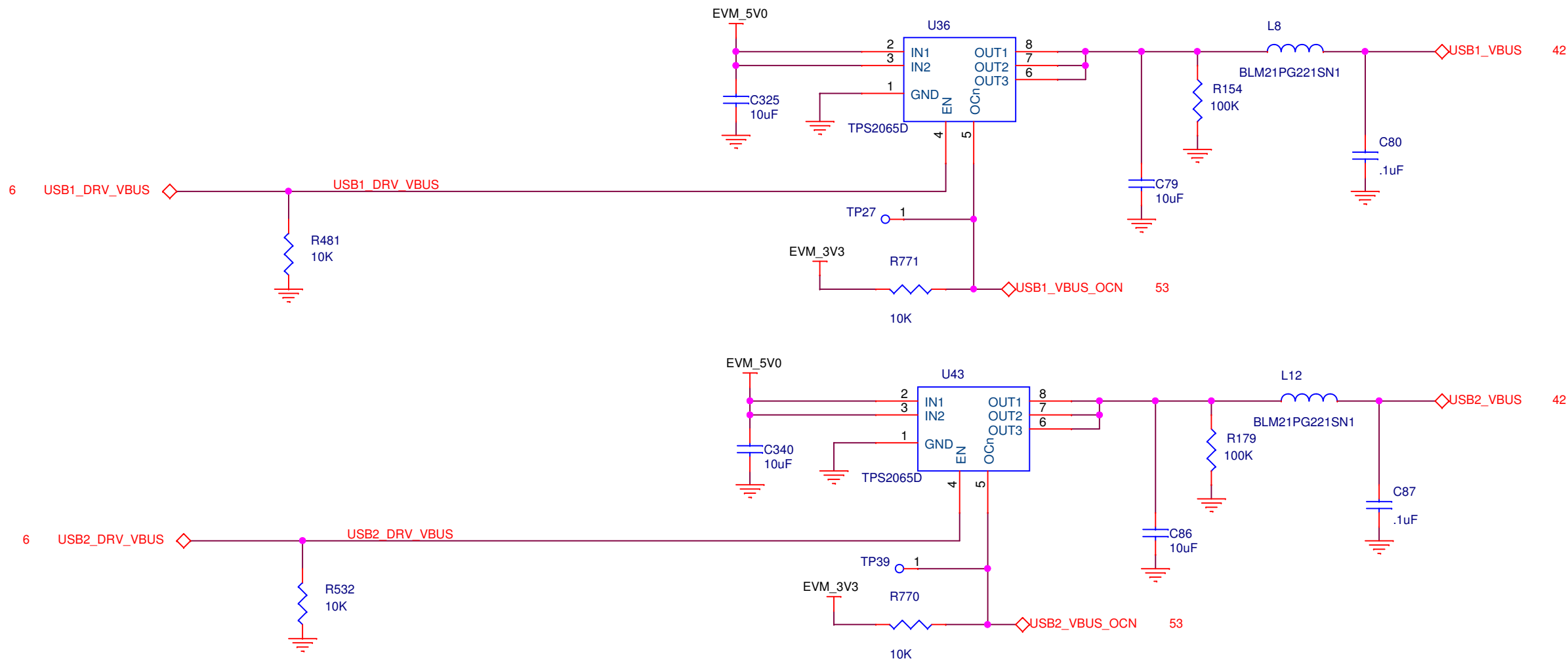






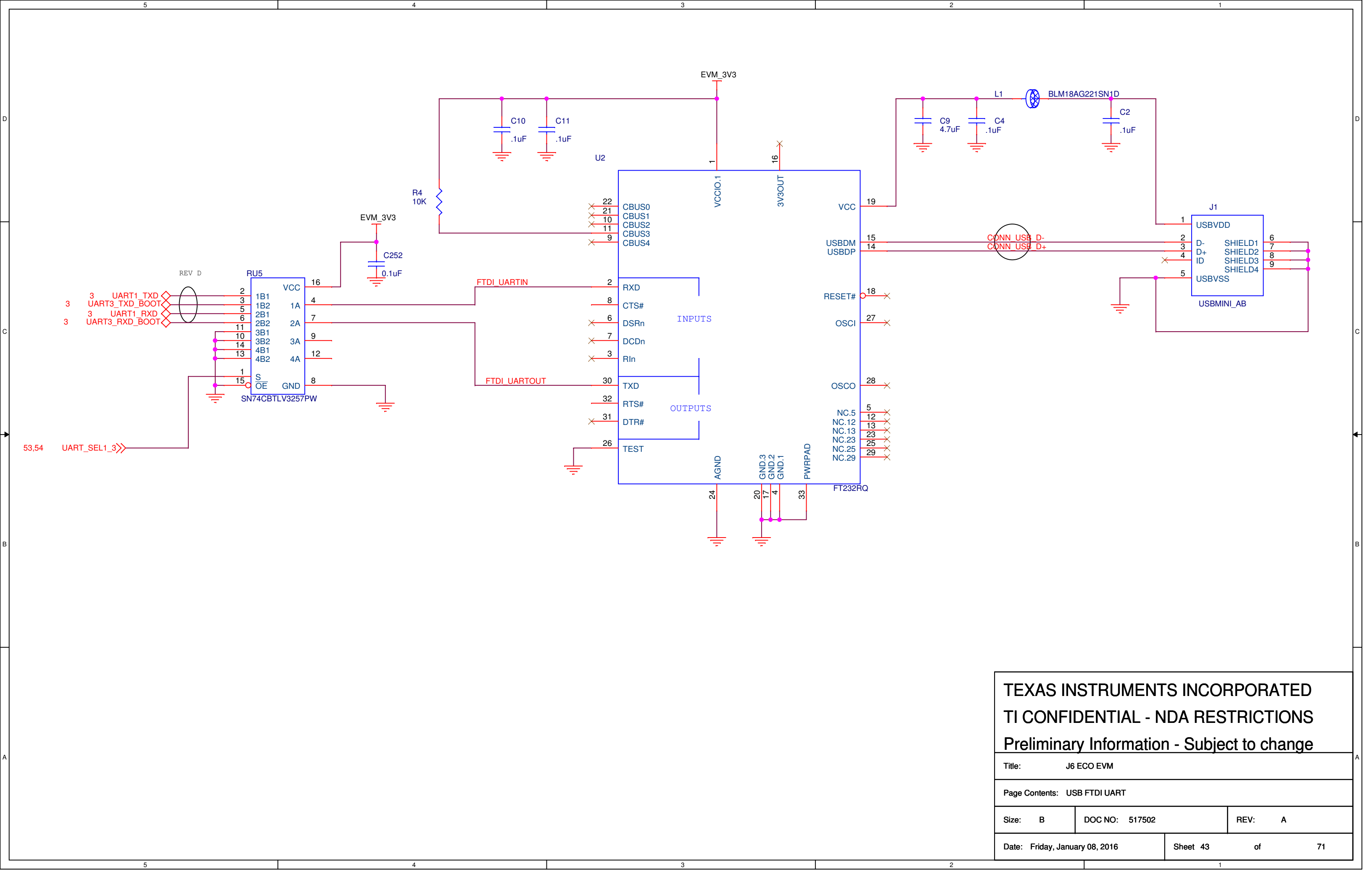
TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

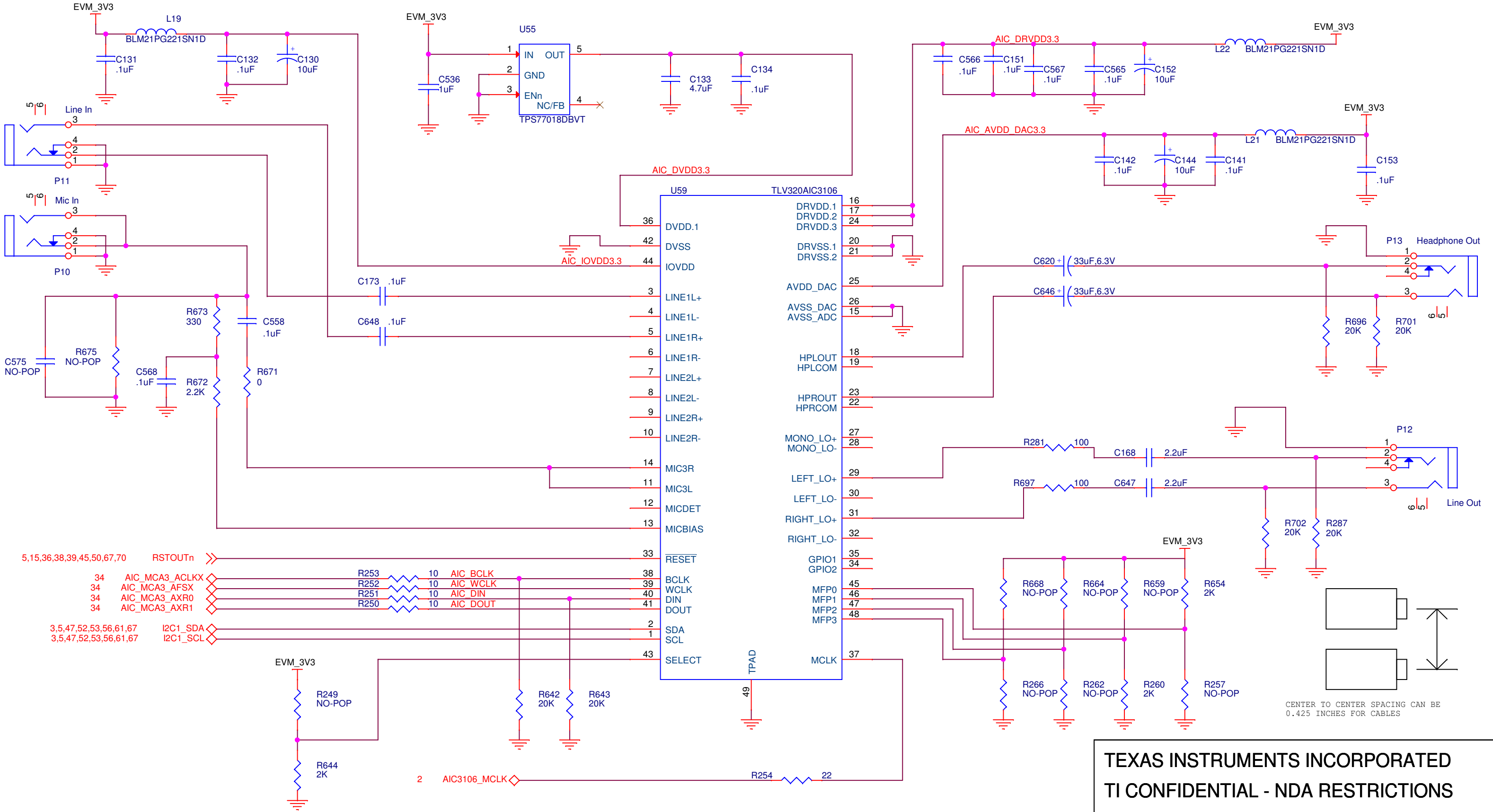
Title: J6 ECO EVM		
Page Contents: MEM SD CARD		
Size: B	DOC NO: 517502	REV: C
Date: Friday, January 08, 2016	Sheet 40	of 71

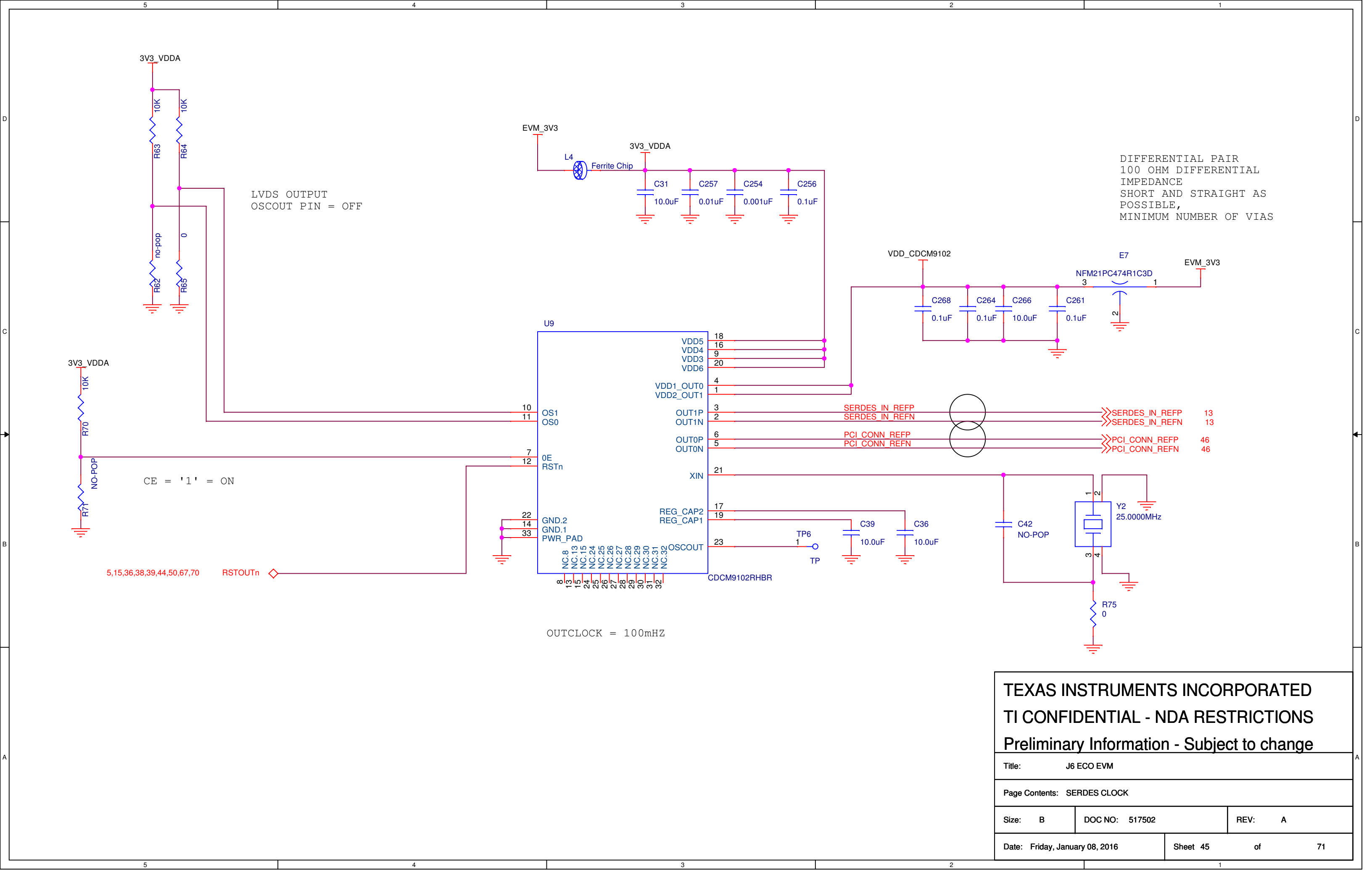


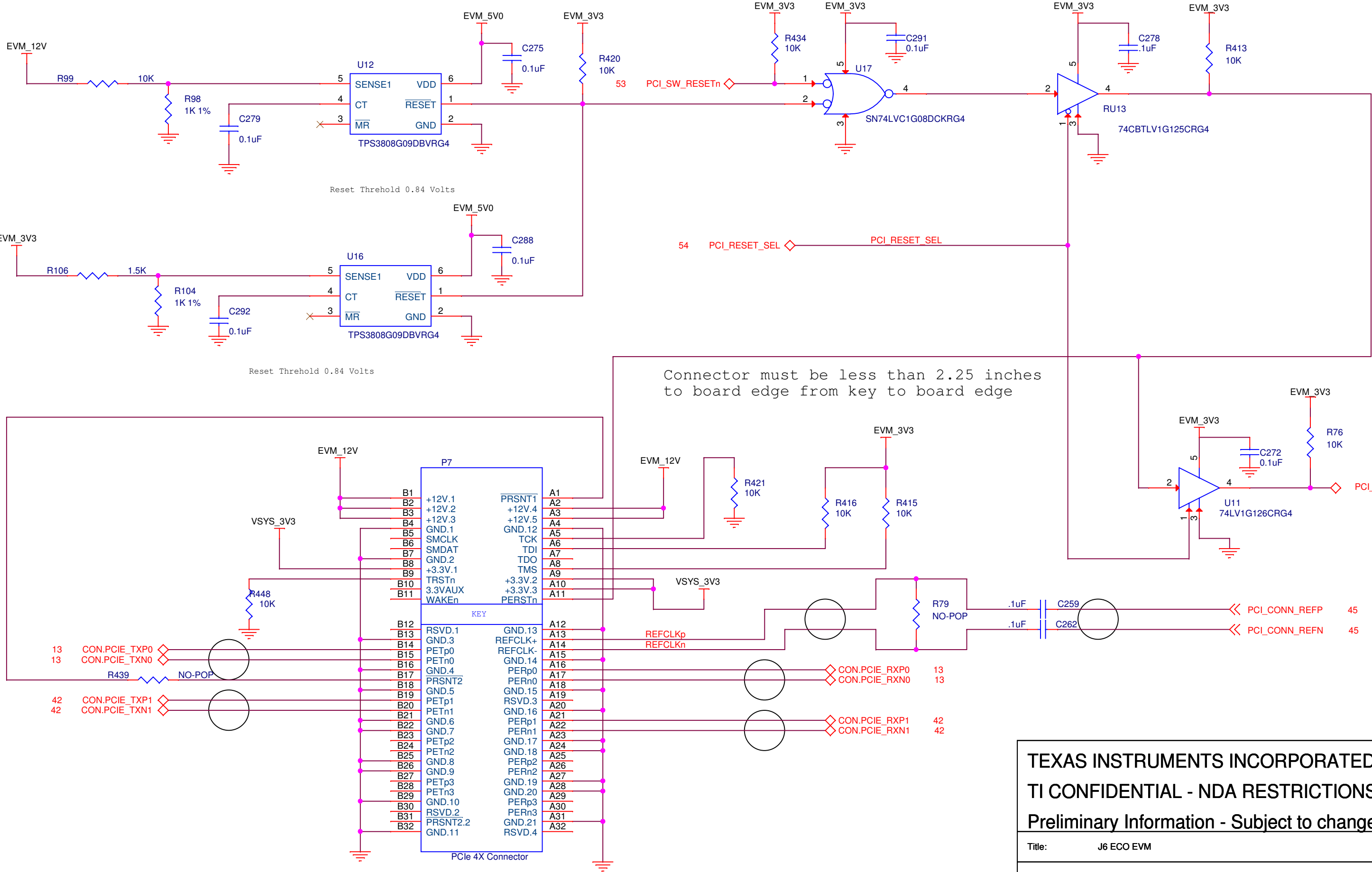
TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

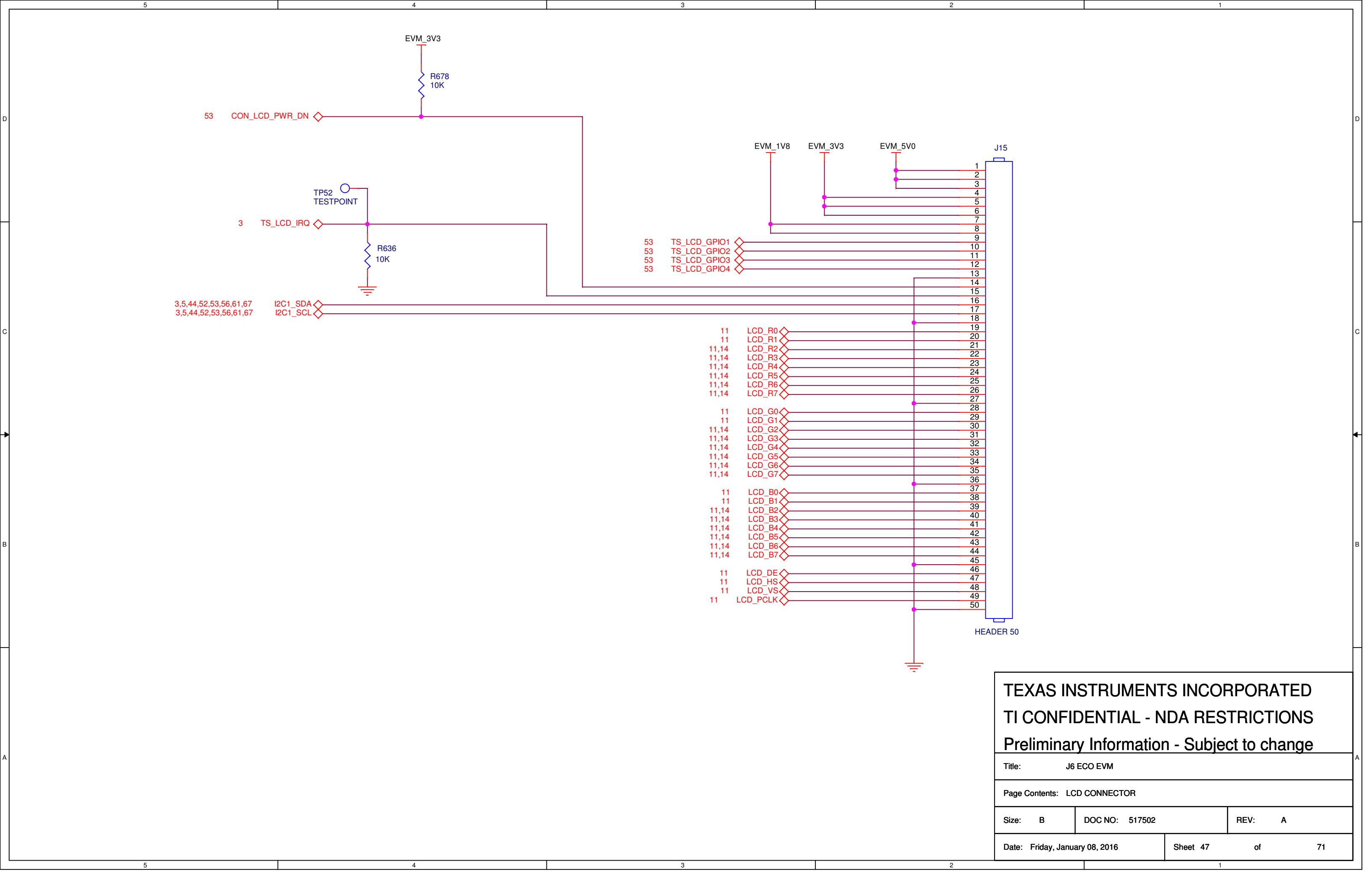
Title: J6 ECO EVM		
Page Contents: USB VBUS		
Size: B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016	Sheet 41	of 71









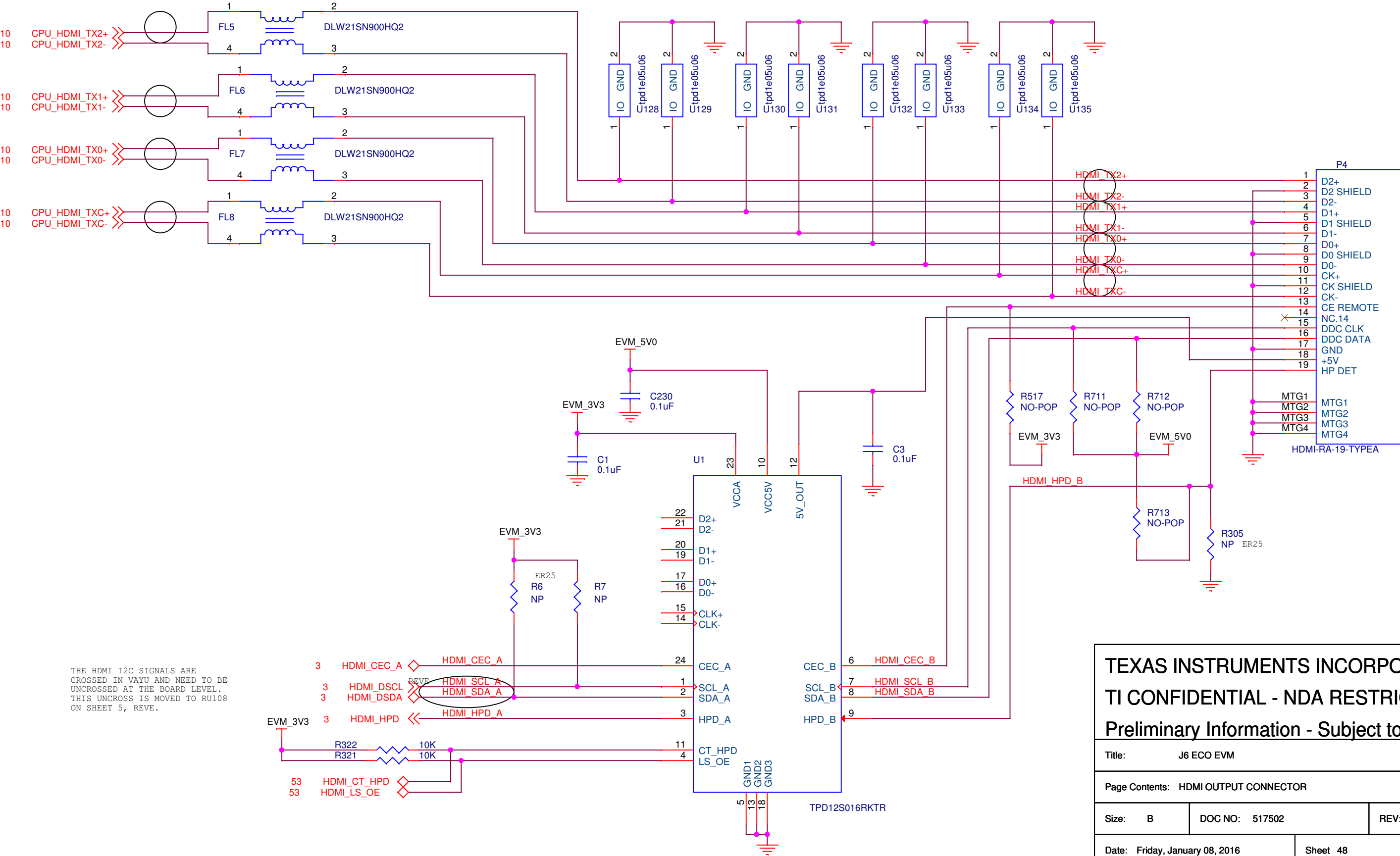


TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

Title: J6 ECO EVM		
Page Contents: LCD CONNECTOR		
Size: B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016	Sheet 47	of 71

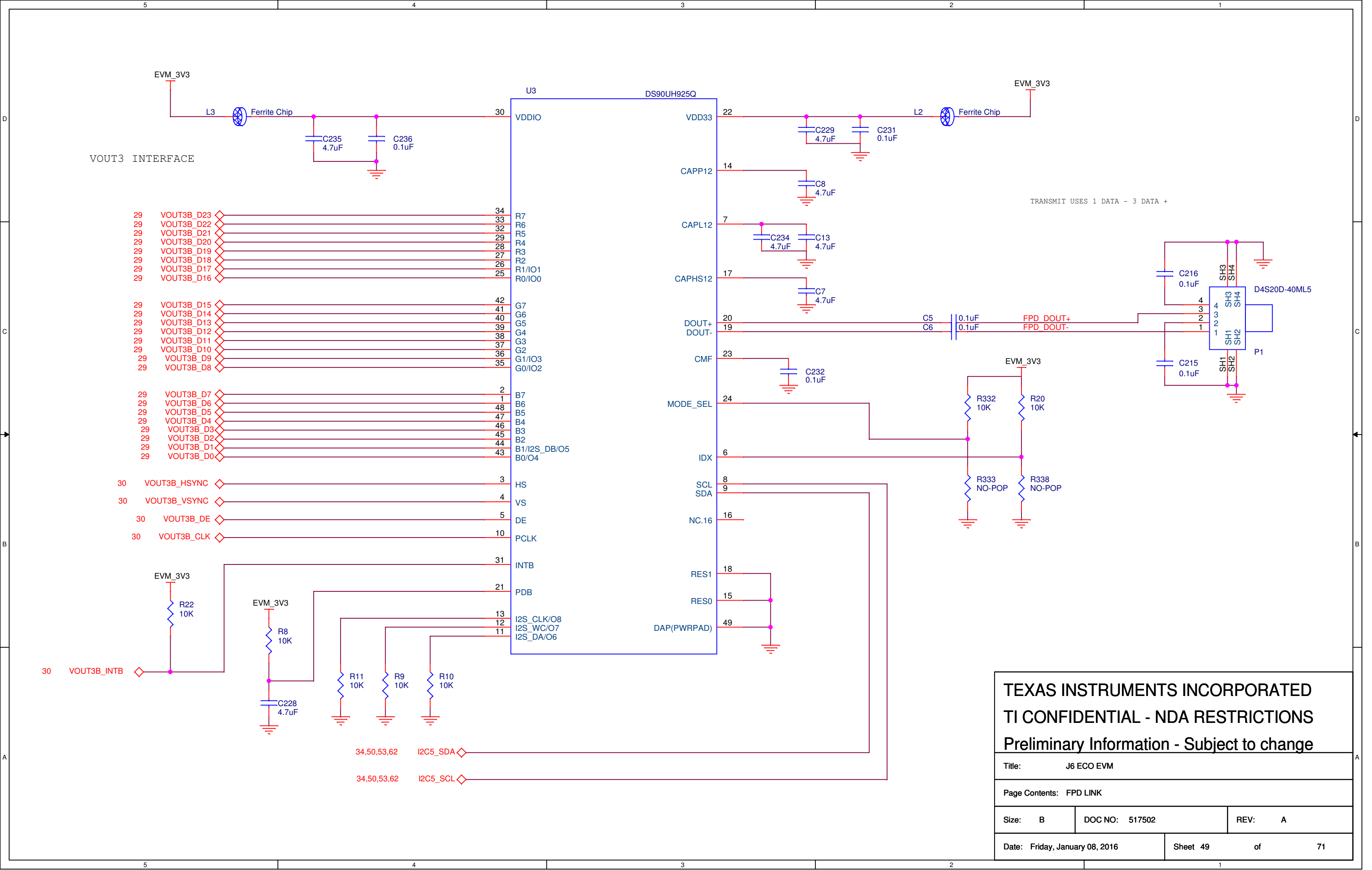
DIFFERENTIAL PAIR
100 OHM DIFFERENTIAL
IMPEDANCE
SHORT AND STRAIGHT AS
POSSIBLE,
MINIMUM NUMBER OF VIAS

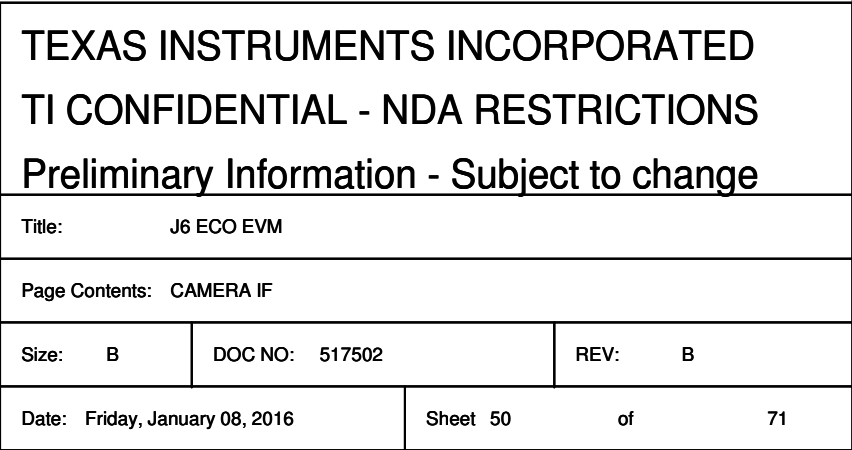
DIFFERENTIAL PAIR
100 OHM DIFFERENTIAL
IMPEDANCE
SHORT AND STRAIGHT AS
POSSIBLE,
MINIMUM NUMBER OF VIAS

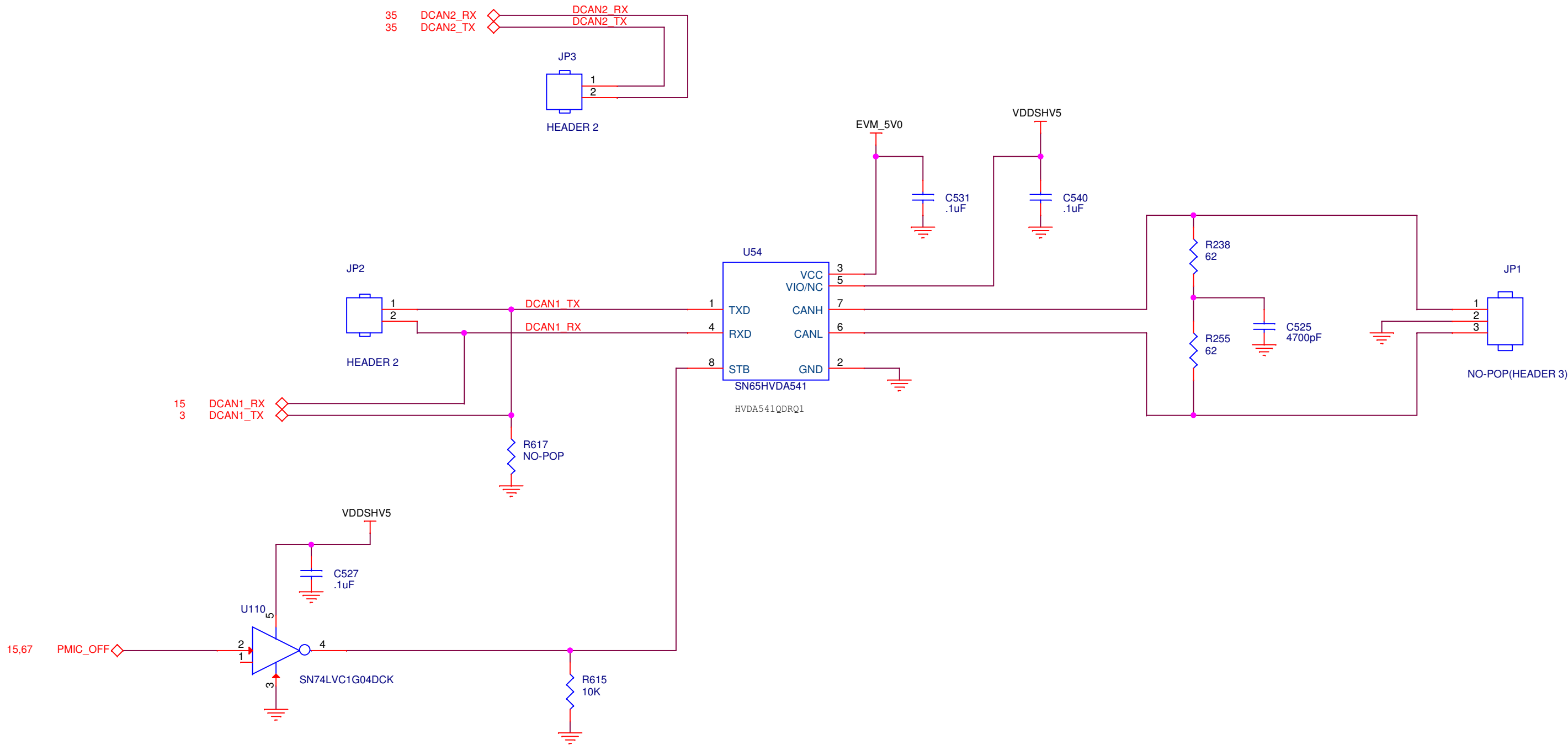


THE HDMI I2C SIGNALS ARE
CROSSED IN VAYU AND NEED TO BE
UNCROSSED AT THE BOARD LEVEL.
THIS UNCROSS IS MOVED TO RU108
ON SHEET 5, REVE.

TEXAS INSTRUMENTS INCORPORATED		
TI CONFIDENTIAL - NDA RESTRICTIONS		
Preliminary Information - Subject to change		
Title: J6 ECO EVM		
Page Contents: HDMI OUTPUT CONNECTOR		
Size: B	DOC NO: 517502	REV: C
Date: Friday, January 08, 2016	Sheet 48	of 71

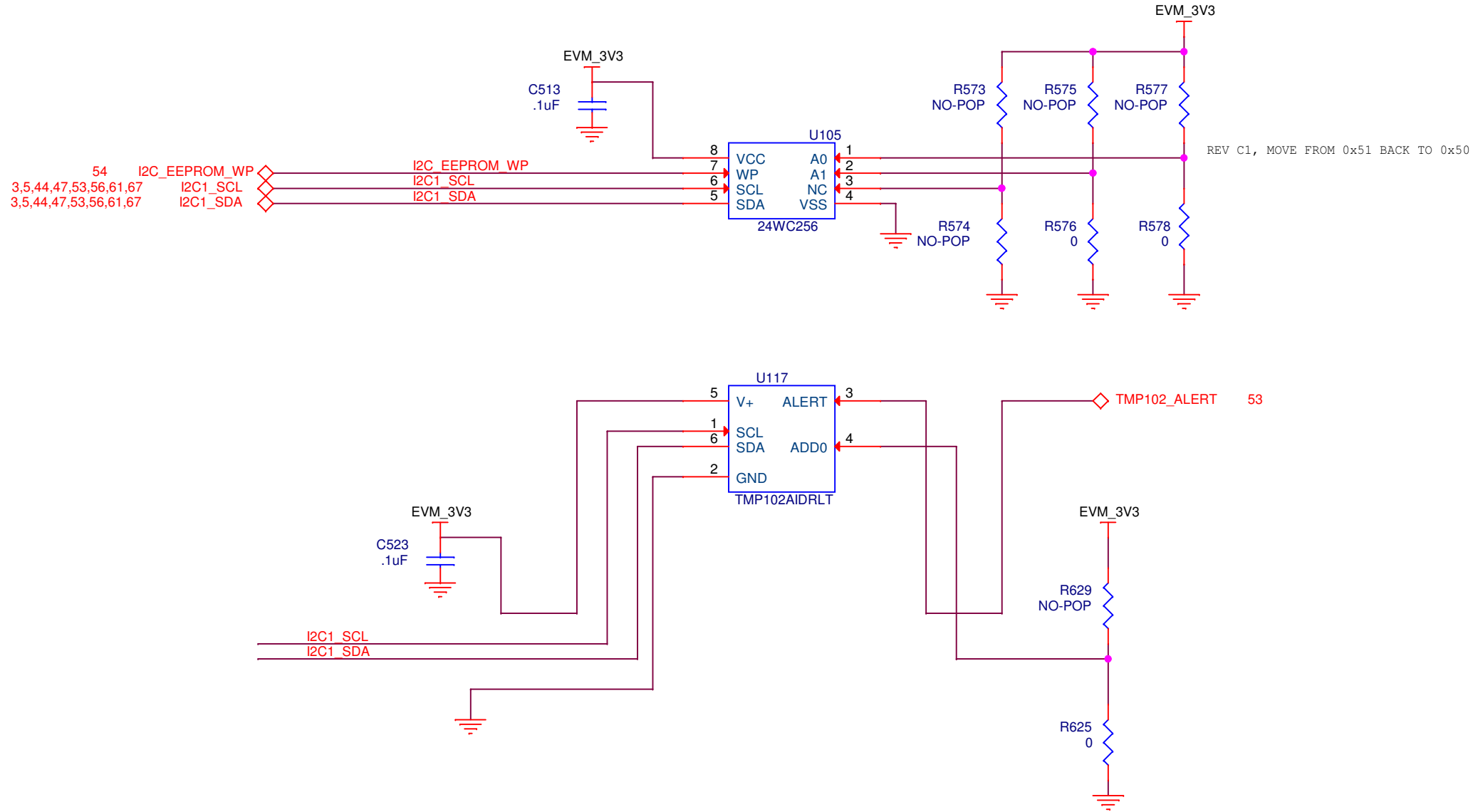






TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

Title: J6 ECO EVM		
Page Contents: CAN CONNECTORS		
Size: B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016	Sheet 51	of 71



PLACE TEMP SENSOR CLOSE TO PROCESSOR

TEXAS INSTRUMENTS INCORPORATED

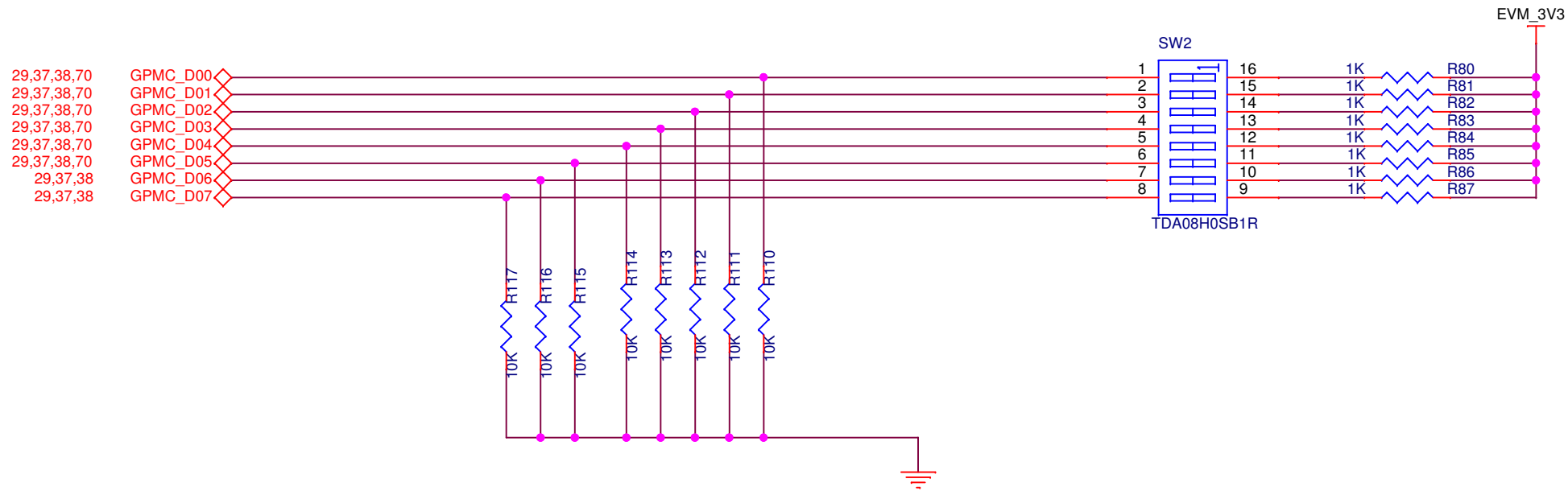
TI CONFIDENTIAL - NDA RESTRICTIONS

Preliminary Information - Subject to change

Title: J6 ECO EVM

Page Contents: I2C EEPROM/TEMP SENSOR

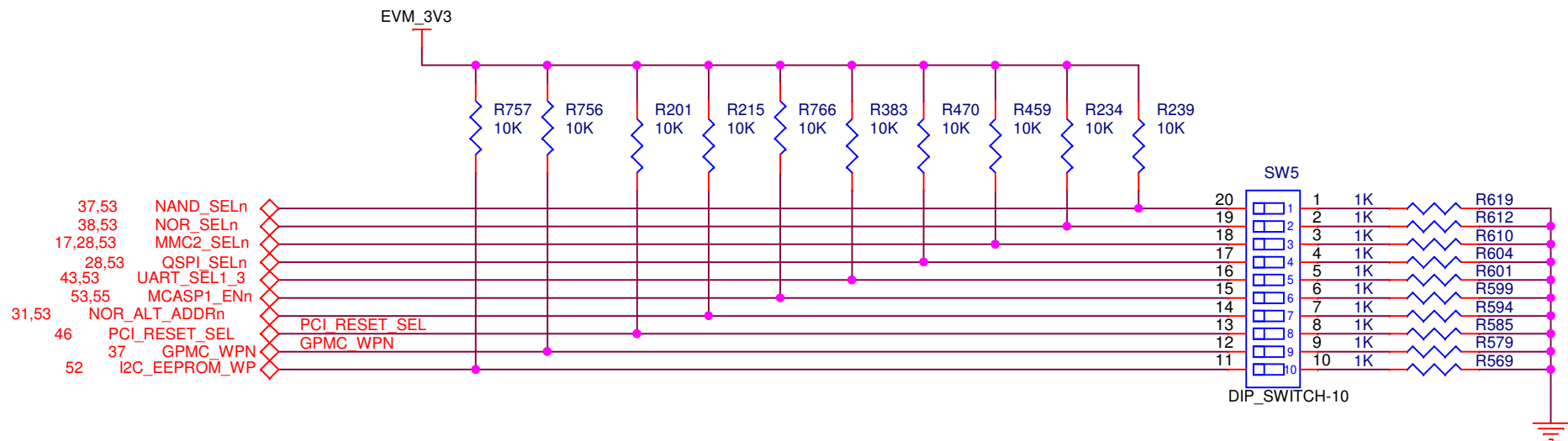
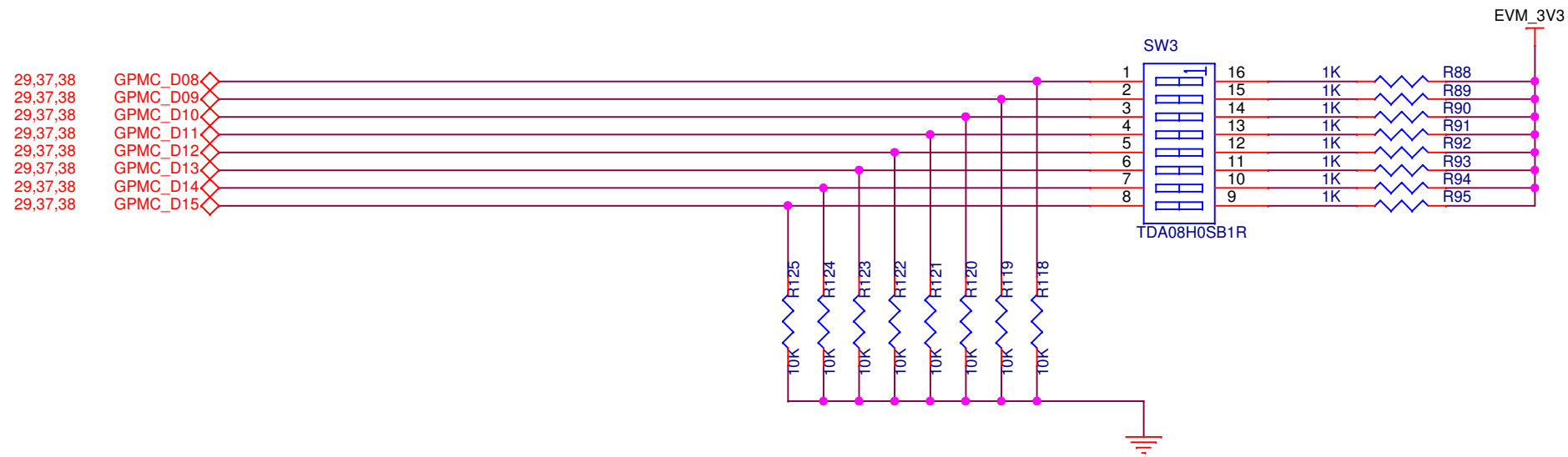
Size: B	DOC NO: 517502	REV: C1
Date: Wednesday, March 02, 2016	Sheet 52	of 71



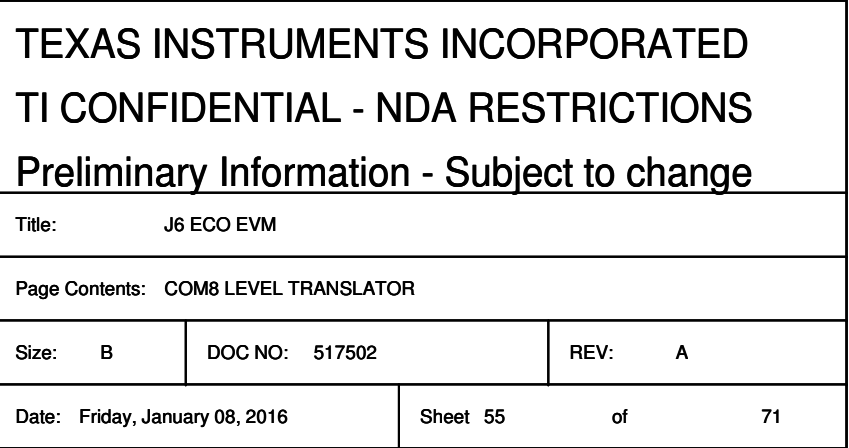
MMC2 BOOT PULL UP ON PAGE 23 SET TO LOGIC 0 TO
ENABLE MMC2 MUX FOR BOOTING SET TO 1 FOR GPMC
BOOTING

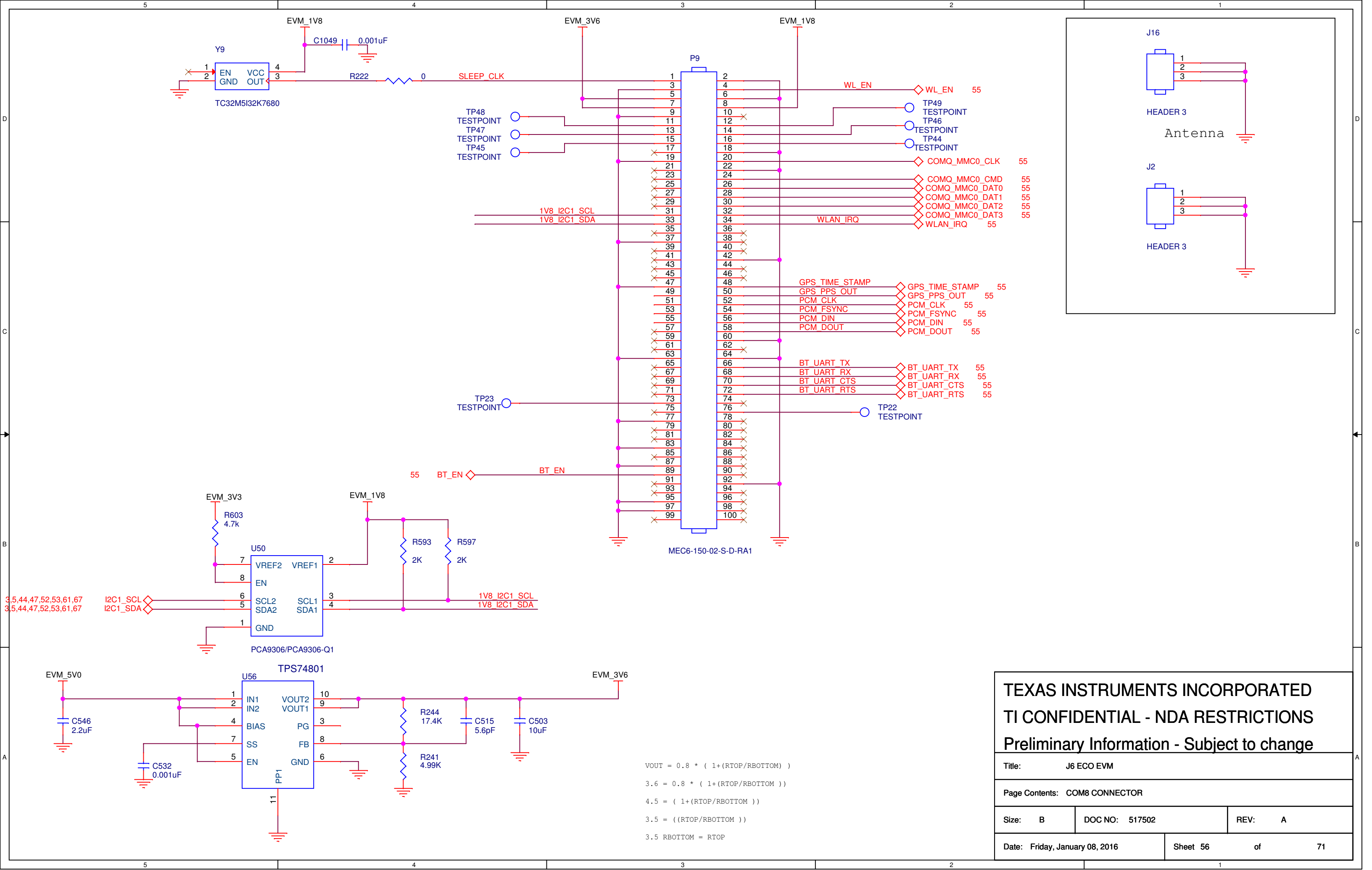
QSPI BOOT PULL UP ON PAGE 23 SET TO LOGIC 0 TO
ENABLE QSPI MUX FOR BOOTING SET TO 1 FOR GPMC
BOOTING

UART SEL1_3 PULL UP ON PAGE 58 SELECTS UART3
MUX INPUT TO BE USART PORT AT MINI_USB A(J1)
DURING BOOTING.
MCASP1_ENn MUST BE SET TO LOGIC 1 TO DISABLE
COM8. ALSO OTHER MUX NEEDS TO BE IN DEFAULT
CONFIGURATION



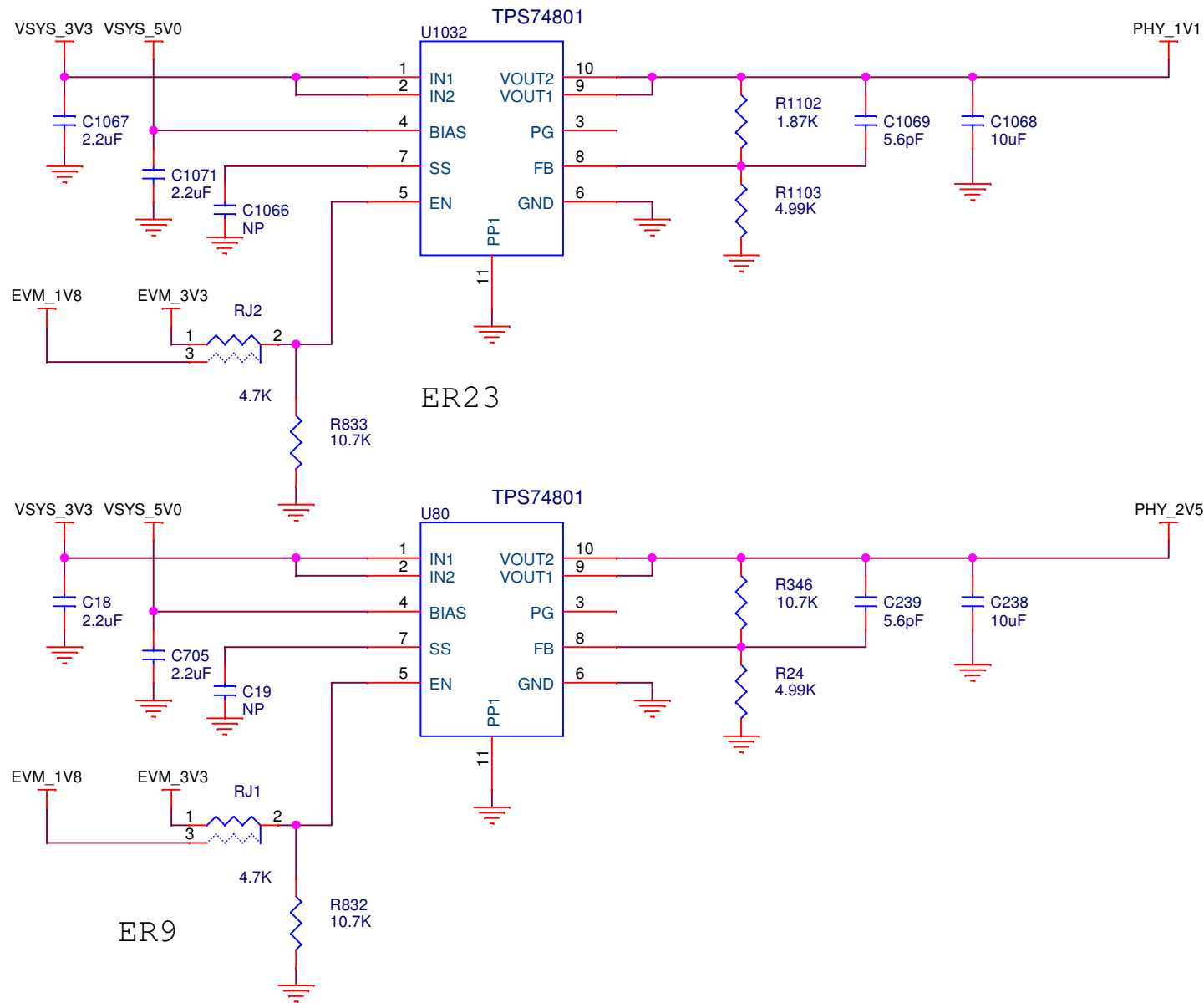
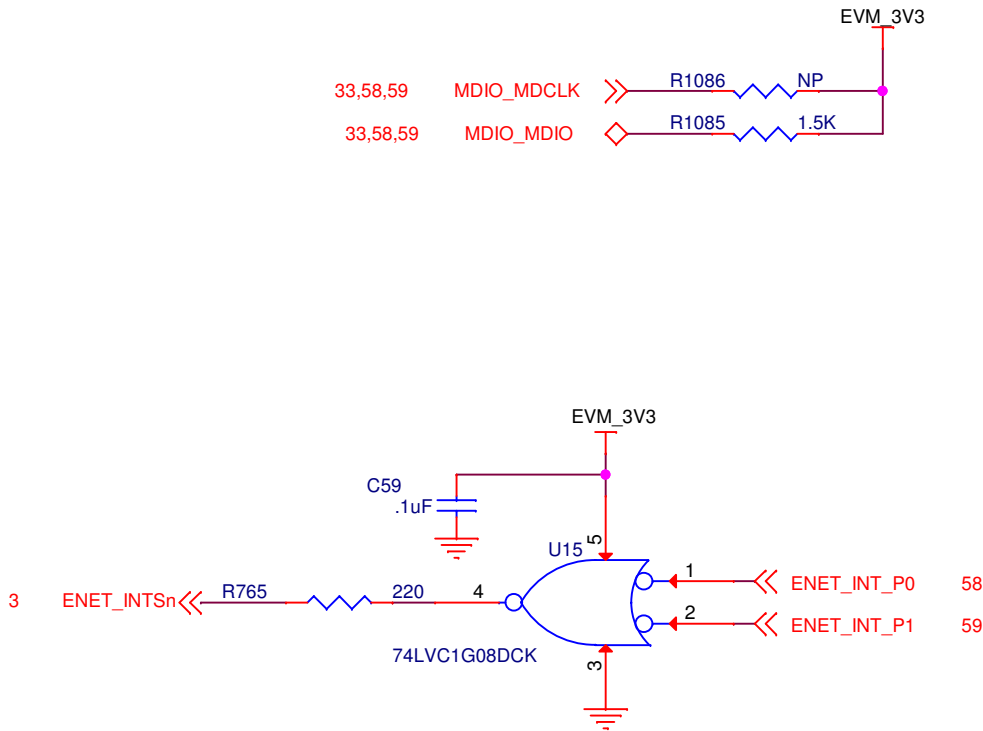
TEXAS INSTRUMENTS INCORPORATED			
TI CONFIDENTIAL - NDA RESTRICTIONS			
Preliminary Information - Subject to change			
Title:		J6 ECO EVM	
Page Contents: BOOTSWITCHES			
Size:	B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016		Sheet 54 of 71	





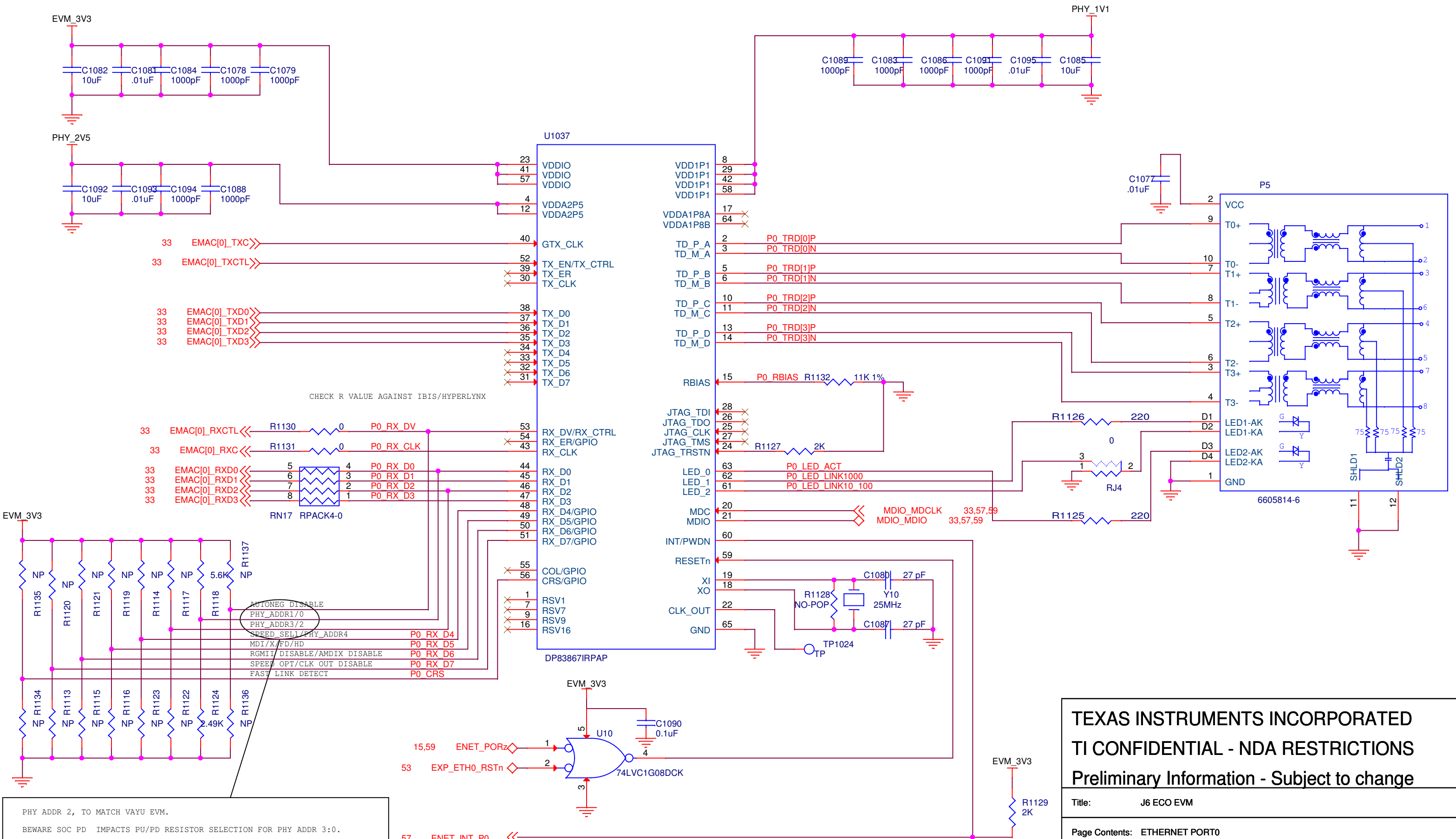
$$VOUT = 0.8 * (1 + (RTOP/RBOTTOM))$$
$$3.6 = 0.8 * (1 + (RTOP/RBOTTOM))$$
$$4.5 = (1 + (RTOP/RBOTTOM))$$
$$3.5 = ((RTOP/RBOTTOM))$$
$$3.5 RBOTTOM = RTOP$$

TEXAS INSTRUMENTS INCORPORATED			
TI CONFIDENTIAL - NDA RESTRICTIONS			
Preliminary Information - Subject to change			
Title: J6 ECO EVM			
Page Contents: COM8 CONNECTOR			
Size: B	DOC NO: 517502		REV: A
Date: Friday, January 08, 2016		Sheet 56	of 71



$$VOUT = 0.8 * (1 + (RTOP/RBOTTOM))$$
$$2.5 = 0.8 * (1 + (RTOP/RBOTTOM))$$
$$2.5 = 0.8 * (1 + (RTOP/4.99K))$$
$$3.125 = (1 + (RTOP/4.99K))$$
$$2.125 = (RTOP/4.99K)$$
$$10.6K = RTOP$$

TEXAS INSTRUMENTS INCORPORATED		
TI CONFIDENTIAL - NDA RESTRICTIONS		
Preliminary Information - Subject to change		
Title: J6 ECO EVM		
Page Contents: ETHERNET POWER AND MISC		
Size: B	DOC NO: 517502	REV: C
Date: Friday, January 08, 2016	Sheet 57	of 71

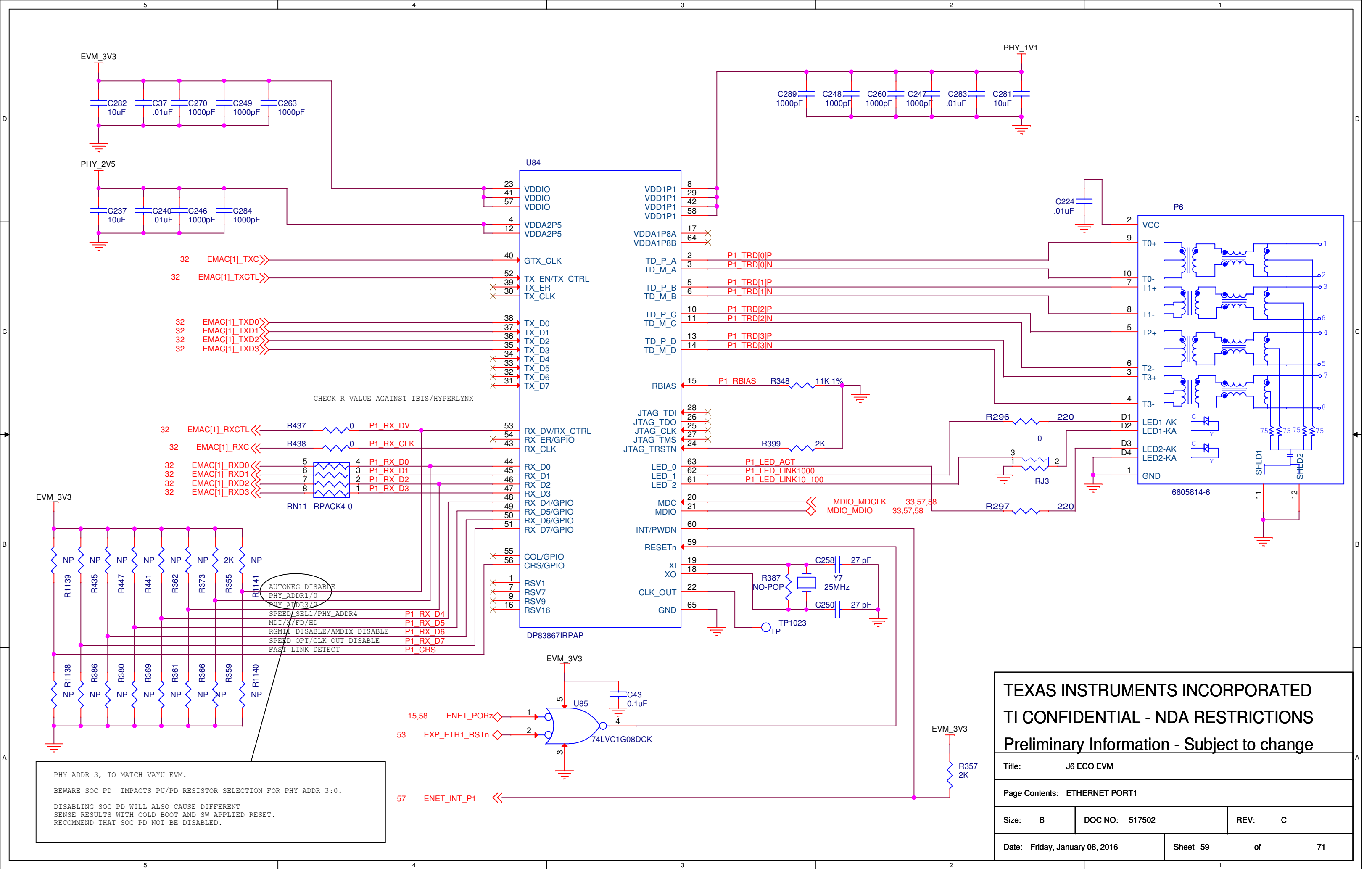


PHY ADDR 2, TO MATCH VAYU EVM.

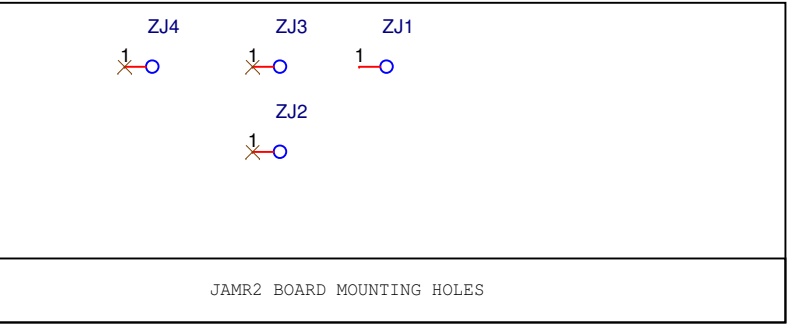
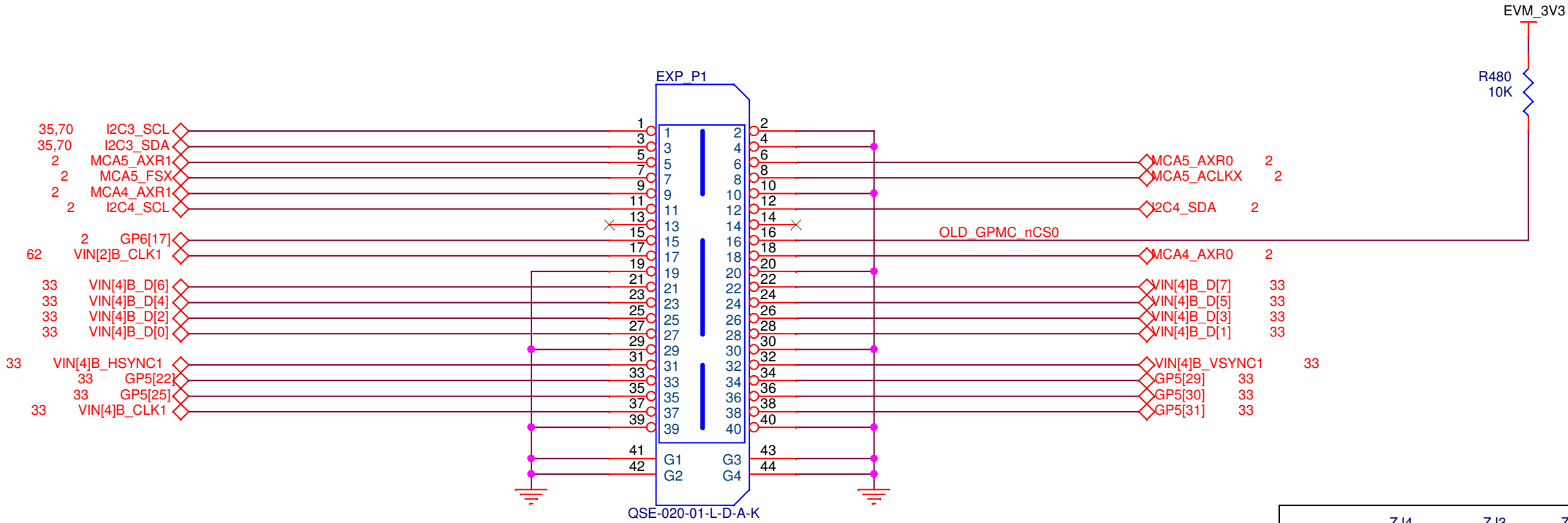
BEWARE SOC PD IMPACTS PU/PD RESISTOR SELECTION FOR PHY ADDR 3:0.

DISABLING SOC PD WILL ALSO CAUSE DIFFERENT SENSE RESULTS WITH COLD BOOT AND SW APPLIED RESET. RECOMMEND THAT SOC PD NOT BE DISABLED.

TEXAS INSTRUMENTS INCORPORATED			
TI CONFIDENTIAL - NDA RESTRICTIONS			
Preliminary Information - Subject to change			
Title:		J6 ECO EVM	
Page Contents: ETHERNET PORT0			
Size:	B	DOC NO: 517502	REV: C
Date:	Friday, January 08, 2016	Sheet 58	of 71

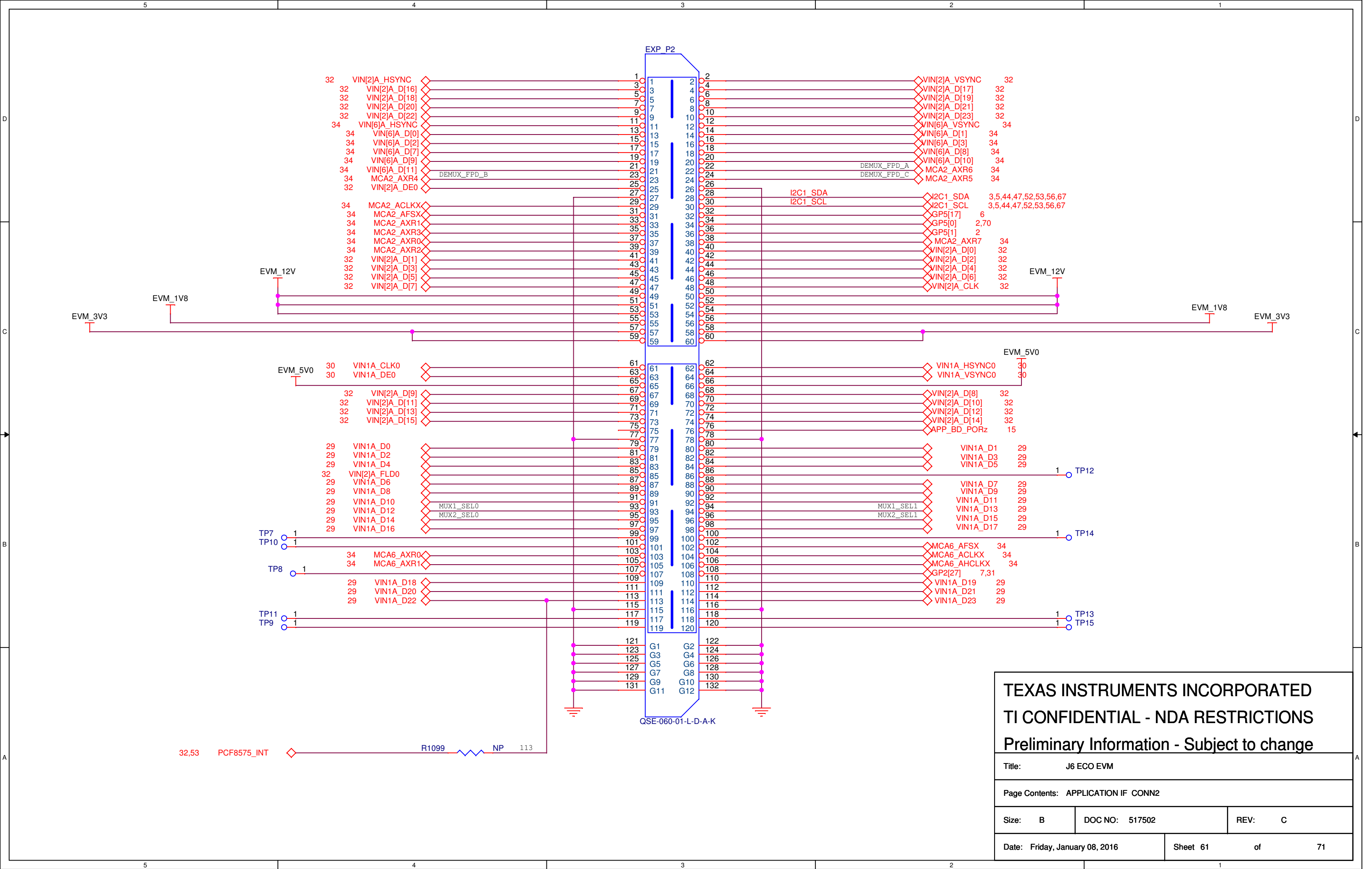


MMC3_D7/VIN2B_CLK1/AB5/P1.P17



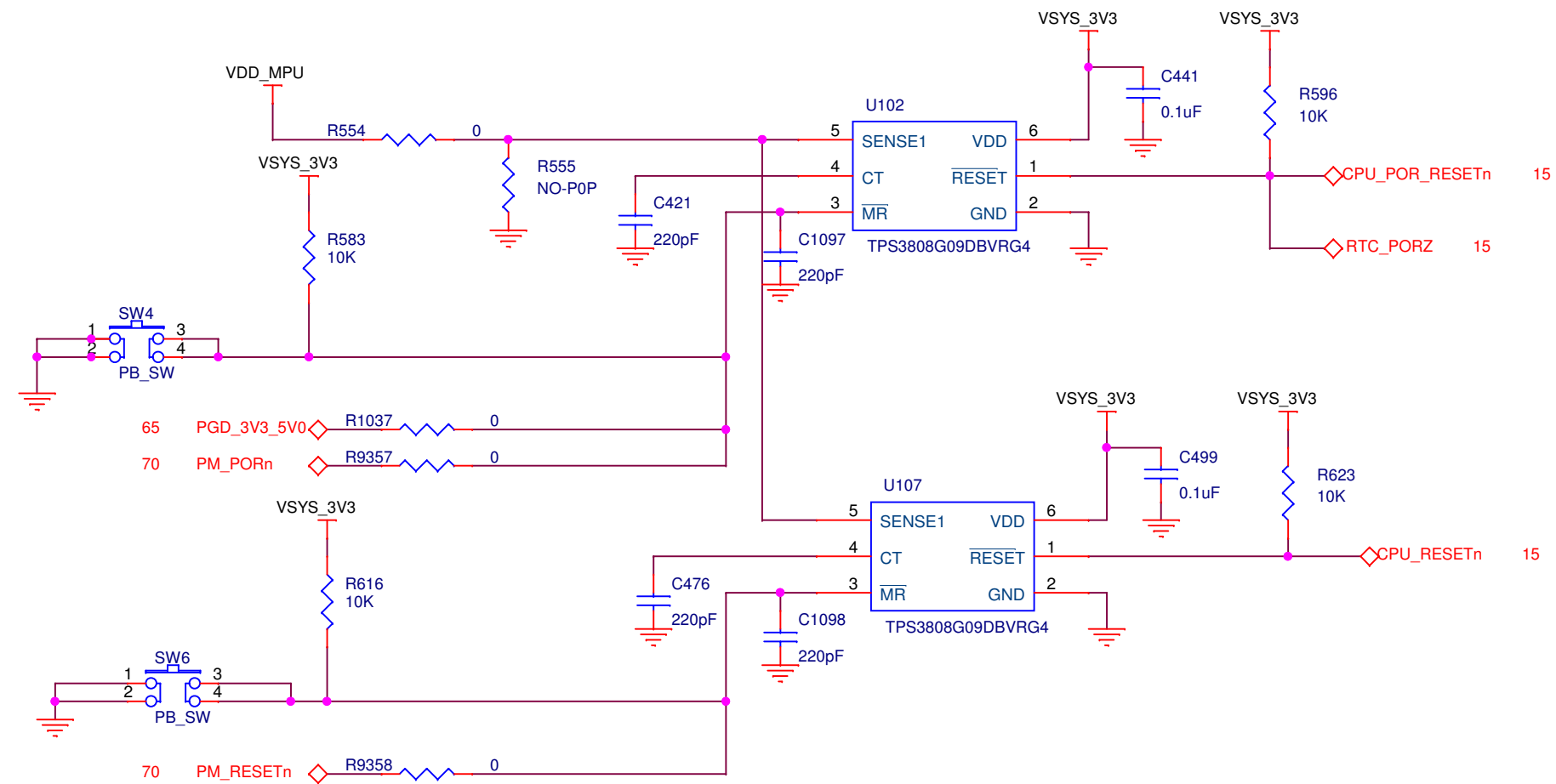
TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

Title: J6 ECO EVM		
Page Contents: APPLICATION IF CONN1		
Size: B	DOC NO: 517502	REV: C
Date: Friday, January 08, 2016	Sheet 60	of 71



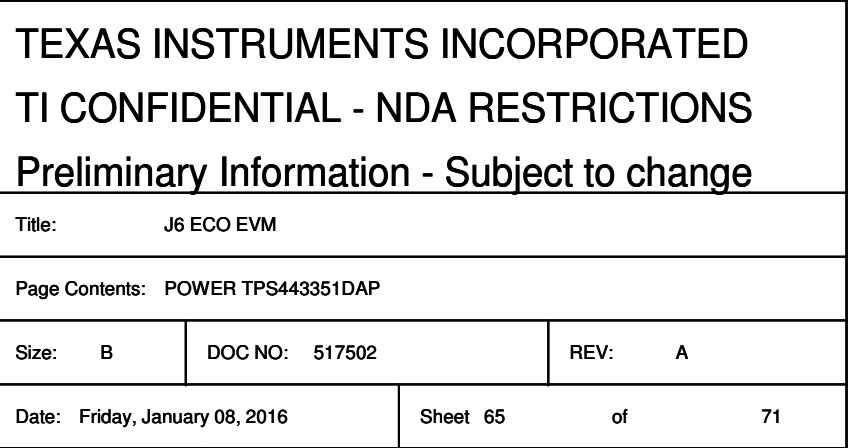
TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

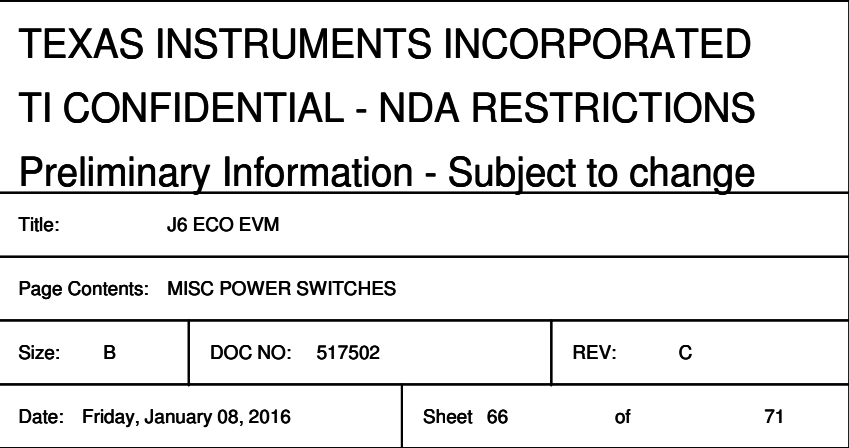
Title: J6 ECO EVM			
Page Contents: APPLICATION IF CONN2			
Size: B	DOC NO: 517502	REV: C	
Date: Friday, January 08, 2016	Sheet 61	of	71

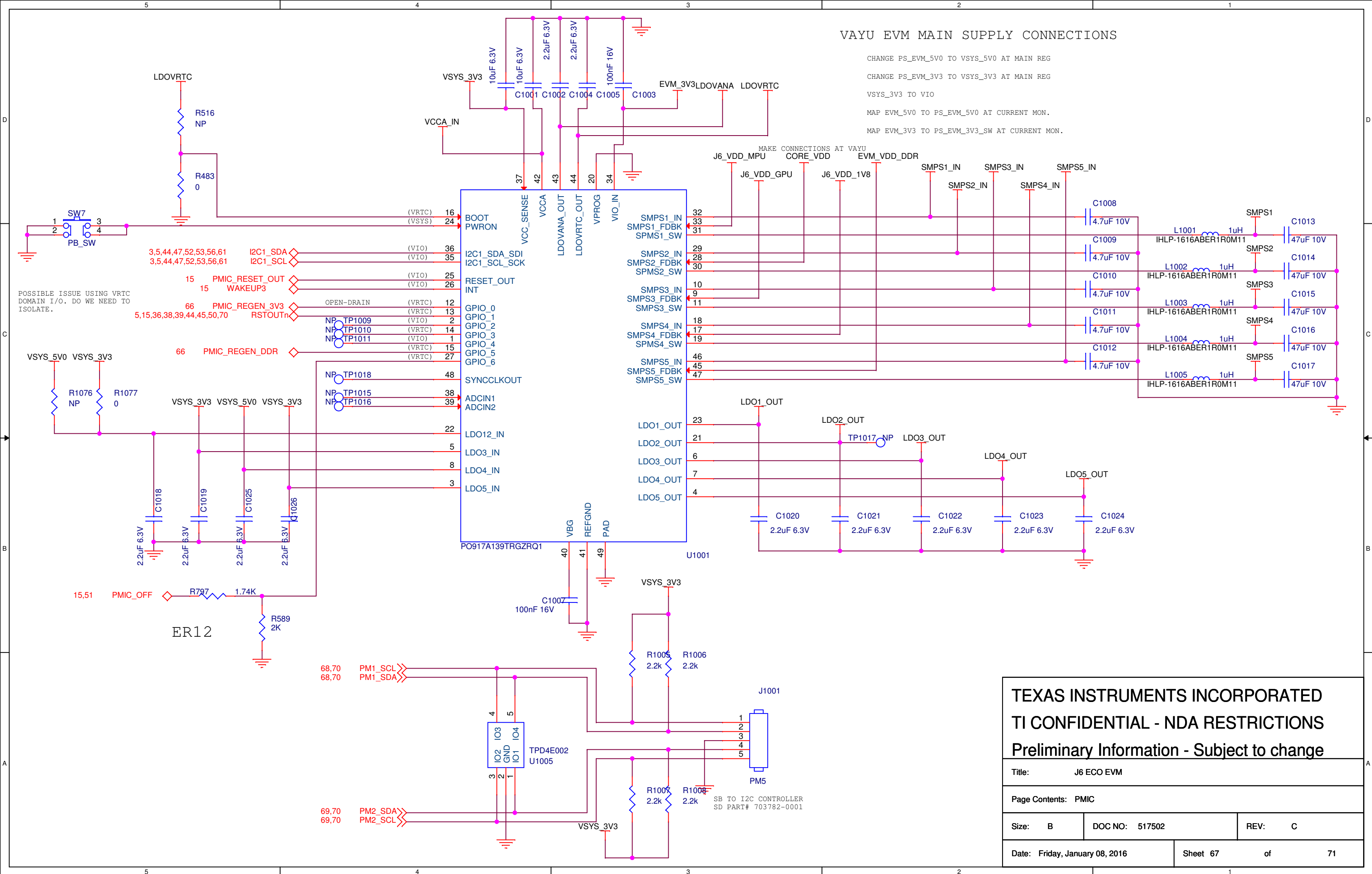


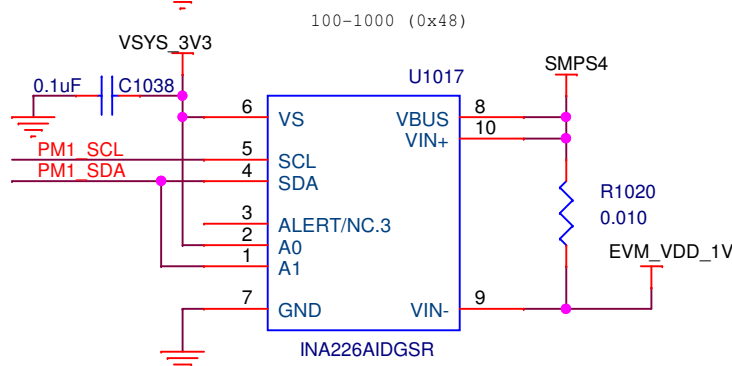
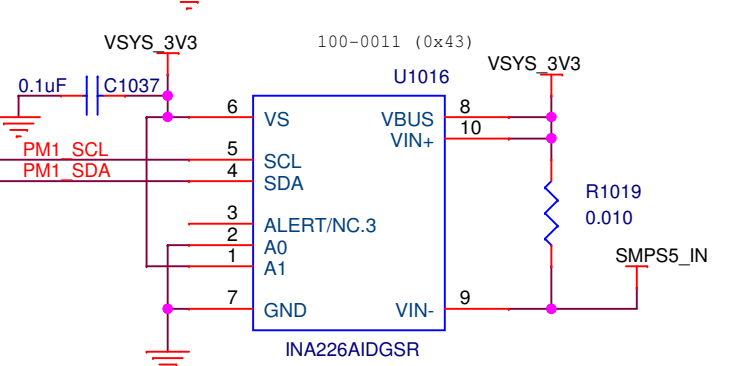
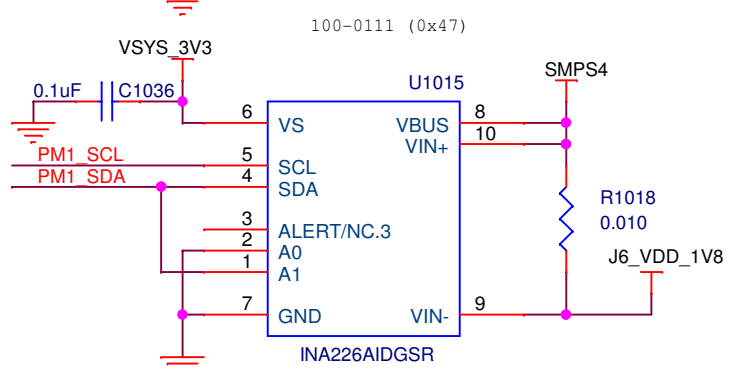
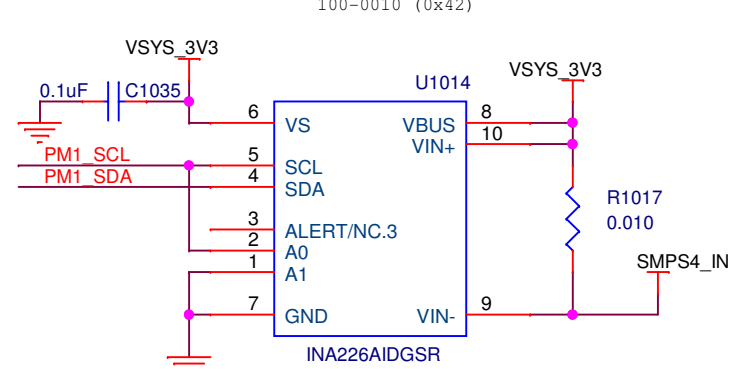
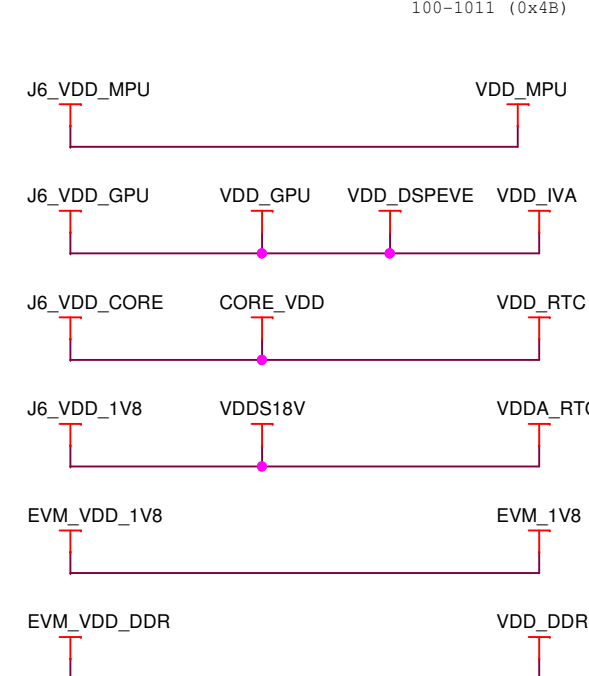
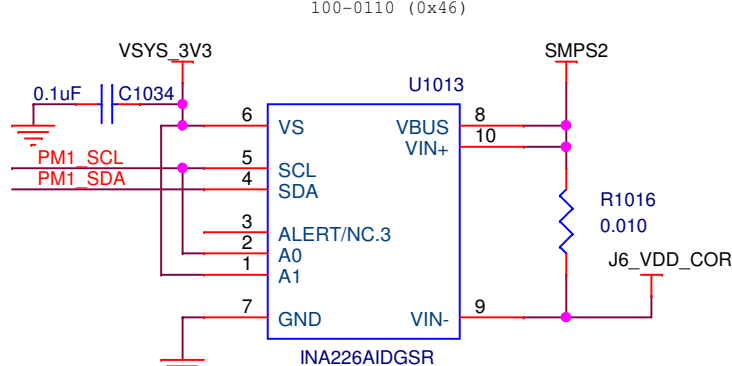
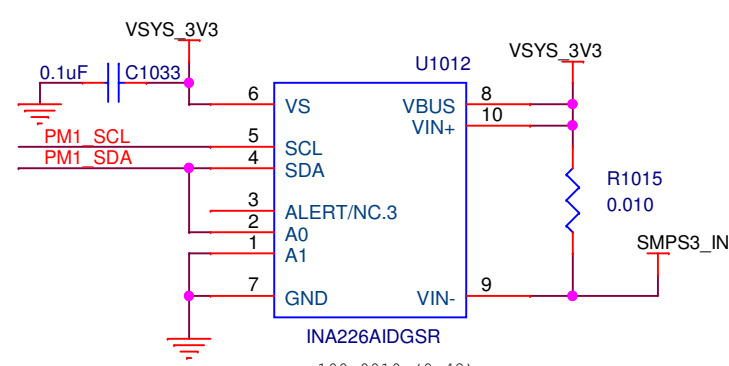
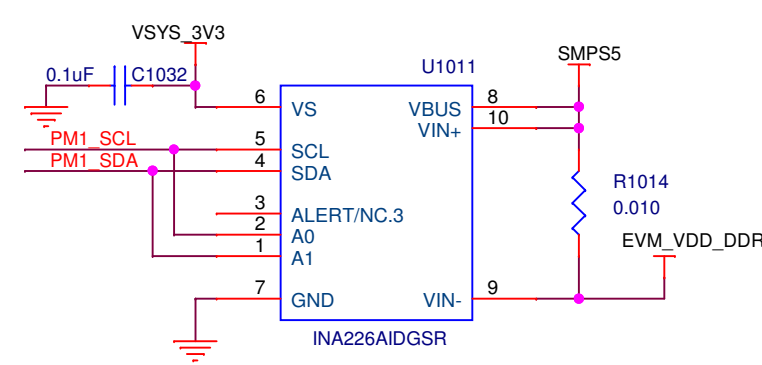
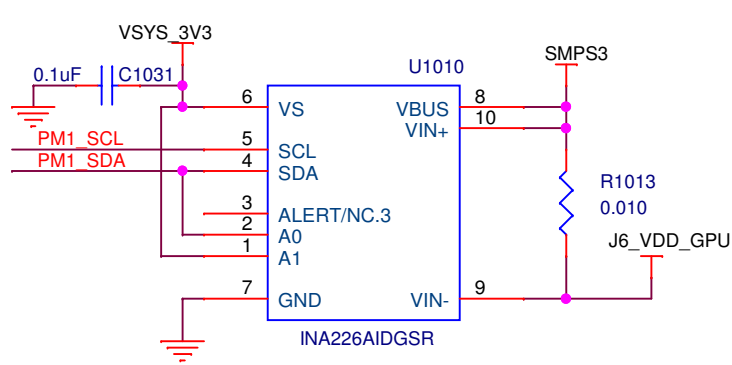
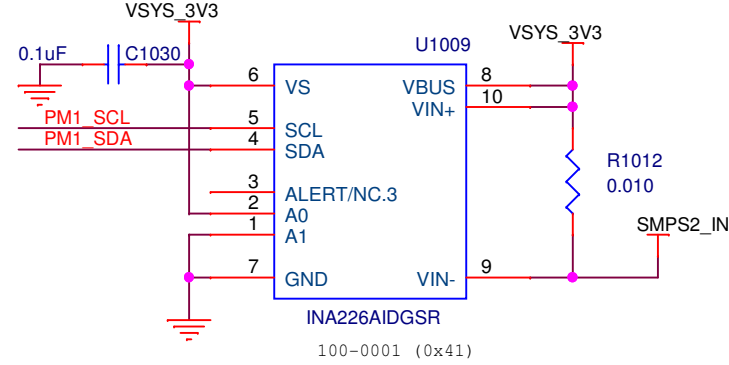
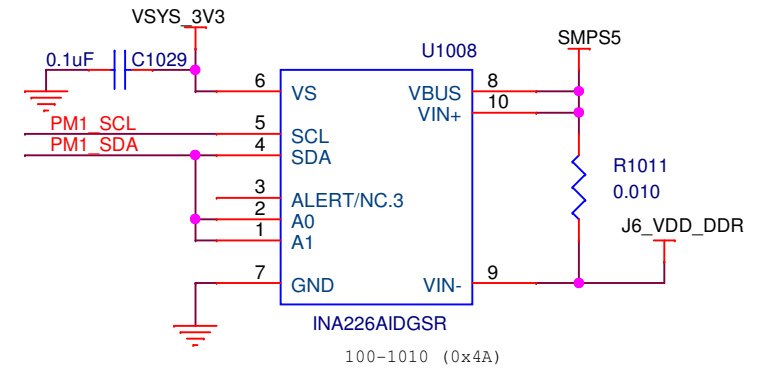
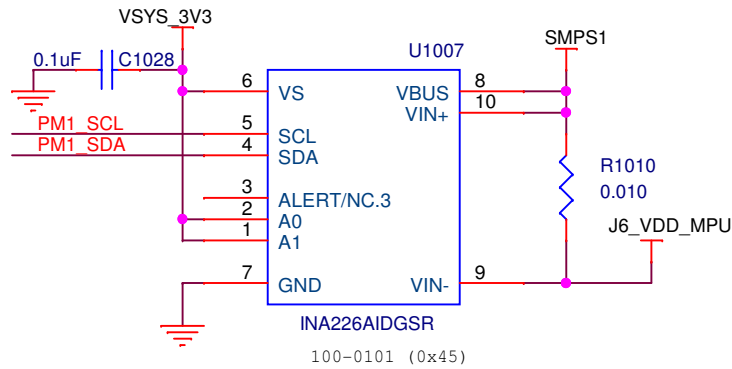
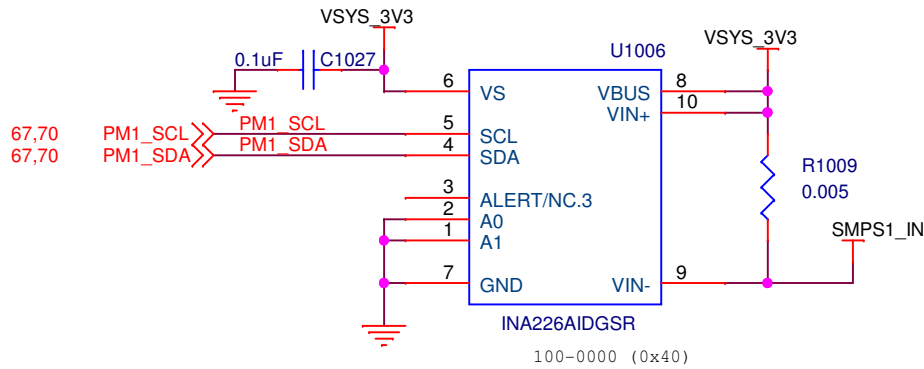
TEXAS INSTRUMENTS INCORPORATED
TI CONFIDENTIAL - NDA RESTRICTIONS
Preliminary Information - Subject to change

Title: J6 ECO EVM		
Page Contents: RESET		
Size: B	DOC NO: 517502	REV: A
Date: Friday, January 08, 2016	Sheet 63	of 71

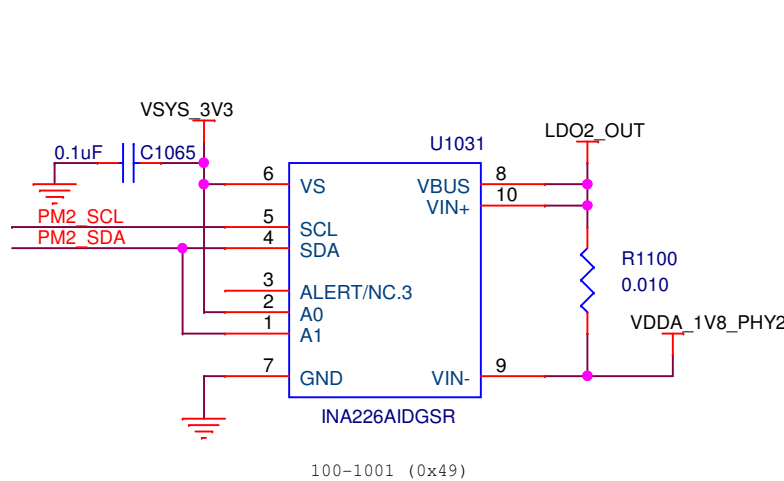
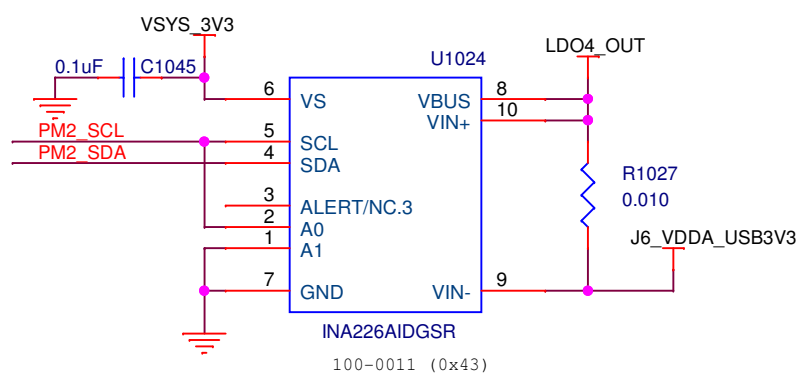
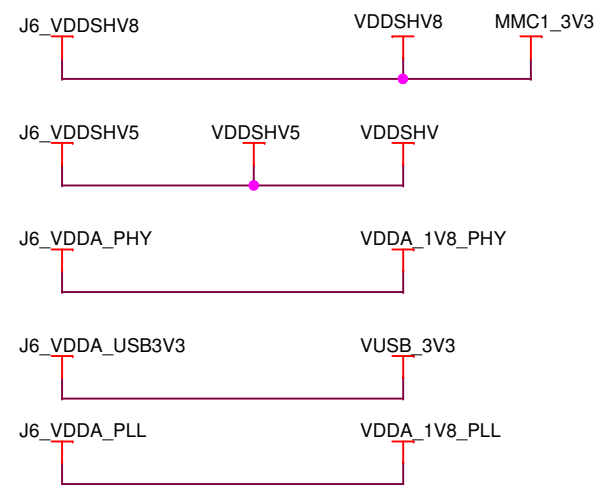
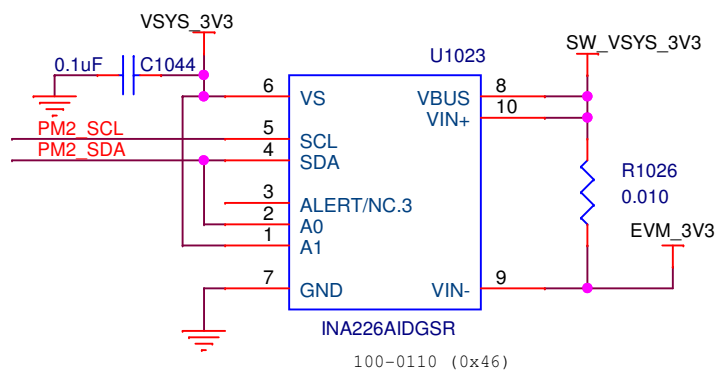
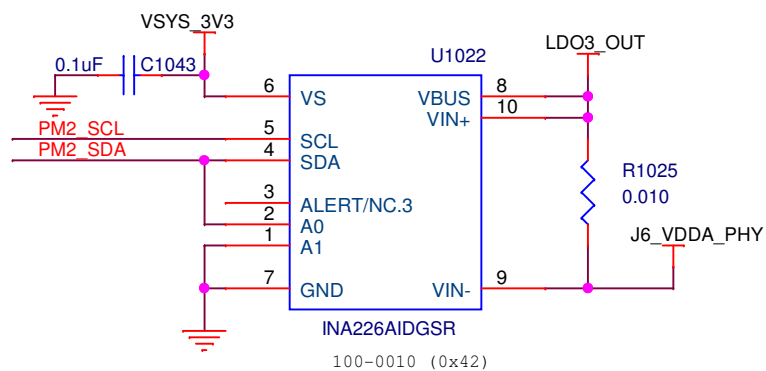
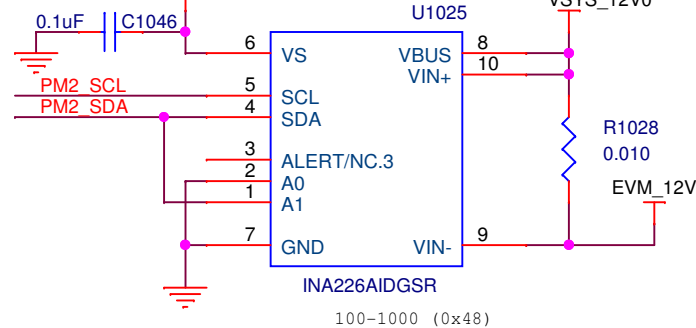
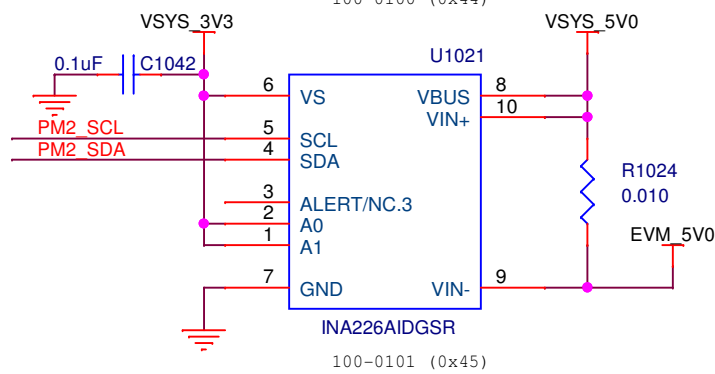
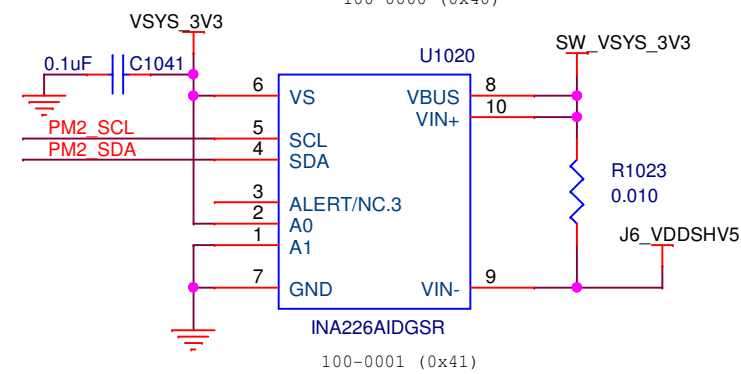
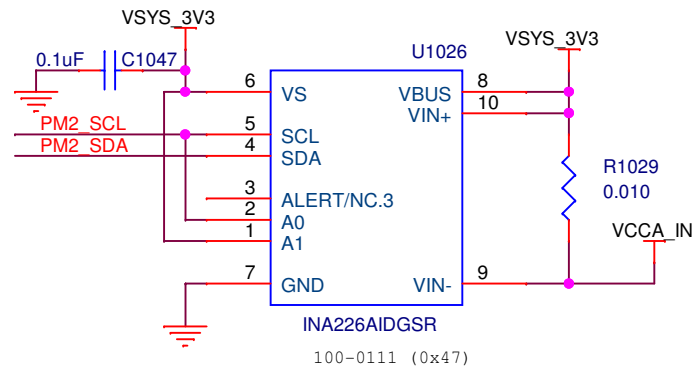
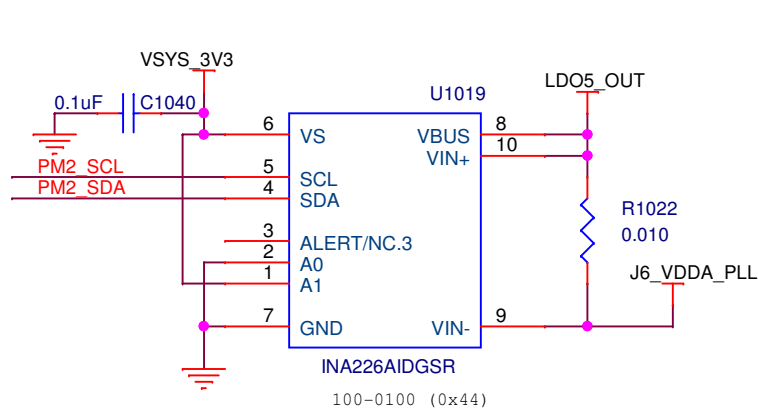
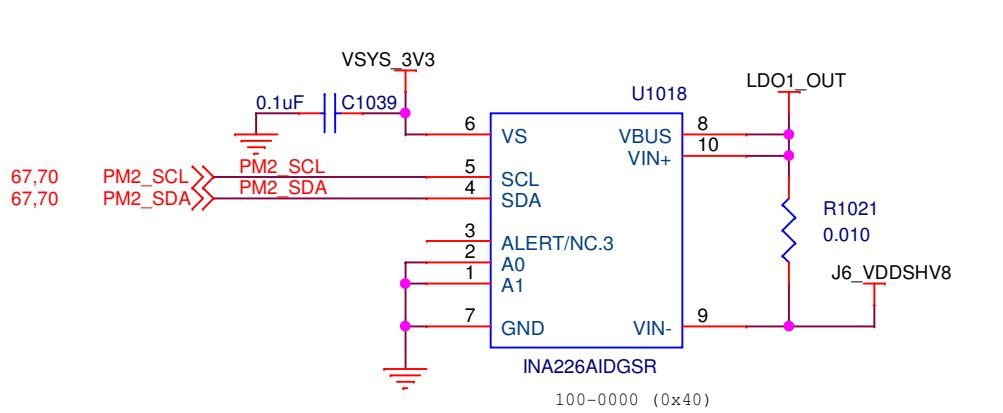








TEXAS INSTRUMENTS INCORPORATED		
TI CONFIDENTIAL - NDA RESTRICTIONS		
Preliminary Information - Subject to change		
Title: J6 ECO EVM		
Page Contents: POWER MONITOR 1		
Size: B	DOC NO: 517502	REV: C
Date: Friday, January 08, 2016	Sheet 68	of 71



ER5

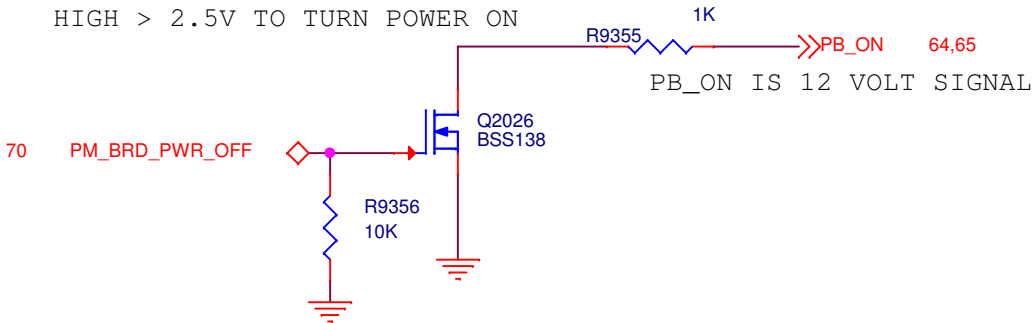
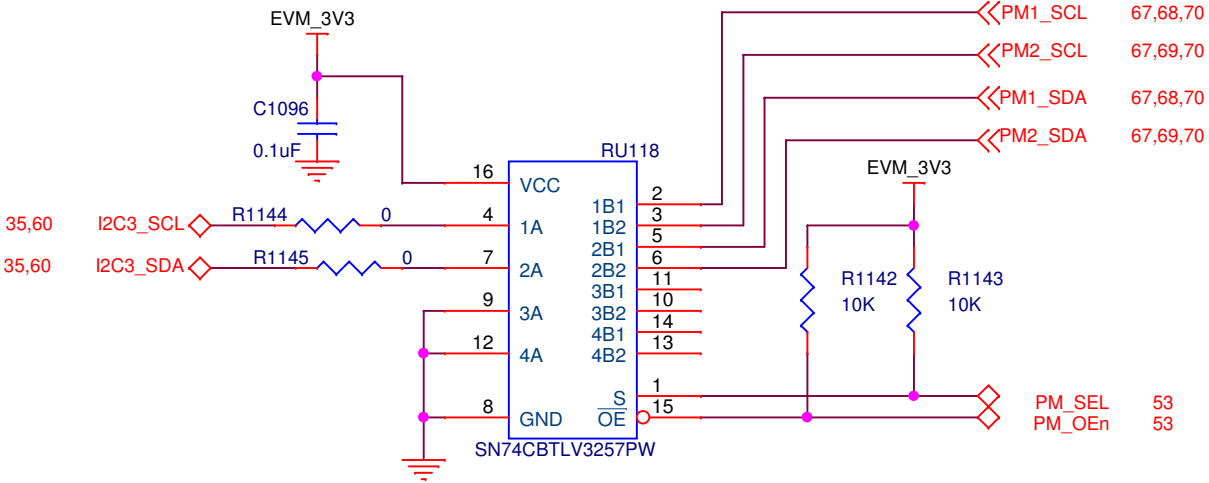
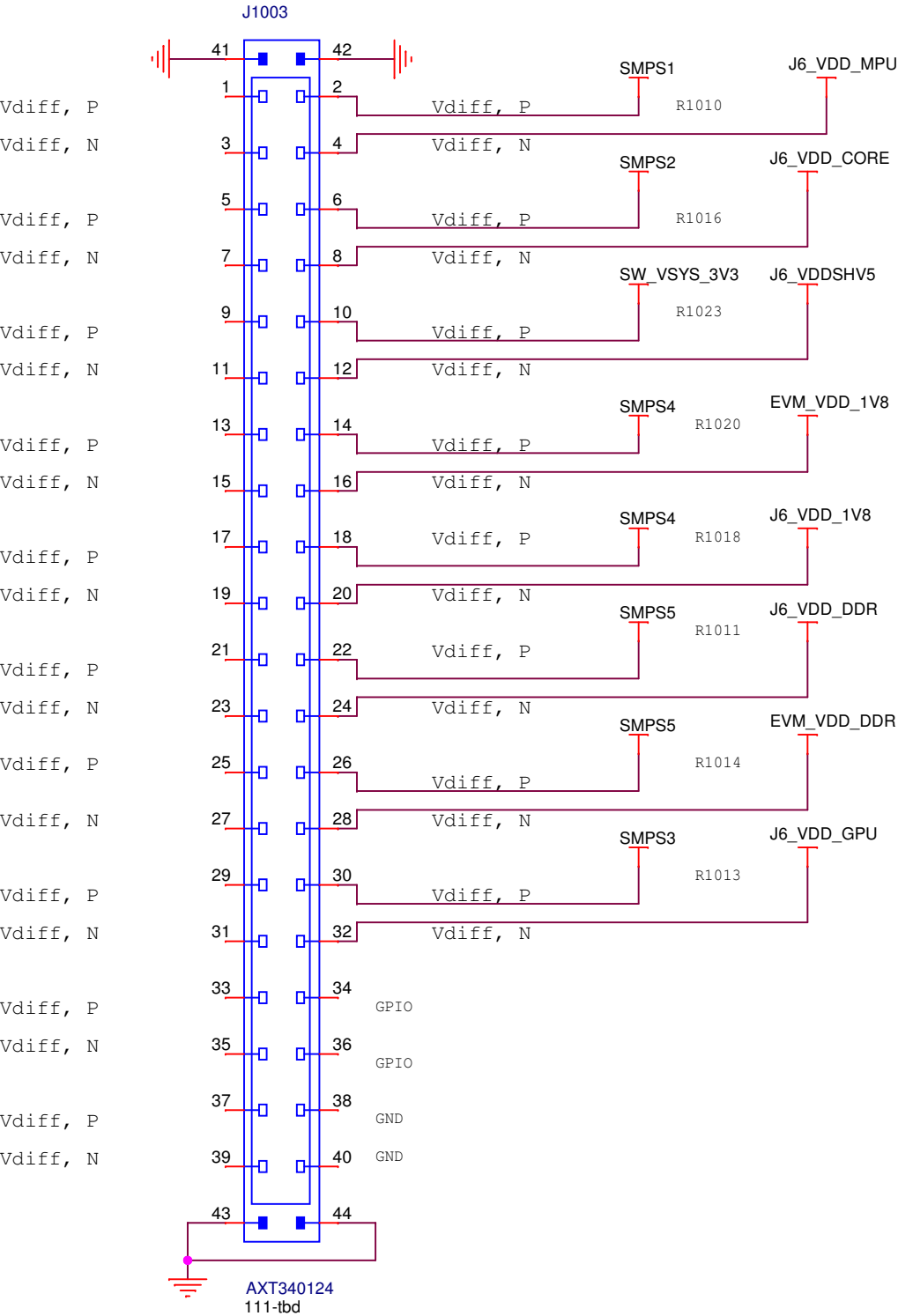
TEXAS INSTRUMENTS INCORPORATED TI CONFIDENTIAL - NDA RESTRICTIONS Preliminary Information - Subject to change		
Title: J6 ECO EVM		
Page Contents: POWER MONITOR 2		
Size: B	DOC NO: 517502	REV: C
Date: Friday, January 08, 2016	Sheet 69	of 71

ER21

DARA Bd Interface Connector

DARA Bd can be interfaced to conduct more in-depth power measurements.

All Vdiff signal pairs (P & N) shown should be routed as pseudo-Diff Pair segments using min trace widths since these are low voltage sensing nets, not power delivery nets.



HIGH > 2.5V TO TURN POWER ON

PB_ON IS 12 VOLT SIGNAL

70 PM_BRD_PWR_OFF

Q2026 BSS138

R9356 10K

R9355 1K

PB_ON

64,65

BOOT SWITCHES SHOULD BE SET TO OFF FOR PULLDOWNS. AUTOMATION WILL DRIVE HIGH TO OVERRIDE.

29,37,38,54 GPMC_D00 SYSBOOT0 1K R9359

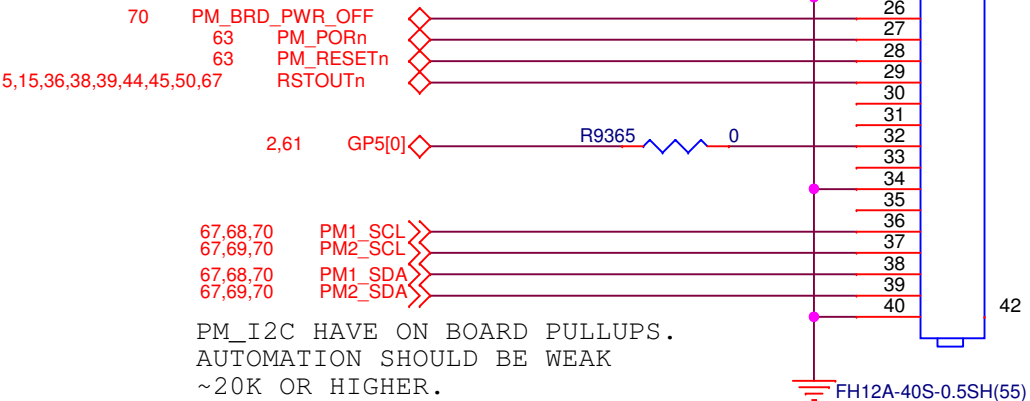
29,37,38,54 GPMC_D01 SYSBOOT1 1K R9360

29,37,38,54 GPMC_D02 SYSBOOT2 1K R9361

29,37,38,54 GPMC_D03 SYSBOOT3 1K R9362

29,37,38,54 GPMC_D04 SYSBOOT4 1K R9363

29,37,38,54 GPMC_D05 SYSBOOT5 1K R9364



PM_I2C HAVE ON BOARD PULLUPS. AUTOMATION SHOULD BE WEAK ~20K OR HIGHER.

ER22

AUTOMATION INTERFACE

ALL SIGNALS SHOULD BE REFERENCED TO EVM_3V3

Cable : Parlex-050R40-76B, .5mm 3"

TEXAS INSTRUMENTS INCORPORATED		
TI CONFIDENTIAL - NDA RESTRICTIONS		
Preliminary Information - Subject to change		
Title: J6 ECO EVM		
Page Contents: POWER MONITOR 1		
Size: B	DOC NO: 517502	REV: C
Date: Friday, January 08, 2016	Sheet 70	of 71

ECN Number	517500-E002			
Date Requested	11/10/2015			
Requester	Tony C.			
Approvals	Tony C.			
Assembly Number	516580-0001			
Current Assembly Rev	B1			
New Assembly Rev	C			
Date Implemented	01/06/2016			
Implentation notes				To many bom changes to list all individually. Diff Rev B1 and C boms for complete details.
Item#	Init Date	Priority	Status	Description
				ASSY FROM B1 TO C
				ASSY FROM B1 TO C
				ASSY FROM B1 TO C
ER1				Upgrade J6 ECO Processor to ES 2.0 U52 From 104492-0001R-TI to 104492-0002R-TI
				Create new J6 ECO part # 104492-0002R-TI IC,BGA,J6 ECO_PROCESSOR GP,ES2.0 XDRA726BXGABC
ER3				Sheet 24, 25 Update memory to 2GB total U115, U116, U68, U69, U67 From 104177-0125R To 104695-0125R MT41K512M8RH-125-AAT-E (IT)
ER4				Change Boot voltages for MPU/CORE to 1.15V
ER5				Sheet 10, 13, 69 VDDA_PHY rail measurements exceeds LDO ratings Create new power net with current monitor. LDO2_OUT - VDDA_1V8_PHY2 i2c Address 0x49 ADD C1065, 0.1uF, 101587-1100R ADD R1100, 0.010, 102598-0001R ADD U1031, INA226AIDGSR ,103928-0001R
ER6				Sheet 39 Update MMC2_DAT[7:0]/CMD Pullups with 47K R404, R405, R406,, R414, R422, R423, R426, R428 From R0402-NOPOP to 101370-4703R R410, R417 From 102145-4993R to 101370-4703R
ER7				Sheet 40 Update SD Data[3:0]/CMD to 47K R19, R18, R16, R15, R14 From 101370-1003R to 101370-4703R
ER8				Sheet 14 Add pull-up to TMS (silicon does not include PU or PD, so floating) Add R629, 4.7K 101370-4702R Change R758 From 0 to 100 From 101370-0001R To 101370-1001R This limits current into VREF with LauterBauch adapter plugged in backwards.
ER9				Sheet 66 Investigate Power sequence of ENET MAC (core before IO) Change the 2.5V PHY power sequence to track EVM_3V3 as recommended in not 7.17 of the datasheet. Add C705 101007-0220R, 2.2uF Add RJ1,101370-4702R, 4.7K Add R832, 100642-1073R, 10.7K Change C19 from 102058-1102R to c0402-nopop NOTE: ER9, ER14 AND ER23 ARE UPDATED TO SWITCH TO NEW PHY.
ER10				Sheet 27 Move DDR termination (net DDR2_CKE) to discrete resistor (so can be populated for Suspend to RAM) Add R1101, 36, 102145-3600R

ER11				Fix CSI2_0 routing (Reference Layer 9 has split plane)
ER12				Sheet 67 Voltage divider for PMIC GPIO_6/NSLEEP (1.74k series, 2k pull-down) Add R589, 2K, 103158-2002R Add R797, 1.74K, 102145-1742R
ER13				Sheet 64 Update reverse battery protection to TI smart diode solution using LM74610 U8 From 103038-0001R To 104635-0001R Q5 From 103998-0001R To 104283-0001R ADD C706 101415-0220R ADD C707 103885-1220R ADD D1 101808-0030R ADD D2 101808-0014R ADD TP831 TP-18RD8-NO-POP
ER14				Sheet 57, 58 .Ethernet PHY core 1.8V leakage to 3.3V rail. Use load switch and duplicate power up circuitry used on 3.3V supply so it can be adjusted independently. ADD C708 C0603-NOPOP C709 100993-1223R R833 100642-1073R RJ2 101370-4702R U145 104267-0001R NOTE: ER9, ER14 AND ER23 ARE UPDATED TO SWITCH TO NEW PHY.
ER15				Sheet 68,69 Current measurement R changes to 10mOhm R1024 Fm: 104445-0001R To: 102598-0001R R1026 Fm: 104445-0001R To: 102598-0001R R1028 Fm: 104297-0002R To: 102598-0001R R1023 Fm: 104444-0005R To: 102598-0001R R1010 Fm: 104445-0001R To: 102598-0001R R1013 Fm: 104445-0001R To: 102598-0001R R1016 Fm: 104297-0002R To: 102598-0001R R1012 Fm: 104444-0005R To: 102598-0001R R1014 Fm: 104444-0005R To: 102598-0001R R1017 Fm: 104444-0005R To: 102598-0001R R1018 Fm: 104444-0005R To: 102598-0001R R1011 Fm: 104444-0005R To: 102598-0001R R1015 Fm: 104444-0005R To: 102598-0001R R1019 Fm: 104444-0005R To: 102598-0001R
ER16				PB free marking in silk screen
ER17				FCC marking in silk screen."For evaluation only, not FCC approved for res
ER18				Sheet 66 REG_EN pullups voltage to SMPS4
ER19				See TI input
ER20				Sheet 65 C51 change to 25V cap from 16 V on 12 V input FROM 101290-0102R TO 104700-0102R
ER21				Sheet 70 Add DARA power measurement J1003 - 104610-0040R
ER22				See TI input
ER23				Sheet 57, 58, 59 Migrate to DP8367IR PHY
ER24				NA - UPDATE 7XXXX TOP LEVELS
ER25				Sheet 48 Remove unneeded pullup/pulldown on HDMI signals. These are included in the TPD12S016RKTR device. R6, R7 From 101370-2202R to r0402-nopop R305 From 103158-1003R to r0201-nopop
Update				Update part body of SN74AVC8T245RHL devices. Layout only change to improve mfg.