



Power Enable and WakeUp Signal From MIC, Ethernet Controller, Expansion Connector

External ADC Reference Voltage

I2C0 Interface
 82C0 Both, 2D Memory
 Expander Conn and I/V
 Connect by User

USB0 Interface
 USB0_DM
 USB0_DP
 USB0_CE
 USB0_ID
 USB0_VBUS

USB1 Interface
 USB1_DM
 USB1_DP
 USB1_CE
 USB1_ID
 USB1_VBUS

VDDSHV6
 R26 4.7K
 R27 4.7K
 R28 4.7K
 R29 4.7K
 R30 4.7K
 R31 4.7K

C6	PMIC_POWER_EN	MI1_TX_CLK	K18
C5	EXT_WAKEUP	MI1_TXD0	K15
B6	AIN0	MI1_TXD1	K15
C7	AIN1	MI1_TXD2	J18
B7	AIN2	MI1_TXD3	J16
A7	AIN3	MI1_TX_EN	H17
C8	AIN4	MI1_CRS	H16
B8	AIN5	MI1_RX_CLK	L18
A8	AIN6	MI1_RXD0	L15
C9	AIN7	MI1_RXD1	L15
B9	VREFP	MI1_RXD2	L17
A9	VREFN	MI1_RXD3	J15
A17	SPI0_SCLK	MI1_RX_ER	J17
B17	SPI0_D0	MI1_RX_DV	H18
A18	SPI0_D1	MI1_REF_CLK	M18
C15	SPI0_CS0	MI1_MDIO	M17
A18	SPI0_CS1	MI1_TXD4	R1
E16	UART0_TXD	MI1_TXD5	R2
E15	UART0_RXD	MI1_TXD6	R3
E18	UART0_CTS_N	MI1_TXD7	R4
E17	UART0_RTS_N	MI1_TXD8	R5
D15	UART1_TXD	MI1_TXD9	R6
D18	UART1_RXD	MI1_TXD10	R7
D18	UART1_CTS_N	MI1_TXD11	R8
D17	UART1_RTS_N	MI1_TXD12	R9
C16	I2C0_SCL	MI1_TXD13	R10
C17	I2C0_SDA	MI1_TXD14	R11
N17	USB0_DP	MI1_TXD15	R12
N18	USB0_DM	MI1_TXD16	R13
M15	USB0_CE	MI1_TXD17	R14
P16	USB0_ID	MI1_TXD18	R15
F16	USB0_VBUS	MI1_TXD19	R16
P15	USB0_VBUS	MI1_TXD20	R17
R17	MCASP0_AHCLKX	MI1_TXD21	R18
R18	MCASP0_ACLKX	MI1_TXD22	R19
P18	MCASP0_FSX	MI1_TXD23	R20
F17	MCASP0_AVR0	MI1_TXD24	R21
F16	MCASP0_ID	MI1_TXD25	R22
T18	MCASP0_AHCLKR	MI1_TXD26	R23
F18	MCASP0_ACLKR	MI1_TXD27	R24
F18	MCASP0_FSR	MI1_TXD28	R25
D13	MCASP0_AXR1	MI1_TXD29	R26
C18	ECAP0_IN_PWM0_OUT	MI1_TXD30	R27

Ethernet Signals

BOOT Selection
 LCD Display Data Lines

AM335X_LCD_PCLK
 AM335X_LCD_VSYNC
 AM335X_LCD_HSYNC
 AM335X_LCD_AC_BIAS_EN

Expansion, COM Conn, HDMI
 GPIO3_14 to GPIO3_21
 used as a GPIO for Peripheral

