3.1.4 ADC32RFxx GUI
1. Start the ADC32RFxx GUI (Start → All Programs → Texas Instruments → ADC32RFxx EVM revD GUI)
2. Make sure that the USB status is green.
3. If the GUI does not connect to the board, click on the "Reconnect FTDI button until the GUI connects to the board. This connection is indicated by a green USB Status.

   NOTE: You may need to close the DAC34J8x GUI for the ADC32RFxx GUI to work.

Figure 14 ADC32RFxx EVM GUI Start page

4. Click on the "Low Level View" tab.
5. Press "Load Config" button and navigate to the "DDC Mode" folder
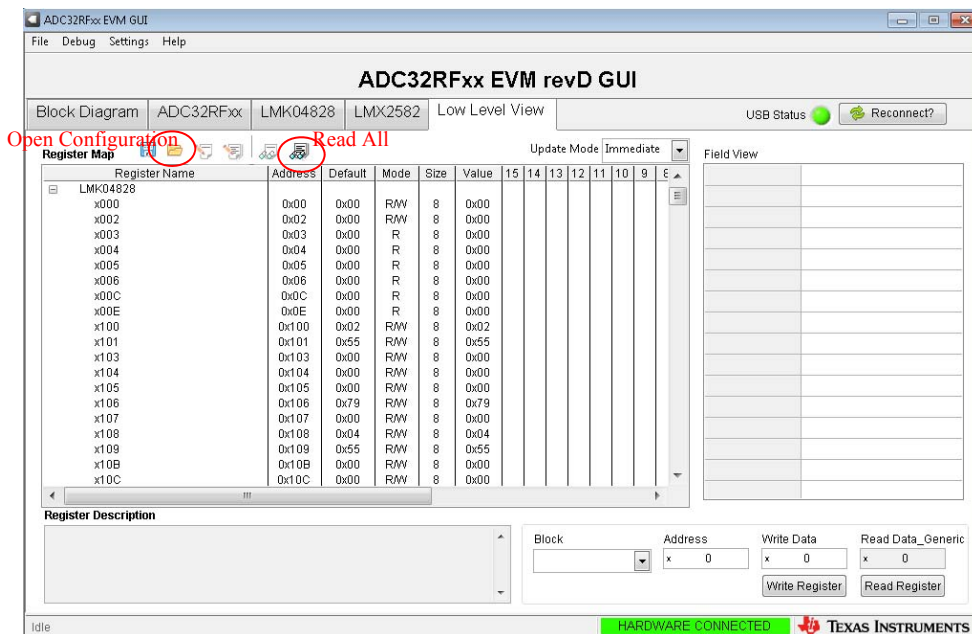6. Select "LMK_ADC32RF4x_ExtClock.cfg" and click "OK"

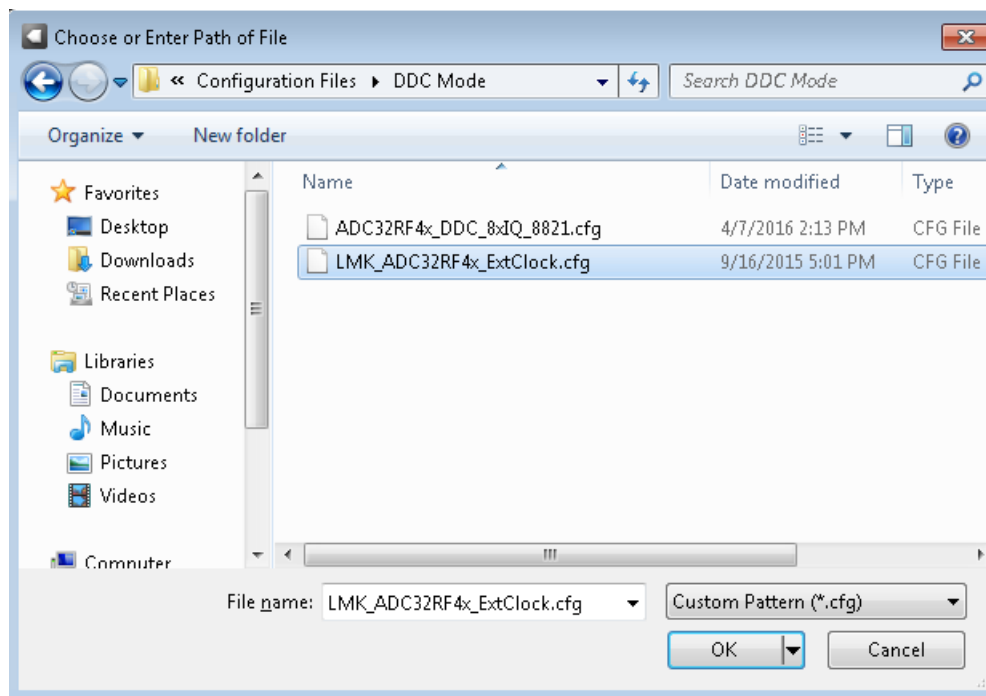**Figure 15 ADC32RFxx EVM GUI – Low Level View**

Figure 16 ADC32RFxx Programming file

7. Press SW1 (ADC RESET) to provide a hardware reset to the ADC.
8. Go to the Low Level View tab and click "Load Config".
9. Navigate to ADC32RF4xDDC_8xIQ_8821.cfg and click OK.
10. Press Read All on the low level tab.
11. Go to the ADC32RFxx tab, check if CHA and CHB DDC EN are selected. If not, go back to low level tab and press "Read All" again
12. Enter 2949.12 in the box for Sampling Clock rate in MHz.

13. For midband demo, enter 139.32 in the box for Ch B DDC0 NCO1 frequency and enter 229.32 in the box for CHB DDC1 NCO frequency, see Figure 17. For wideband demo, enter 92.16 in the box for Ch B DD0 NCO1 frequency, see Figure 18.
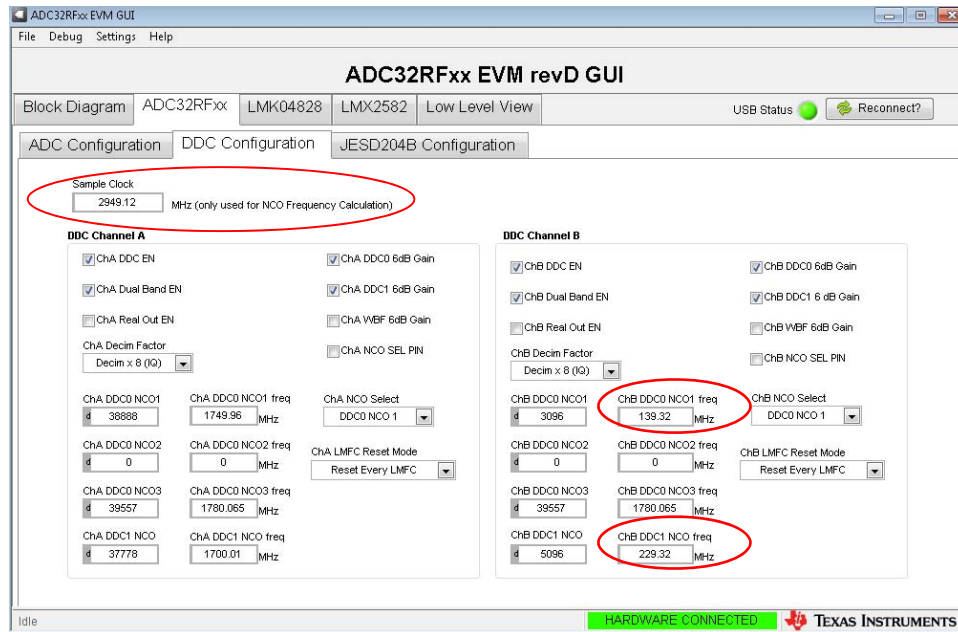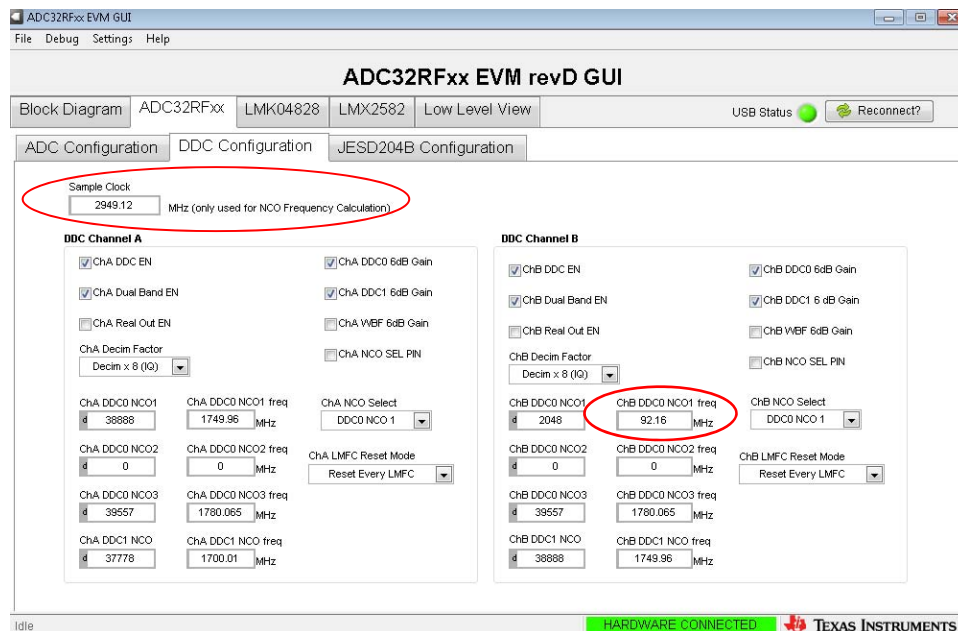

Figure 17 ADC32RFxx EVM GUI ADC tab – midband demo

Figure 18 ADC32RFxx EVM GUI ADC tab – wideband demo

NOTE: Channel B of ADC32RFxx EVM is used. It's connected to a dual-band DDC. Midband demo: we use DDC0 and DDC1 NCO to select the two carriers. Wideband demo, we use DDC0 NCO1 alone to select one wideband carrier.