

Position	OFF	OFF	OFF	OFF	OFF	OFF	ON	ON
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For Spectrum Digital EVM:

Pin#	1	2	3	4	5	6	7	8
Position	OFF	OFF	OFF	OFF	OFF	ON	OFF	ON

1. Connect a serial cable from the host computer to the OMAP-L138 EVM
2. Open a command prompt on the host computer and change directory to where the SFH executable is located.
3. Run the flashing utility with this command:

- `..\OMAP-L138\gnu\sfh_OMAP-L138.exe [options]`

4. Turn on the board

Under Linux

The serial flasher can be run on a Linux machine with the latest open-source Mono Framework installed. The steps are identical to the Windows environment except the command:

- `mono ./sfh_OMAP-L138.exe [options]`

Serial Flasher Options

There are three modes for using the serial flasher:

1. Erase the target flash type - This will erase the entire contents of the flash.

- `..\sfh_OMAP-L138.exe -erase`

2. Flash the memory with a single application image - This will place an application image at address 0x0 of the flash. This must be an AIS format binary, which can be created with the AISgen utilities bundled with the Bootloader User Guide and associated files.

- `..\sfh_OMAP-L138.exe -flash_noubl <binary application file>`

3. Flash the memory with a UBL and application image - This will place a UBL at address 0x0 of the flash and an application image, such as u-boot, at address 0x10000. In general, a UBL is not required, and this mode is only used when restoring the default flash contents, as explained in the following section.

- `..\sfh_OMAP-L138.exe -flash <UBL binary file> <binary application file>`

For C6748 devices, use the following option: "-targetType C6748"

For AM1808 devices, use the following option: "-targetType AM1808"

Note: Only the UBL found in the OMAP-L138/GNU/UBL directory can be used with the serial flasher. Other UBLs are not compatible with the serial flasher format.

Note: The 'flash_noubl' and 'flash' options will automatically erase the necessary amount of memory in order to fit the UBL and/or application image.

Currently, the only supported flash types are NAND, NOR, and SPI.

Additional options are shown below:

```

-targetType      : Specifies exact target type within OMAP-L138 family (default OMAP-L138)
-flashType       : Specifies exact flash type (default SPI_MEM)
-p <COM PORT NAME> : Allows specifying com port other than default 'COM1' or '/dev/ttyS0'.
-h               : Show help text.
-v               : See verbose output from target device
-baud <BAUD RATE> : Allows specifying baud rate other than default (115200)
-APPStartAddr   : Changes entry point of application (default 0xC1080000)
-APPLoadAddr    : Changes load address of application (default 0xC1080000)
-APPFlashBlock  : Changes the block to flash the image into (only for no_ubl mode)

```

Once any command is run, the "Waiting for BOOTME..." prompt shows. Power cycle the board or press the reset button to continue.

Restoring the OMAP-L138 EVM SPI Flash

If the contents of the OMAP-L138 SPI flash become corrupted, they can be restored through the following steps:

1. Set the boot pins to UART2 boot mode. This is done by setting switch S7 on the OMAP-L138 EVM according to the following table:

Pin#	1	2	3	4	5	6	7	8
Position	OFF	OFF	OFF	OFF	OFF	OFF	ON	ON

For Spectrum Digital EVM:

Pin#	1	2	3	4	5	6	7	8
Position	OFF	OFF	OFF	OFF	OFF	ON	OFF	ON

1. Obtain the UBL and u-boot binary files and place them in the same directory as the SPI flashing utility.

- A pre-built UBL bin file is located in the `OMAP-L138/GNU/ubl/` directory of this serial flashing project. The UBL bin file from the PSP installation will also work, located at `images/u-boot/`.
- A pre-built u-boot image is located in the `images/u-boot/` directory of the PSP installation.

2. Run the following command to flash the UBL and u-boot binary files:

- `..\sfh_OMAP-L138.exe -flash <UBL binary file> <u-boot binary file>`

- Note that if the serial port is not on COM1, the correct port must be specified using the "-p" option. To determine which COM port to use, open HyperTerminal, connect to one of the ports, and make sure the word "BOOTME" appears when the EVM is reset.

- Note: If you are using a device other than OMAP-L138, such as C6748, you must use the "-flashType" option and specify the device you are using.
3. Turn on the board
 4. After the UBL and u-boot files have been flashed, set the boot pins to SPI boot mode and reboot the board. This is done by setting switch S7 on the OMAP-L138 EVM according to the following table:

Pin#	1	2	3	4	5	6	7	8
Position	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF

For Spectrum Digital EVM:

Pin#	1	2	3	4	5	6	7	8
Position	OFF	OFF	OFF	OFF	OFF	ON	ON	OFF

1. Once u-boot has loaded, follow the instructions in the Getting Started Guide to copy the Linux kernel and filesystem to the SPI flash.

- **Booting from SPI Flash**
- **Note: This step requires that the setup procedures in the Getting Started guide have been followed:**

- [Install SDK Software](#)
- [Setup a TFTP Server](#)

Modifications for Custom Boards

The default settings used by the tools apply only to the EVMs. For custom boards, changes will most likely be required, and the tools must be rebuilt. The custom changes should be made to the files in the OMAP-L138/Common/src and OMAP-L138/Common/include directories. Common changes include:

- **DDR Configuration**
 - OMAP-L138/Common/src/device.c: Modify the parameters passed to the function DEVICE_ExternalMemInit to match the DDR timing requirements for the custom board
- **UART Settings**
 - OMAP-L138/Common/include/device_uart.h: Modify the #define DEVICE_UART_PERIPHNUM to set which UART is connected to the host PC.
 - Note that flow control is not used, so only the RX and TX lines need to be connected for boot and flashing purposes.
- **SPI Settings**
 - OMAP-L138/Common/include/device_spi.h: Modify the global macros to select the appropriate peripheral and chip select numbers.
 - OMAP-L138/Common/src/device_spi.c: Modify the flash organization in DEVICE_SPI_MEM_params.
 - The SPI flash on the EVM does not need to be unlocked in order to erase or write. Some SPI flashes may need to set the BL bits before writing. Check the device datasheet to see if this step needs to be added to the initialization.
- **NAND Settings**
 - OMAP-L138/Common/include/device_async_mem.h: Modify the #define DEVICE_ASYNC_MEM_NANDBOOT_BUSWIDTH to match the bus width of your NAND
- **PLL Settings (for non 24 MHz input clocks)**
 - OMAP-L138/Common/src/device.c: Modify the parameters passed into the function DEVICE_PLL0Init and DEVICE_PLL1Init to set the proper PLL output frequencies
 - OMAP-L138/Common/src/device_uart.c: Modify the 'divider' field in DEVICE_UART_config to achieve 115200 baud rate

Known Issues

The software provided has no known issues on TI supported evaluation platforms. The software may need modifications in case of custom hardware.

License

These tools are provided as freeware, with no guarantee.

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